Am2922

Eight Input Multiplexer with Control Register

DISTINCTIVE CHARACTERISTICS

- High speed eight-input multiplexer
- On-chip Multiplexer Select and Polarity Control Register
- Output polarity control for inverting or non-inverting output

- Three-state output for expansion
- Common register enable, asynchronous register clear
- AC parameters specified over operating temperature and power supply ranges

GENERAL DESCRIPTION

The Am2922 is an eight-input Multiplexer with Control Register. The device features high speed from clock to output and is intended for use in high speed computer control units or structured state machine designs.

The Am2922 contains an internal register which holds the A, B and C multiplexer select lines as well as the POL (Polarity) control bit. When the Register Enable input ($\overline{RE}$) is LOW, new data is entered into the register on the LOW-to-HIGH transition of the clock. When $\overline{RE}$ is HIGH, the register retains its current data. An asynchronous clear input ($\overline{CLR}$) is used to reset the register to a logic LOW level.

The A, B and C register outputs select one of eight multiplexer data inputs. A HIGH on the Polarity Control flip-flop output causes a true (non-inverting) multiplexer output, and a LOW causes the output to be inverted. In a computer control unit, this allows testing of either true or complemented flag data at the microprogram sequencer test input.

An active LOW Multiplexer Enable input ($\overline{ME}$) allows the selected multiplexer input to be passed to the output. When $\overline{ME}$ is HIGH, the output is determined only by the Polarity Control bit.
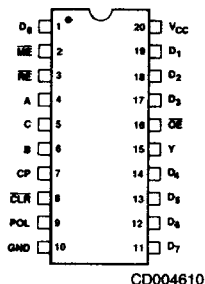
The Am2922 also features a three-state Output Enable control ($\overline{OE}$) for expansion. When $\overline{OE}$ is LOW, the output is enabled. When $\overline{OE}$ is HIGH, the output is in the high impedance state.
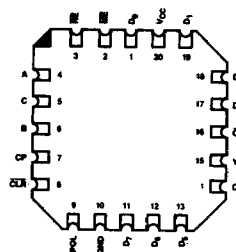
BLOCK DIAGRAM

BD002410

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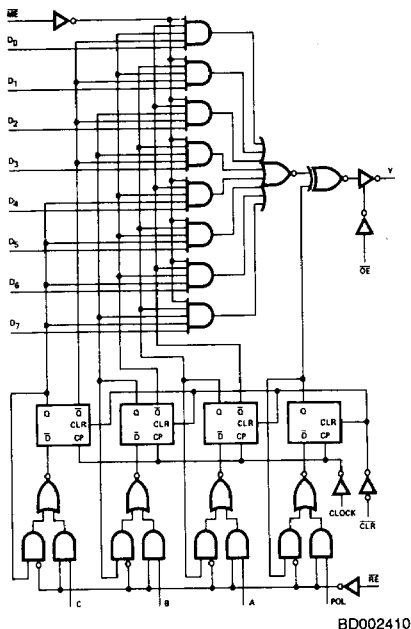
CONNECTION DIAGRAM
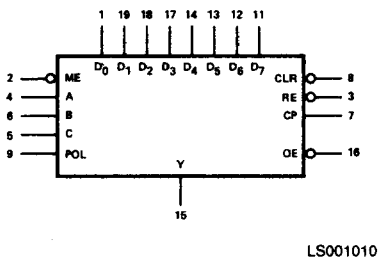## Top View

P-20
D-20

L-20-1
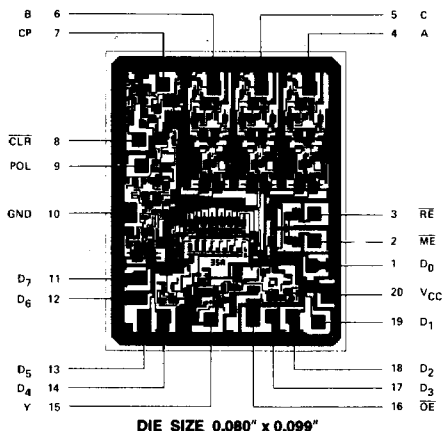

CD004610

CD004720

Note: Pin 1 is marked for orientation
F-20 pin configuration identical to D-20, P-20.

LOGIC SYMBOL

LS001010

METALLIZATION AND PAD LAYOUT

DIE SIZE 0.080″ x 0.099″

ORDERING INFORMATION

AMD products are available in several packages and operating ranges. The order number is formed by a combination of the following: Device number, speed option (if applicable), package type, operating range and screening option (if desired).

Am2922   D   C   B

└── Screening Option
Blank – Standard processing
B – Burn-in

└── Temperature (See Operating Range)
C – Commercial (0°C to +70°C)
M – Military (–55°C to +125°C)

└── Package
D – 20-pin CERDIP
F – 20-pin flatpak
L – 20-pin leadless chip carrier
P – 20-pin plastic DIP
X – Dice

└── Device type
8-Input Multiplexer
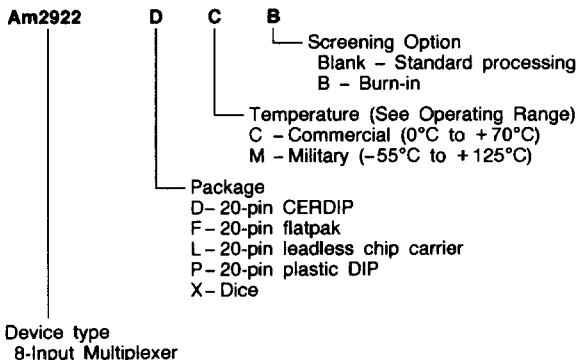
Valid Combinations	
Am2922	PC
	DC, DCB, DM, DMB
	FM, FMB
	LC, LCB, LM, LMB
	XC, XM

Valid Combinations
Consult the AMD sales office in your area to determine if a device is currently available in the combination you wish.

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PIN DESCRIPTION

| Pin No.	Name	I/O	Description
4, 6, 5	A, B, C	I	Multiplexer Select Lines. One of eight multiplexer data inputs is selected by the A, B and C register outputs.
9	POL	I	Polarity Control Bit. A HIGH register output causes a true (non-inverted) output and a LOW causes the output to be inverted.
2	$\overline{ME}$	I	Multiplexer Enable. When LOW, it enabled the 8-input multiplexer. When HIGH, the Y output is determined by only the Polarity Control bit.
3	$\overline{RE}$	I	Register Enable. When LOW, the Multiplexer Select and Polarity Control Register is enabled for loading. When HIGH, the register holds its current data.
8	$\overline{CLR}$	I	Clear. A LOW asynchronously resets the Multiplexer Select and Polarity Control Register.
	D_1-D_8	I	Data Inputs to the 8-input multiplexer.
7	CP	I	Clock Pulse. When $\overline{RE}$ is LOW, the Multiplexer Select and Polarity Control Register changes state on the LOW-to-HIGH transition of CP.
16	$\overline{OE}$	I	Output Enable. When LOW, the output is enabled. When HIGH, the output is in the high impedance state.
15	Y	O	The chip output.

FUNCTION TABLE

MODE	INPUTS							INTERNAL				INPUTS		OUTPUT
	C	B	A	POL	$\overline{RE}$	$\overline{CLR}$	CP	Q_C	Q_B	Q_A	Q_{POL}	$\overline{ME}$	$\overline{OE}$	Y
Clear	X	X	X	X	X	L	X	L	L	L	L	H	L	H
	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	↓	L	L	$\overline{D}_0$
												X	H	Z
Reg. Disable	X	X	X	X	H	H	X	NC	NC	NC	NC	L	L	$\overline{D}_i/D_i$ (Note 1)
Select (Multiplex)	L	L	L	L/H	L	H	↑	L	L	L	L/H	L	L	$\overline{D}_0/D_0$
	L	L	H					L	L	H				$\overline{D}_1/D_1$
	L	H	L					L	H	L				$\overline{D}_2/D_2$
	L	H	H					L	H	H				$\overline{D}_3/D_3$
	H	L	L					H	L	L				$\overline{D}_4/D_4$
	H	L	H					H	L	H				$\overline{D}_5/D_5$
	H	H	L					H	H	L				$\overline{D}_6/D_6$
	H	H	H	↓	↓	↓	↓	H	H	H	↓	↓	↓	$\overline{D}_7/D_7$
Multiplexer Disable	X	X	X	X	X	H	X	X	X	X	L	H	L	H
								X	X	X	H	H	L	L
Tri-state Output Disable	↓	↓	↓	↓	↓	↓	↓	X	X	X	X	X	H	Z

NC = No Change
X = Don't Care
H = High
L = Low
↑ = Low-to-High Transition
Z = High-Impedance

Note 1: The output will follow the selected input, D_i, or its complement depending on the state of the POL flip-flop.

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APPLICATIONS
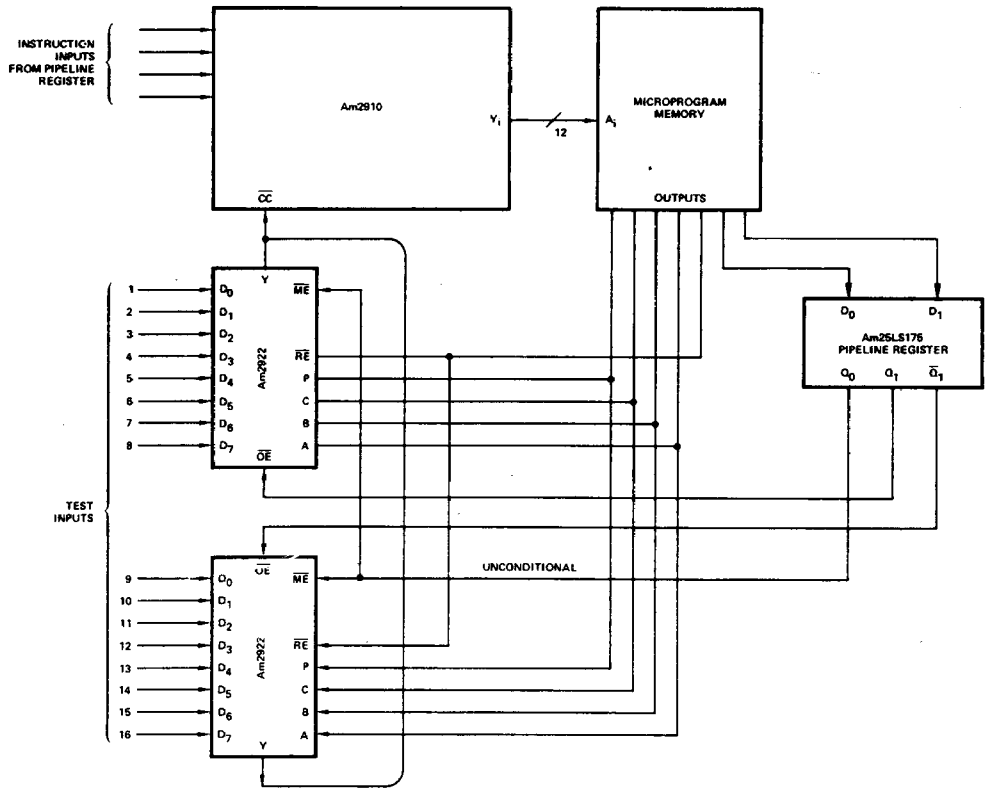
AF001840

A versatile one-of-sixteen Test Select with Polarity Control and Test Select Hold.

ABSOLUTE MAXIMUM RATINGS

Storage Temperature .... −65°C to +150°C
(Ambient) Temperature Under Bias .... −55°C to +125°C
Supply Voltage to Ground Potential
   Continuous .... −0.5V to +7.0V
DC Voltage Applied to Outputs For
   High Output State .... −0.5V to +V_{CC} max
DC Input Voltage .... −0.5V to +5.5V
DC Output Current, Into Outputs 30mA
DC Input Current .... −30mA to +5.0mA

Stresses above those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent device failure. Functionality at or above these limits is not implied. Exposure to absolute maximum ratings for extended periods may affect device reliability.

OPERATING RANGES

Commercial (C) Devices
   Temperature .... 0°C to +70°C
   Supply Voltage .... +4.75V to +5.25V

Military (M) Devices
   Temperature .... −55°C to +125°C
   Supply Voltage .... +4.5V to +5.5V

Operating ranges define those limits over which the functionality of the device is guaranteed.

DC CHARACTERISTICS over operating range unless otherwise specified

Parameters	Description	Test Conditions (Note 2)		Min	Typ (Note 1)	Max	Units
V_{OH}	Output HIGH Voltage	V_{CC} = MIN V_{IN} = V_{IH} or V_{IL}	MIL, I_{OH} = −2.0mA	2.4	3.4		Volts
			COM'L, I_{OH} = −6.5mA	2.4	3.2		
V_{OL}	Output LOW Voltage	V_{CC} = MIN V_{IN} = V_{IH} or V_{IL}	I_{OL} = 4.0mA			0.4	Volts
			I_{OL} = 8.0mA			0.45	
			I_{OL} = 20mA			0.5	
V_{IH}	Input HIGH Level	Guaranteed input logical HIGH voltage for all inputs		2.0			Volts
V_{IL}	Input LOW Level	Guaranteed input logical LOW voltage for all inputs	MIL			0.7	Volts
			COM'L	0.8			
V_{I}	Input Clamp Voltage	V_{CC} = MIN, I_{IN} = −18mA				−1.5	Volts
I_{IL}	Input LOW Current	V_{CC} = MAX, V_{IN} = 0.4V	$\overline{ME}$, $\overline{OE}$, $\overline{RE}$			−0.72	mA
			D_{N}, A, B, C, POL, CP, $\overline{CLR}$			−2.0	
I_{IH}	Input HIGH Current	V_{CC} = MAX, V_{IN} = 2.7V	$\overline{ME}$, $\overline{OE}$, $\overline{RE}$			40	μ
			D_{N}, A, B, C, POL, CP, $\overline{CLR}$			50	
I_{I}	Input HIGH Current	V_{CC} = MAX, V_{IN} = 5.5V	$\overline{ME}$, $\overline{OE}$, $\overline{RE}$			0.1	mA
			D_{N}, A, B, C, POL, CP, $\overline{CLR}$			1.0	
I_{OZ}	Off-State (High-Impedance) Output Current	V_{CC} = MAX	V_{O} = 0.4V			−50	μA
			V_{O} = 2.4V			50	
I_{SC}	Output Short Circuit Current (Note 3)	V_{CC} = MAX		−40		−100	mA
I_{CC}	Power Supply Current (Note 4)	V_{CC} = MAX			97	148	mA

Notes: 1. Typical limits are at V_{CC} = 5.0V, 25°C ambient and maximum loading.
    2. For conditions shown as MIN or MAX, use the appropriate value specified under Operating Ranges for the applicable device type.
    3. Not more than one output should be shorted at a time. Duration of the short circuit test should not exceed one second.
    4. D_{N}, A, B, C, POL, $\overline{ME}$ at Gnd. All other inputs and outputs open. Measured after a momentary ground then 4.5V applied to clock input.

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SWITCHING CHARACTERISTICS ($T_A = +25°C$, $V_{CC} = 5.0V$)

Parameters	Description	Test Conditions	Min	Typ	Max	Units
t_{PLH}	Clock to Y POL – LOW			21	32	ns
t_{PHL}				19	29	
t_{PLH}	Clock to Y POL – HIGH			16	24	ns
t_{PHL}				19	29	
t_{PLH}	D_n to Y			10	16	ns
t_{PHL}				13	19	
t_{PLH}	CLR to Y	$C_L = 15pF$		22	33	ns
t_{PHL}		$R_L = 2.0k\Omega$		22	33	
t_{PLH}	$\overline{ME}$ to Y			12	18	ns
t_{PHL}				12	18	
t_{ZL}	$\overline{OE}$ to Y			8	14	ns
t_{ZH}				8	14	
t_{LZ}		$C_L = 5.0pF$		10	17	ns
t_{HZ}		$R_L = 2.0k\Omega$		10	17	
t_s	A, B, C, POL		10			ns
	CE		15			
t_s	CLR Recovery	$C_L = 15pF$	5			ns
t_{pw}	Clock	$R_L = 2.0k\Omega$	10			ns
	Clear (LOW)		10			
t_h	A, B, C, POL, CE		0			ns

SWITCHING CHARACTERISTICS over operating range unless otherwise specified*

Parameters	Description	Test Conditions	COMMERCIAL Am2922		MILITARY Am2922		Units
			Min	Max	Min	Max	
t_{PLH}	Clock to Y POL – LOW			40		47	ns
t_{PHL}				34		38	
t_{PLH}	Clock to Y POL – HIGH			29		33	ns
t_{PHL}				35		41	
t_{PLH}	D_n to Y			19		21	ns
t_{PHL}				22		24	
t_{PLH}	CLR to Y	$C_L = 50pF$		39		45	ns
t_{PHL}		$R_L = 2.0k\Omega$		39		45	
t_{PLH}	$\overline{ME}$ to Y			22		26	ns
t_{PHL}				19		20	
t_{ZL}	$\overline{OE}$ to Y			19		24	ns
t_{ZH}				22		29	
t_{LZ}		$C_L = 5.0pF$		24		30	ns
t_{HZ}		$R_L = 2.0k\Omega$		24		30	
t_s	A, B, C, POL		11		12		ns
	CE		18		20		
t_s	CLR Recovery	$C_L = 50pF$	6		7		ns
t_{pw}	Clock	$R_L = 2.0k\Omega$	11		12		ns
	Clear (LOW)		11		12		
t_h	A, B, C, POL, CE		3		3		ns

*Switching Characteristics' performance over the operating temperature range is guaranteed by testing defined in Group A, Subgroup 9.

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LOW-POWER SCHOTTKY INPUT/OUTPUT CURRENT INTERFACE CONDITIONS
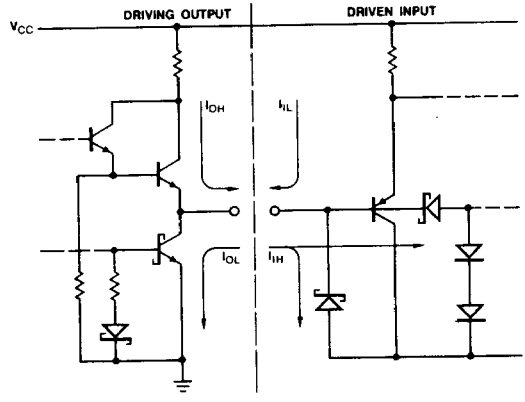
IC000381

Note: Actual current flow direction shown.

RELATED PRODUCTS

Part No.	Description
Am25LS2535	8-Input Multiplexer
Am2923	8-Input Multiplexer

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Refer to Page 13-1 for Essential Information on Military Devices