

MOS FIELD EFFECT TRANSISTOR

2SK2275

SWITCHING N-CHANNEL POWER MOS FET INDUSTRIAL USE

DESCRIPTION

The 2SK2275 is N-channel Power MOS Field Effect Transistor designed for high voltage switching applications.

FEATURES

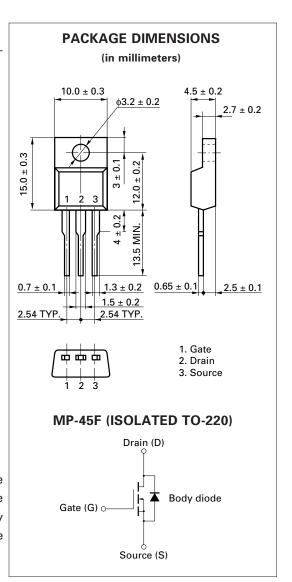
- Low On-state Resistance $R_{DS(on)} = 2.8 \Omega$ MAX. (Vgs = 10 V, ID = 2.0 A)
- Low Ciss Ciss = 1 000 pF TYP.
- High Avalanche Capability Ratings

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C)

Drain to Source Voltage	VDSS	900	V
Gate to Source Voltage	Vgss	±30	V
Drain Current (DC)	ID (DC)	±3.5	Α
Drain Current (pulse)	ID (pulse)*	±14	Α
Total Power Dissipation (Tc = 25 °C)	P _{T1}	35	W
Total Power Dissipation (Ta = 25 °C)	P _{T2}	2.0	W
Storage Temperature	T _{stg} -55	to +150	$^{\circ}\text{C}$
Channel Temperature	Tch	150	$^{\circ}\text{C}$
Single Avalanche Current	las**	3.5	Α
Single Avalanche Energy	Eas**	22	mJ

*PW \leq 10 μ s, Duty Cycle \leq 1%

The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device is actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.



^{**}Starting Tch = 25 °C, Rg = 25 Ω , Vgs = 20 V \rightarrow 0

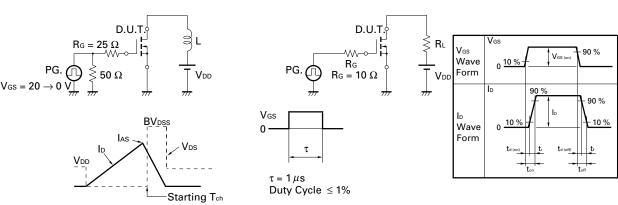


ELECTRICAL CHARACTERISTICS (TA = 25 °C)

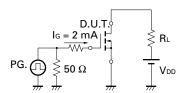
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Drain to Source On-state Resistance	RDS(on)		2.2	2.8	Ω	Vgs = 10 V, ID = 2 A
Gate to Source Cutoff Voltage	V _{GS(off)}	2.5		3.5	V	V _{DS} = 10 V, I _D = 1 mA
Forward Transfer Admittance	yfs	1.0			S	V _{DS} = 20 V, I _D = 2 A
Drain Leakage Current	IDSS			100	μΑ	V _{DS} = 900 V, V _{GS} = 0
Gate to Source Leakage Current	Igss			±10	μΑ	$V_{GS} = \pm 30 \text{ V, } V_{DS} = 0$
Input Capacitance	Ciss		1 000		pF	V _{DS} = 10 V
Output Capacitance	Coss		170		pF	Vgs = 0
Reverse Transfer Capacitance	Crss		60		pF	f = 1 MHz
Turn-On Delay Time	td(on)		20		ns	Vgs = 10 V
Rise Time	t r		20		ns	V _{DD} = 150 V
Turn-Off Delay Time	td(off)		90		ns	$I_D = 2 A$, $R_G = 10 \Omega$
Fall Time	t f		20		ns	RL = 75 Ω
Total Gate Charge	Q _G		42		nC	Vgs = 10 V
Gate to Source Charge	Qgs		6.0		nC	ID = 3.5 A
Gate to Drain Charge	Q _{GD}		20		nC	V _{DD} = 450 V
Diode Forward Voltage	V _{F(S-D)}		0.9		V	IF = 3.5 A, VGS = 0
Reverse Recovery Time	trr		480		ns	1 _F = 3.5 A
Reverse Recovery Charge	Qrr		2.5		μC	di/dt = 50 A/μs

Test Circuit 1: Avalanche Capability

Test Circuit 2: Switching Time



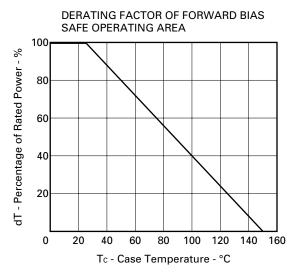
Test Circuit 3: Gate Charge

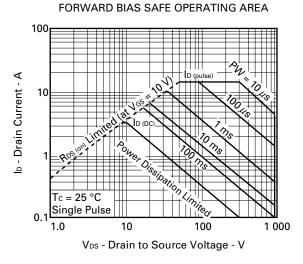


The application circuits and their parameters are for references only and are not intended for use in actual design-in's.

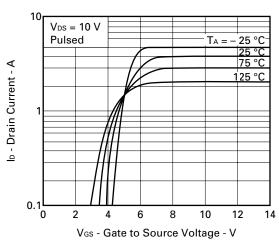


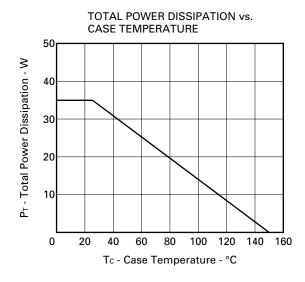
TYPICAL CHARACTERISTICS (T_A = 25 °C)



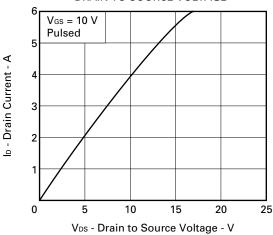


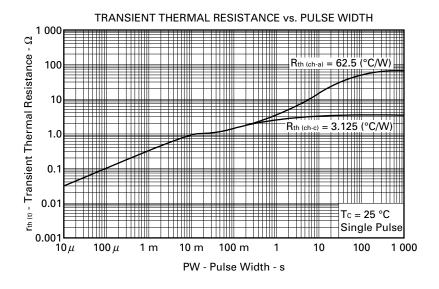
TRANSFER CHARACTERISTICS

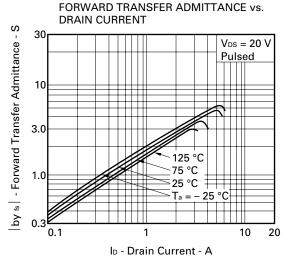


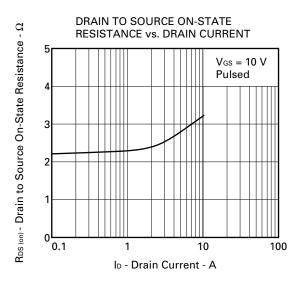


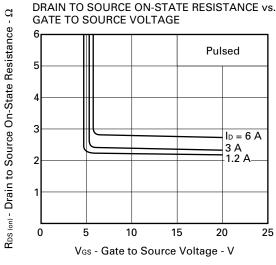


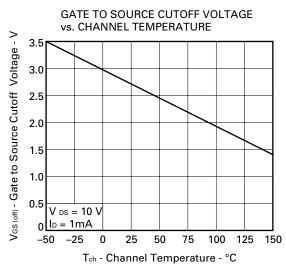




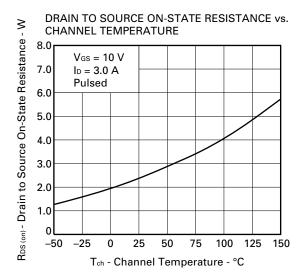


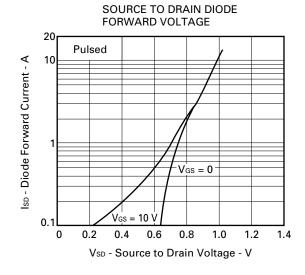


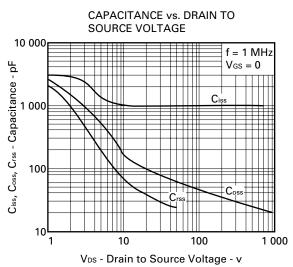


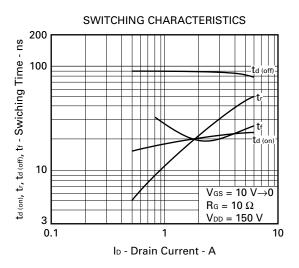


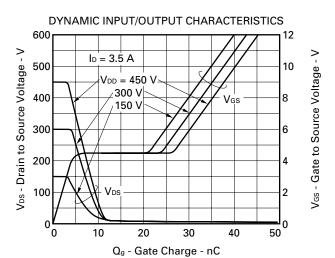


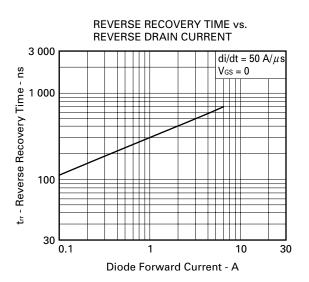


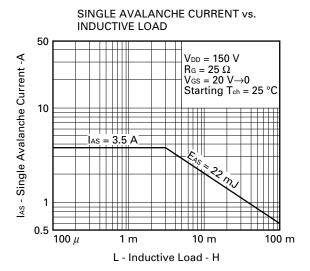




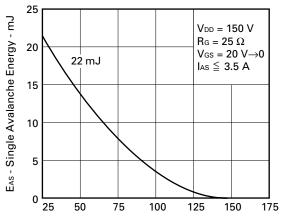








SINGLE AVALANCHE ENERGY vs. STARTING CHANNEL TEMPERATURE



Starting T_{ch} - Starting Channel Temperature - $^{\circ}C$



REFERENCE

Document Name	Document No.
NEC semiconductor device reliability/quality control system.	TEI-1202
Quality grade on NEC semiconductor devices.	IEI-1209
Semiconductor device mounting technology manual.	IEI-1207
Semiconductor device package manual.	IEI-1213
Guide to quality assurance for semiconductor devices.	MEI-1202
Semiconductor selection guide.	MF-1134
Power MOS FET features and application switching power supply.	TEA-1034
Application circuits using Power MOS FET.	TEA-1035
Safe operating area of Power MOS FET.	TEA-1037

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Anti-radioactive design is not implemented in this product.

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