PM7350

S/UNI -
DUPLEX ™
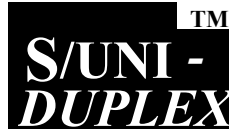
S/UNI-DUPLEX DEVICE DRIVER

DUAL SERIAL-LINK, PHY MULTIPLEXER

DEVICE DRIVER RELEASE NOTES

RELEASE-1.0

ISSUE 3: AUGUST 2000

PUBLIC REVISION HISTORY

Issue No.	Issue Date	Details of Change
1	July 1999	Document created
2	Jan 2000	Beta-1.1 driver release. This release enables Rev. B device detection.
3	August 2000	Updated documentation for Release-1.0 Added FIFO reset routine (see section 4)

1   ABOUT THIS RELEASE

This is Release-1.0 of the PM7350 (S/UNI-DUPLEX) device driver. It is intended for use with PM7350 S/UNI-DUPLEX devices. The Release-1.0 is functionally complete and has been tested by PMC-Sierra as described in Section 3 below.

2   WHAT'S INCLUDED IN THIS RELEASE?

Release-1.0 of the S/UNI-DUPLEX driver includes the following files:

Directory	Filename	File Version
dpxdrv/src	dpx_api.c	30
	dpx.c	23
	dpx_hw.c	11
	dpx_rtos.c	18
	dpx_test.c[1]	31
dpxdrv/inc	dpx_api.h	15
	dpx.h	19
	dpx_hw.h	9
	dpx_rtos.h	14
	dpx_err.h	10
	dpx_test.h[1]	8
bsp	sysPci.c[2]	4
	sysPci.h[2]	3

Notes:

1. dpx_test.c, dpx_test.h files contain example callback function implementation, as well as example application code for testing.

2. sysPci.c, sysPci.h files contain code specific to PMC test platform and VxWorks RTOS. This code should only be used as a reference.

3   TESTING STATUS OF THIS RELEASE

All S/UNI-DUPLEX device driver Application Programming Interface (API) functions have been tested in-house using the PM7350 on a proprietary PMC-Sierra, Inc. validation platform running VxWorks™ RTOS. Example code has been included in this release, demonstrating interoperability with this hardware and operating system.

As of the publication date of this document, there are no known bugs in this release. However, the customer is advised to consult our web site (http://www.pmc-sierra.com) for any relevant errata that may have been issued subsequent to the publication of this document.

4   WHAT'S NEW IN THIS RELEASE?

Release-1.0: This release has the following changes since beta-1.1 release:

Additions:

1. Added the utility function `duplexResetFifos,` which can be used to reset all Receive and Transmit logical channels, RXD1 and RXD2 Extract FIFOs, to the file dpx.c
2. Added `(volatile UINT1 *)` to `sysDuplexRawRead` and `sysDuplexRawWrite` macros in the file dpx_hw.h

Modifications:

1. Added calls to utility FIFO reset function `duplexResetFifos` in two API functions: `duplexInit` and `duplexReset`
2. Changed compiler switch `DEVICE_CHK_OVERRIDE` to `DPX_CSW_CHK_OVERRIDE` in the file dpx_api.c

Bug Fixes:

1. Fixed byte order in (VPI, VCI) and AAL5 packet length calculation in the sample code (see file dpx_test.c)
2. Fixed `u1RxHssBocIntEn[10]` index to be `1` in the sample code (see file dpx_test.c)
3. Corrected `DPX_MASK_HSS_RX_CFG_HDSCR` and `DPX_MASK_HSS_RX_CFG_DDSCR` definitions in the file dpx.h

5　　REPORTING PROBLEMS

Please refer to the revision numbers in the files when reporting problems. For technical support, please contact PMC-Sierra by e-mail at apps@pmc-sierra.com or by telephone at 604-415-4533.

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