

MN152811

Type	MN152811		
ROM (x8-bit)	8K		
RAM (x4-bit)	256		
Number of Instructions	115		
Minimum Instruction Execution Time	2.0µs (at 4.5 to 5.5V, 6MHz)		
Interrupts	• RESET • SIRQ • Remote Control Input • Timer • Serial (Only when choosing Mask Option)		
Timer Counter	Timer Counter : 8-bit x 1 (Timer Output, Event Count) Clock Source 1/2, 1/8, 1/32, 1/128 of System Clock Interrupt Source Overflow of Timer Counter		
Serial Interface	Serial : 8-bit x 1 Clock Source System Clock, $\overline{\text{SBT}}$ Pin Input		
I/O Pins	I/O	5	• Common use · 2 • Specified pull-up Resistor available (Mask Option) · 2 • Nch Open-drain available (Output) 5
	Input	4	• Common use 1 • Specified pull-up Resistor available (Mask Option) 1 • Output selectable (Software Programmable) 3
	High Voltage Output	5	• Nch Open-drain (Breakdown Voltage 12V) · 5 • Push-pull Output selectable (Mask Option) 4
	Output	3	
A/D Inputs	5-bit x 4ch (Conversion by Software)		
PWM	7-bit x 6ch (Repetition Cycle 256µs, at 6MHz), 14-bit x 1ch (Repetition Cycle 32 8ms, at 6MHz)		
Special Ports	Tri-state Output (PTO), Remote Control Reception		
CRTC	5 x 7 dots, 16 characters, 2 lines, 7 colors, 56 patterns, Rounding function, Framing function		
Notes	Remote Control Data Detection Circuit built-in, For Voltage Synthesizer		
Package	SDIP042-P-0600		

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Electrical Characteristics

Supply Current

Parameter	Symbol	Condition	Limit			Unit
			min	typ	max	
Operating Supply Current	IDD1	fosc=6MHz, VDD=5V Ta=25°C		5.0	10	mA
Supply Current at STOP	IDD3	fosc=0Hz, VDD=5V Ta=25°C			2.0	μA

(VDD=5.0V, VSS=0V)

A/D, D/A Converter Characteristics

Parameter	Symbol	Condition	Limit			Unit
			min	typ	max	
A/D Conversion Absolute Error		VDD=5V, VSS=0V			±1	LSB
Analog Input Voltage			VSS		VDD	

(VDD=5.0V, VSS=0V, Ta=25, 80°C)

Support Tool

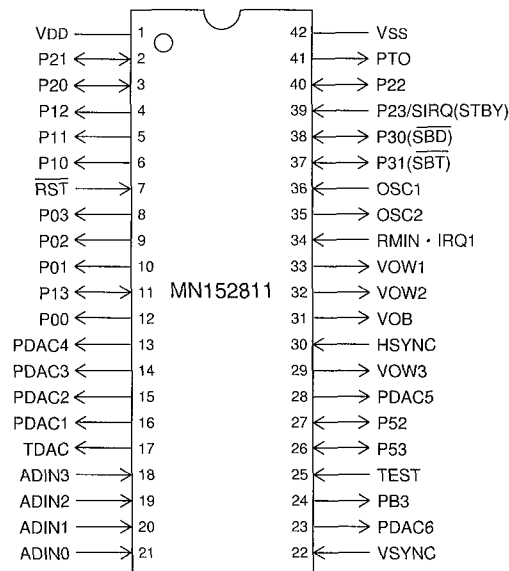
In-Circuit Emulator

PX-ICE1500 + PX-PRB152811

Piggyback

Use EP152811

Pin Assignment



SDIP042-P-0600