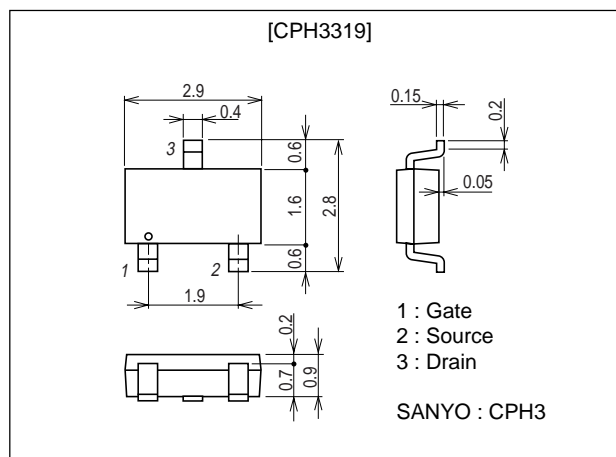


**CPH3319****Ultrahigh-Speed Switching Applications****Features**

- Low ON-resistance.
- Ultrahigh-speed switching.
- 1.8V drive.

Package Dimensionsunit : mm
2152A**Specifications**

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		-12	V
Gate-to-Source Voltage	V _{GSS}		±8	V
Drain Current (DC)	I _D		-1.5	A
Drain Current (Pulse)	I _{DP}	PW≤10μs, duty cycle≤1%	-6	A
Allowable Power Dissipation	P _D	Mounted on a ceramic board (900mm²×0.8mm)	0.9	W
Channel Temperature	T _{ch}		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	I _D =-1mA, V _{GS} =0	-12			V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} =-12V, V _{GS} =0			-10	μA
Gate-to-Source Leakage Current	I _{GSS}	V _{GS} =±6.4V, V _{DS} =0			±10	μA
Cutoff Voltage	V _{GS(off)}	V _{DS} =-6V, I _D =-1mA	-0.3		-1.0	V
Forward Transfer Admittance	y _{fs}	V _{DS} =-6V, I _D =-0.8A	1.3	1.8		S
Static Drain-to-Source On-State Resistance	R _{DS(on)1}	I _D =-0.8A, V _{GS} =-4.5V		220	290	mΩ
	R _{DS(on)2}	I _D =-0.4A, V _{GS} =-2.5V		320	450	mΩ
	R _{DS(on)3}	I _D =-0.1A, V _{GS} =-1.8V		430	650	mΩ

Marking : JU

Continued on next page.

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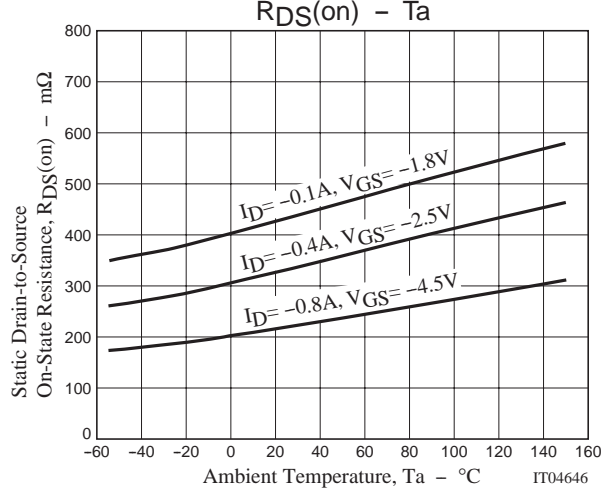
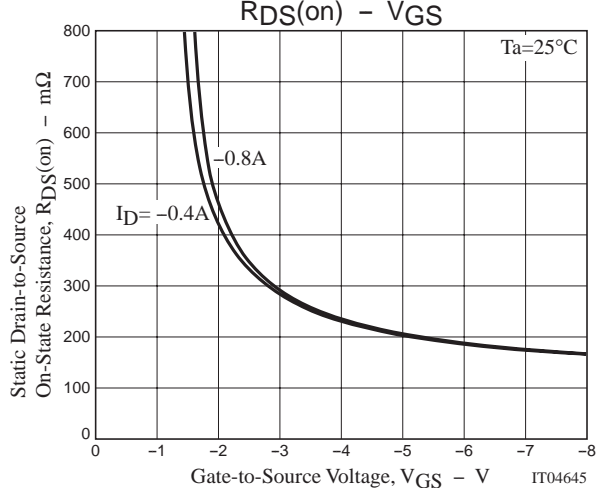
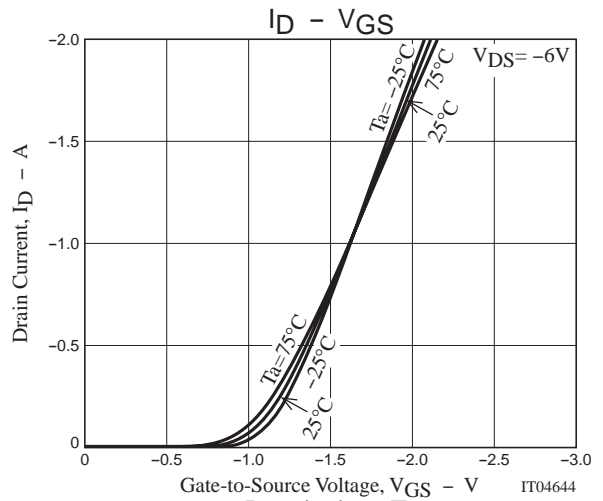
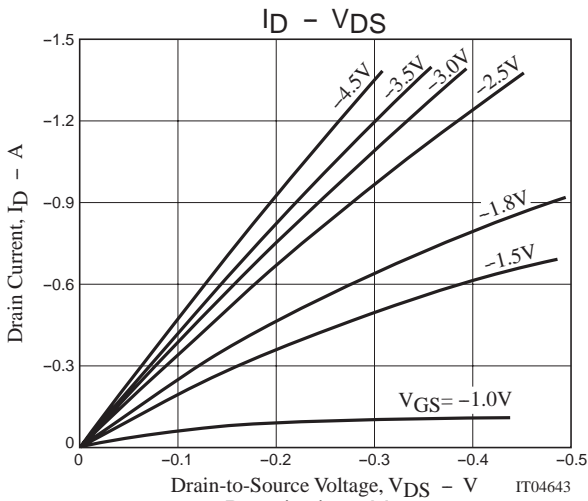
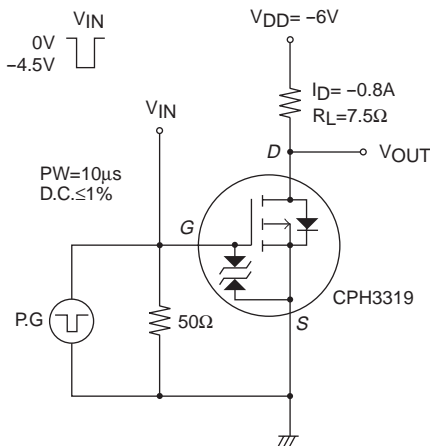
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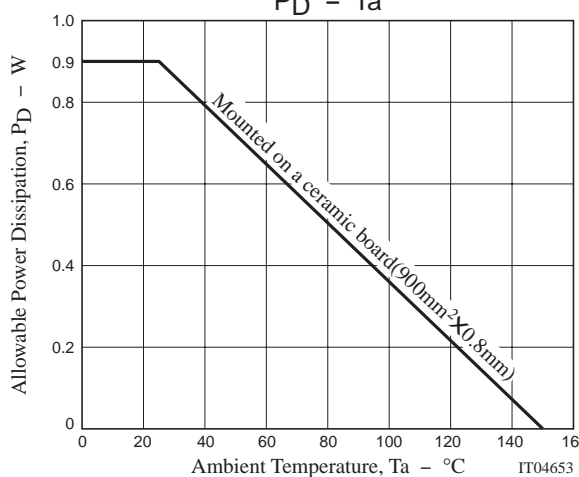
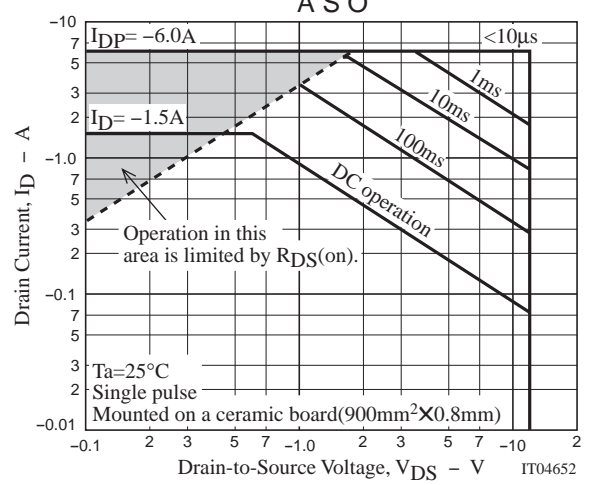
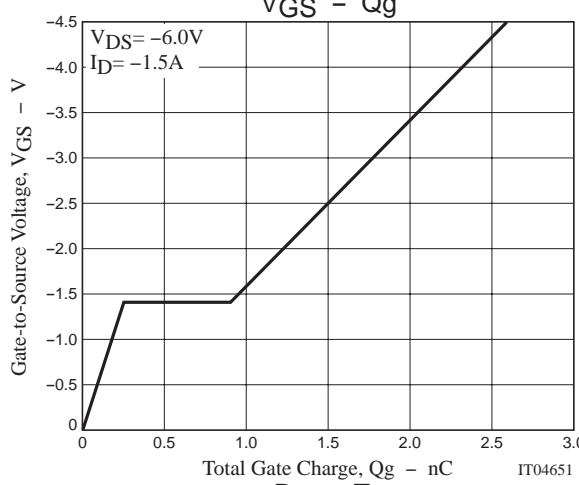
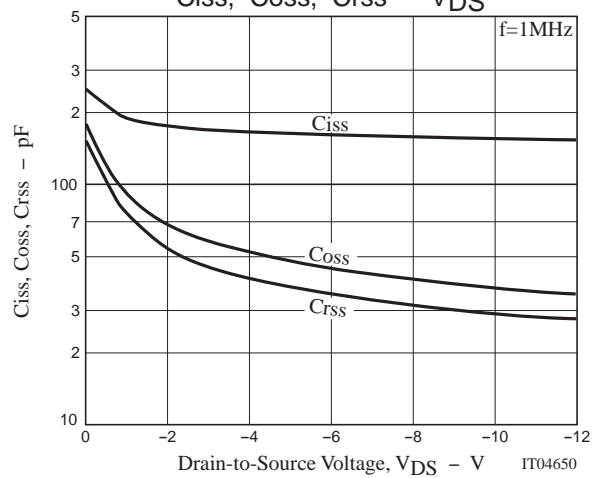
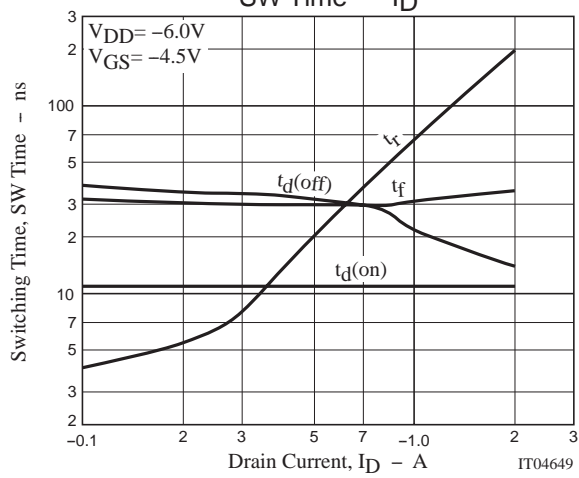
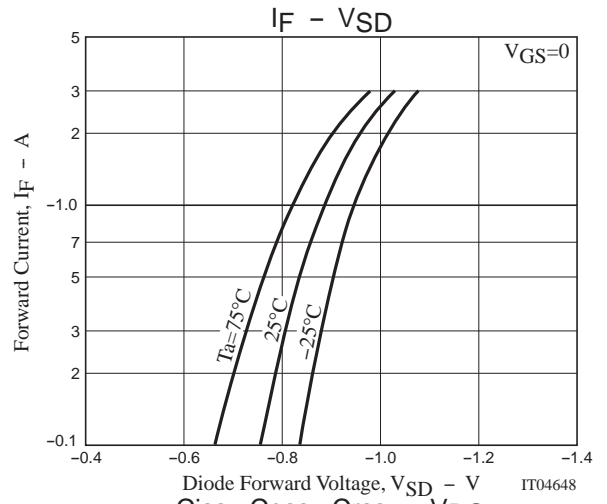
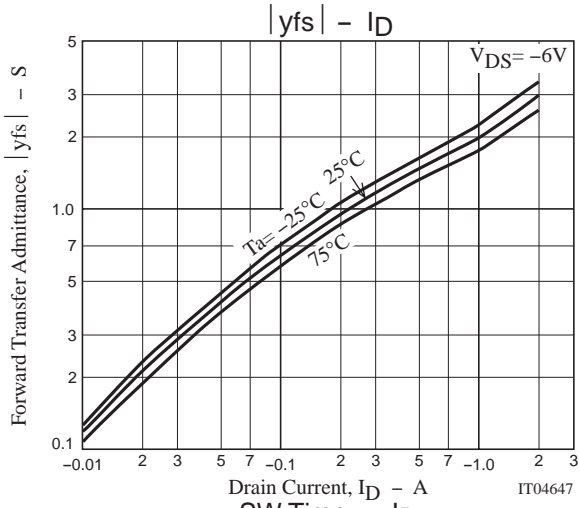
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Input Capacitance	Ciss	V _{DS} =-6V, f=1MHz		160		pF
Output Capacitance	Coss	V _{DS} =-6V, f=1MHz		45		pF
Reverse Transfer Capacitance	Crss	V _{DS} =-6V, f=1MHz		35		pF
Turn-ON Delay Time	t _{d(on)}	See specified Test Circuit.		11		ns
Rise Time	t _r	See specified Test Circuit.		45		ns
Turn-OFF Delay Time	t _{d(off)}	See specified Test Circuit.		29		ns
Fall Time	t _f	See specified Test Circuit.		30		ns
Total Gate Charge	Q _g	V _{DS} =-6V, V _{GS} =-4.5V, I _D =-1.5A		2.6		nC
Gate-to-Source Charge	Q _{gs}	V _{DS} =-6V, V _{GS} =-4.5V, I _D =-1.5A		0.25		nC
Gate-to-Drain "Miller" Charge	Q _{gd}	V _{DS} =-6V, V _{GS} =-4.5V, I _D =-1.5A		0.65		nC
Diode Forward Voltage	V _{SD}	I _S =-1.5A, V _{GS} =0	-0.92		-1.5	V

Switching Time Test Circuit





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