

CEP8060R/CEB8060R

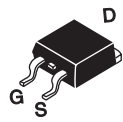
PRELIMINARY

N-Channel Enhancement Mode Field Effect Transistor

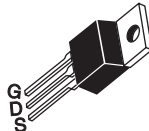
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FEATURES

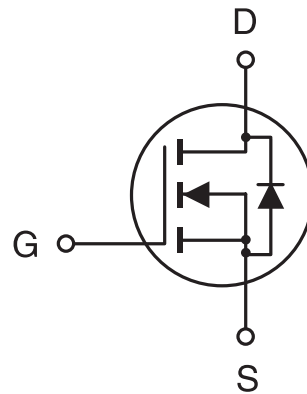
- 60V , 80.0A , $R_{DS(ON)}=10.0m\Omega$ @ $V_{GS}=10V$.
- Super high dense cell design for extremely low $R_{DS(ON)}$.
- High power and current handling capability.
- TO-220 & TO-263 package.



CEB SERIES
TO-263(DD-PAK)



CEP SERIES
TO-220



ABSOLUTE MAXIMUM RATINGS (Tc=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	60	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous -Pulsed	I _D	80	A
	I _{DM}	225	A
Drain-Source Diode Forward Current	I _S	80	A
Maximum Power Dissipation @Tc=25°C Derate above 25°C	P _D	150	W
		1	W/°C
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to 175	°C

THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Case	R _{θJC}	1	°C/W
Thermal Resistance, Junction-to-Ambient	R _{θJA}	62.5	°C/W

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ELECTRICAL CHARACTERISTICS (Tc=25°C unless otherwise noted)

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Parameter	Symbol	Condition	Min	Typ ^c	Max	Unit
DRAIN-SOURCE AVALANCHE RATING^a						
Single Pulse Drain-Source Avalanche Energy	EAS	V _{DD} =25V, I _D =150A		430		mJ
Maximum Drain-Source Avalanche Current	IAS	L=25μH		150		A
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA	60			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V			25	μA
Gate-Body Leakage	I _{GSS}	V _{GS} =±20V, V _{DS} =0V			±100	nA
ON CHARACTERISTICS^a						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	2	2.6	4	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =40A		9.0	10	mΩ
On-State Drain Current	I _{D(ON)}	V _{GS} =10V, V _{DS} =10V	60			A
Forward Transconductance	g _{FS}	V _{DS} =10V, I _D =40A		38		S
SWITCHING CHARACTERISTICS^b						
Turn-On Delay Time	t _{D(ON)}	V _{DD} =30V, I _D =75A, V _{GS} =10V,		20	30	ns
Rise Time	t _r			250	400	ns
Turn-Off Delay Time	t _{D(OFF)}			50	80	ns
Fall Time	t _f			135	200	ns
Total Gate Charge	Q _g	V _{DS} =48V, I _D =75A, V _{GS} =10V		61	115	nC
Gate-Source Charge	Q _{gs}			15		nC
Gate-Drain Charge	Q _{gd}			18		nC

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ELECTRICAL CHARACTERISTICS ($T_c=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
DYNAMIC CHARACTERISTICS^b						
Input Capacitance	C_{iss}	$V_{DS}=25\text{V}, V_{GS}=0\text{V}$ $f=1.0\text{MHz}$		2532	3300	pF
Output Capacitance	C_{oss}			924	1200	pF
Reverse Transfer Capacitance	C_{rss}			148	190	pF
DRAIN-SOURCE DIODE CHARACTERISTICS^a						
Diode Forward Voltage	V_{SD}	$V_{GS}=0\text{V}, I_S=37.5\text{A}$		0.9	1.3	V

Notes

- a. Pulse Test: Pulse Width $\leq 300\ \mu\text{s}$, Duty Cycle $\leq 2\%$.
- b. Guaranteed by design, not subject to production testing.

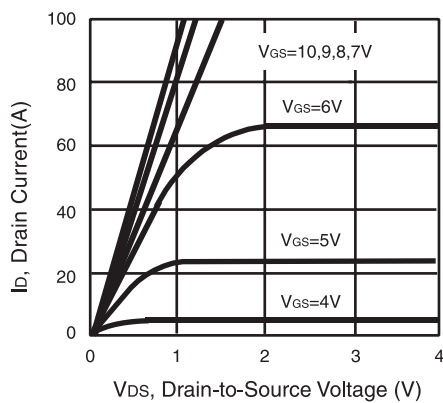


Figure 1. Output Characteristics

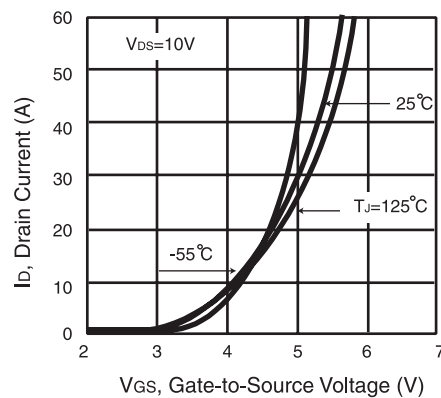


Figure 2. Transfer Characteristics

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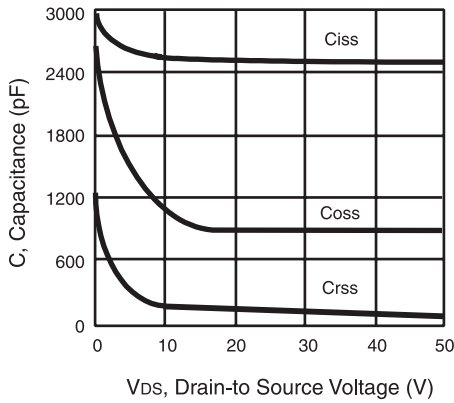


Figure 3. Capacitance

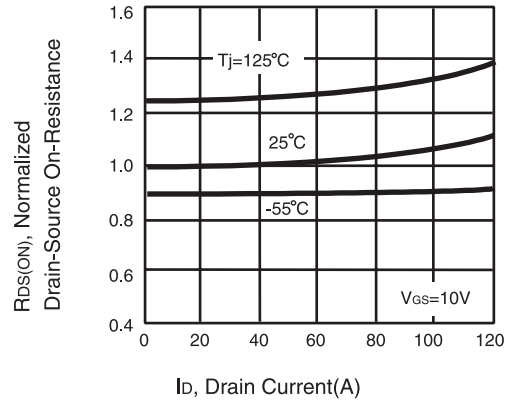


Figure 4. On-Resistance Variation with Drain Current and Temperature

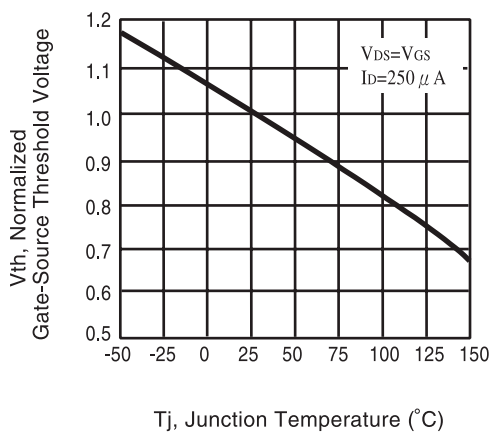


Figure 5. Gate Threshold Variation with Temperature

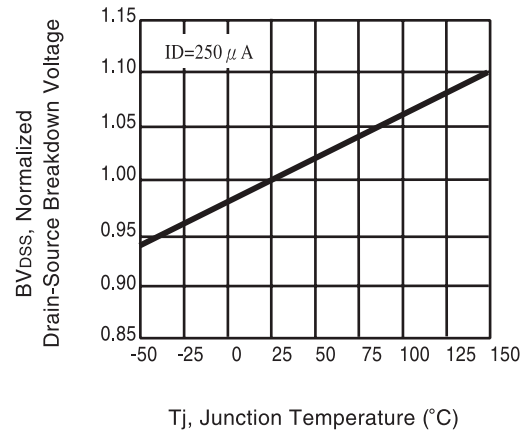


Figure 6. Breakdown Voltage Variation with Temperature

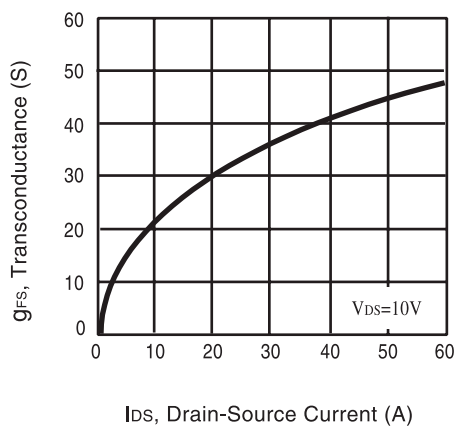


Figure 7. Transconductance Variation with Drain Current

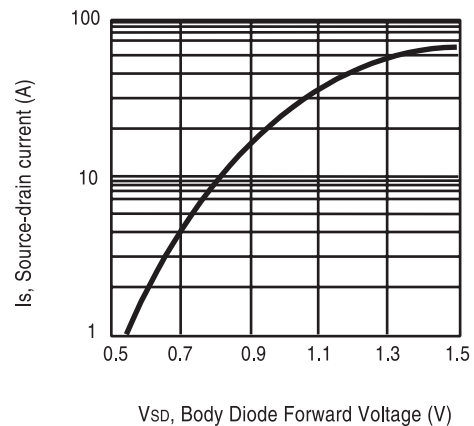


Figure 8. Body Diode Forward Voltage Variation with Source Current

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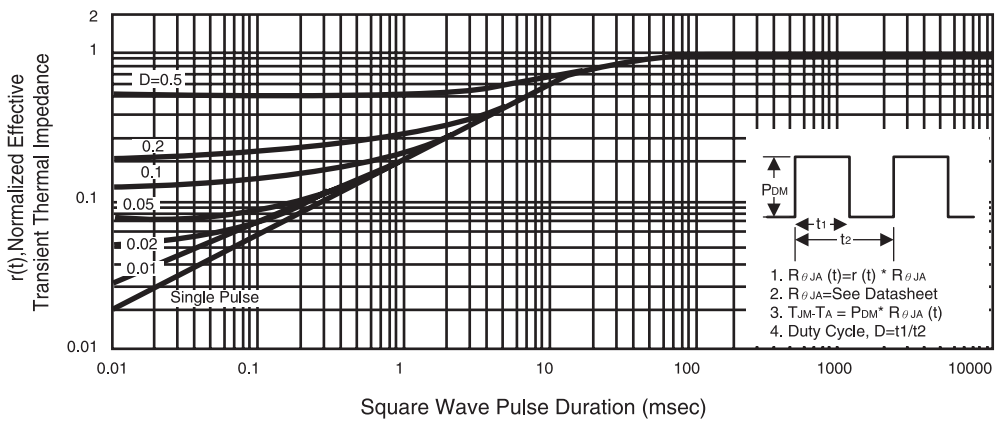
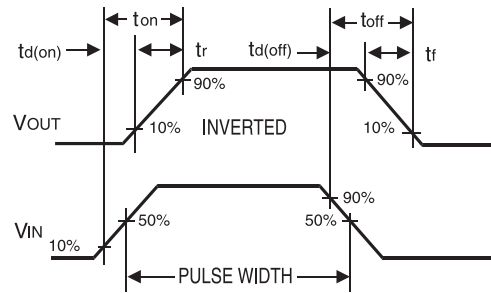
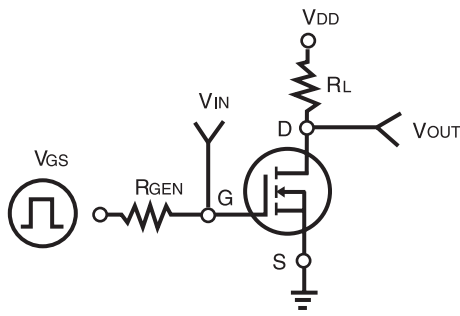
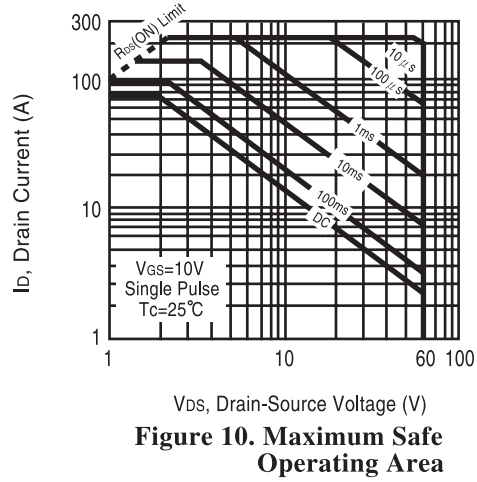
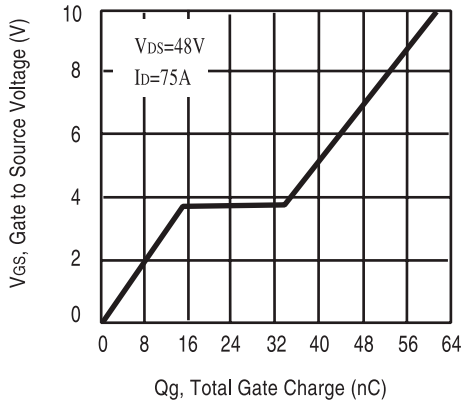


Figure 13. Normalized Thermal Transient Impedance Curve