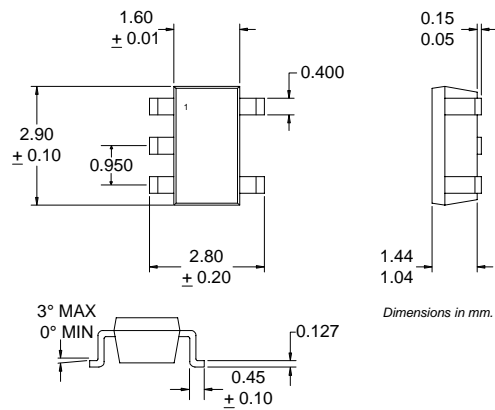


Typical Applications

- Broadband Gain Blocks
- Final PA for Low-Power Applications
- IF or RF Buffer Amplifiers
- Driver Stage for Power Amplifiers
- Oscillator Loop Amplifiers

Product Description

The RF2326 is a general purpose, low-cost silicon amplifier designed for operation from a 3V supply. The Darlington circuit configuration with resistive feedback allows for broadband cascadable amplification. The device is unconditionally stable and internally matched to 50Ω. The only external components required for specified performance are bypass and DC blocking capacitors and two bias elements (as shown in application schematic). The RF2326 is available in a very small industry-standard SOT-23 5-lead surface mount package, enabling compact designs which conserve board space.



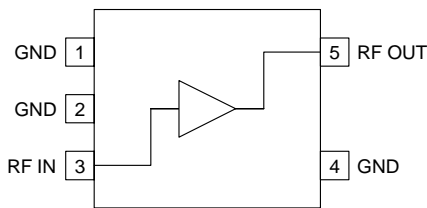
4
GENERAL PURPOSE AMPLIFIERS

Optimum Technology Matching® Applied

- | | | |
|--|-----------------------------------|--------------------------------------|
| <input checked="" type="checkbox"/> Si BJT | <input type="checkbox"/> GaAs HBT | <input type="checkbox"/> GaAs MESFET |
| <input type="checkbox"/> Si Bi-CMOS | <input type="checkbox"/> SiGe HBT | <input type="checkbox"/> Si CMOS |

Package Style: SOT 5-Lead

- Features
- DC to >2000MHz Operation
 - 2.7V to 3.3V Single Supply
 - +18dBm Output IP3
 - 12dB Gain at 900MHz
 - 10dB Gain at 1900MHz
 - Internally 50Ω Matched Input and Output



Functional Block Diagram

Ordering Information

RF2326	3V General Purpose Amplifier
RF2326 PCBA	Fully Assembled Evaluation Board

RF Micro Devices, Inc. 7628 Thorndike Road Greensboro, NC 27409, USA	Tel (336) 664 1233 Fax (336) 664 0454 http://www.rfmd.com
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Absolute Maximum Ratings

Parameter	Rating	Unit
Supply Voltage	4.0	V
Operating Ambient Temperature	-40 to +85	°C
Storage Temperature	-40 to +150	°C



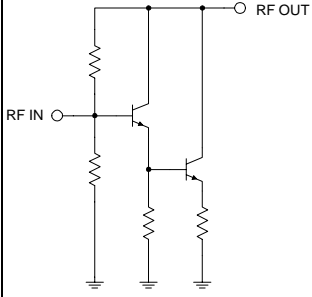
Caution! ESD sensitive device.

RF Micro Devices believes the furnished information is correct and accurate at the time of this printing. However, RF Micro Devices reserves the right to make changes to its products without notice. RF Micro Devices does not assume responsibility for the use of the described product(s).

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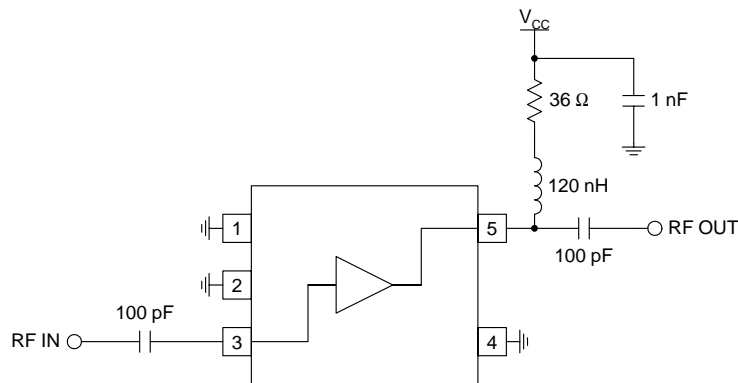
GENERAL PURPOSE
AMPLIFIERS

Parameter	Specification			Unit	Condition
	Min.	Typ.	Max.		
Overall Frequency Range		DC to >2000		MHz	T=27 °C, V _{CC} =3.0V
100MHz Performance Gain Noise Figure Output IP3 Output P _{1dB} Input Return Loss Output Return Loss Isolation		12 5.9 19 7 13 13 18		dB dB dBm dBm dB dB dB	T=27 °C, V _{CC} =3.0V
500MHz Performance Gain Noise Figure Output IP3 Output P _{1dB} Input Return Loss Output Return Loss Isolation		13 5.9 19 8 15 27 17		dB dB dBm dBm dB dB dB	T=27 °C, V _{CC} =3.0V
900MHz Performance Gain Noise Figure Output IP3 Output P _{1dB} Input Return Loss Output Return Loss Isolation	10.6	12 5.7 18 7 12 21 18	13.5	dB dB dBm dBm dB dB dB	T=27 °C, V _{CC} =3.0V
1000MHz Performance Gain Noise Figure Output IP3 Output P _{1dB} Input Return Loss Output Return Loss Isolation		12 5.8 17 6 11 20 18		dB dB dBm dBm dB dB dB	T=27 °C, V _{CC} =3.0V
2000MHz Performance Gain Noise Figure Output IP3 Output P _{1dB} Input Return Loss Output Return Loss Isolation		10 5.8 13 4 9 16 18		dB dB dBm dBm dB dB dB	T=27 °C, V _{CC} =3.0V
Power Supply Operating Voltage Operating Current		3.0±10% 25	28.7	V mA	V _{CC} =3.0V

Pin	Function	Description	Interface Schematic
1	GND	Ground connection. Keep traces physically short and connect immediately to ground plane for best performance.	
2	GND	Same as pin 1.	
3	RF IN	RF input pin. This pin is not externally DC blocked and thus requires an external blocking capacitor suitable for the frequency of operation. The input impedance of this pin is internally matched to 50Ω using resistive feedback.	
4	GND	Same as pin 1.	
5	RF OUT	RF output and bias pin. The input impedance of this pin is internally matched to 50Ω using resistive feedback. Bias should be supplied to this pin through an external series resistor and RF choke inductor. Because DC biasing is present on this pin, a DC blocking capacitor should be used in most applications (see application schematic). The supply side of the bias network should be well-bypassed.	See pin 3 schematic.

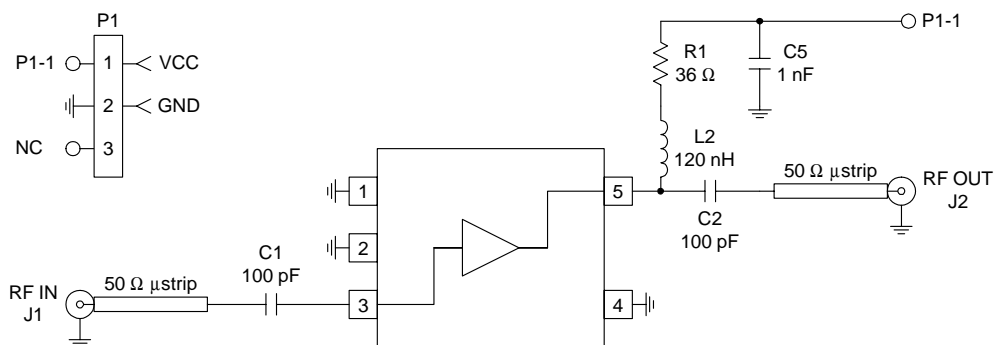
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GENERAL PURPOSE
AMPLIFIERS

Application Schematic

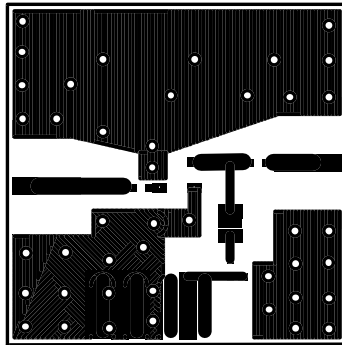
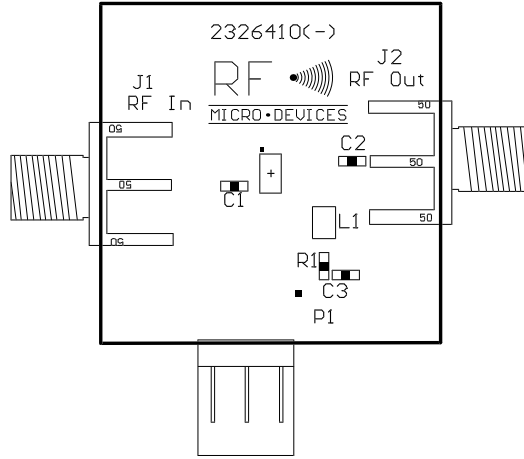


Evaluation Board Schematic

(Download [Bill of Materials](http://www.rfmd.com) from www.rfmd.com.)

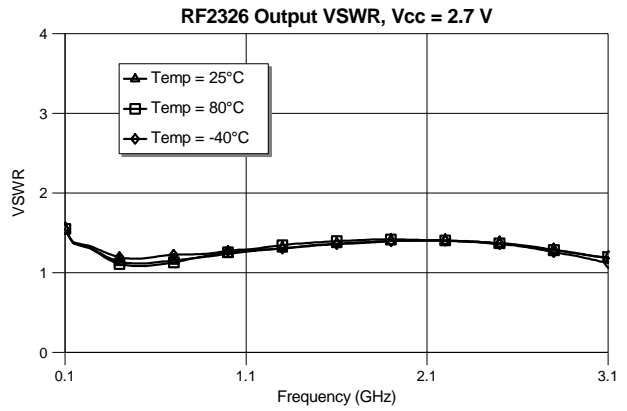
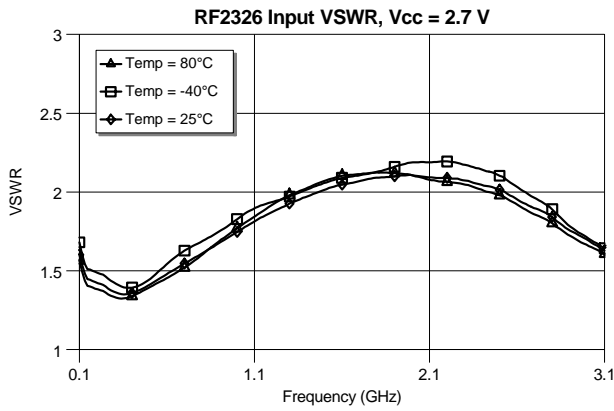
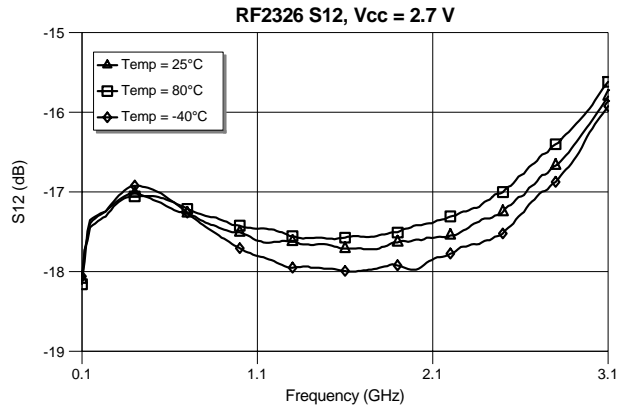
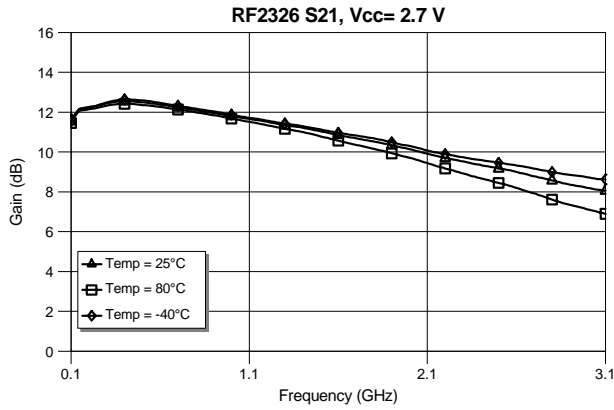


Evaluation Board Layout
1" x 1"

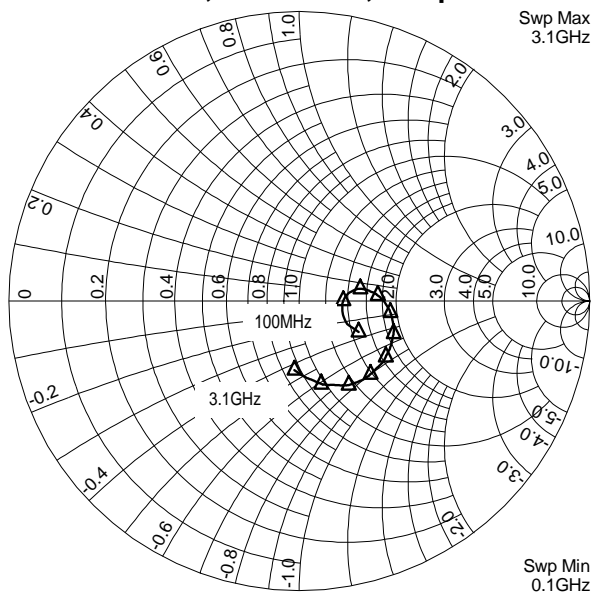


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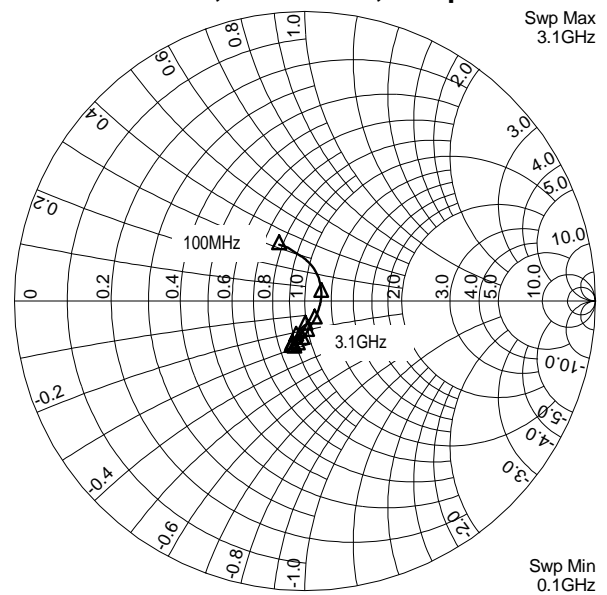
GENERAL PURPOSE
AMPLIFIERS

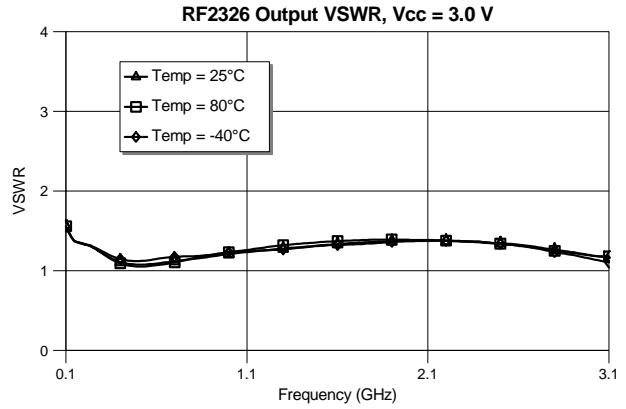
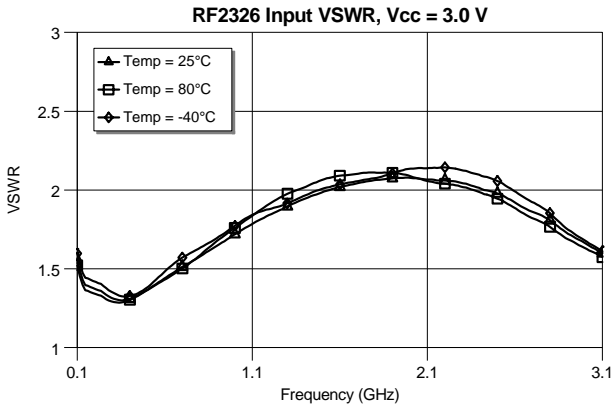
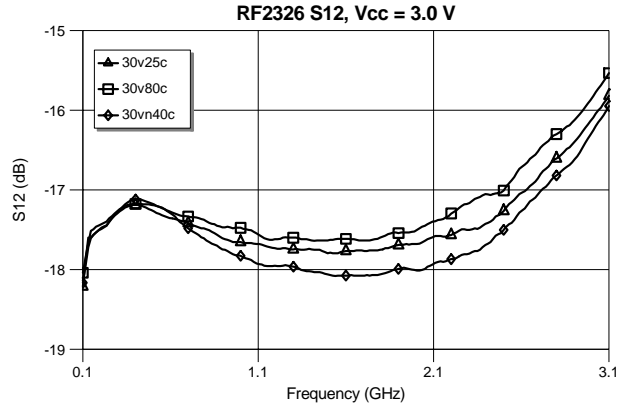
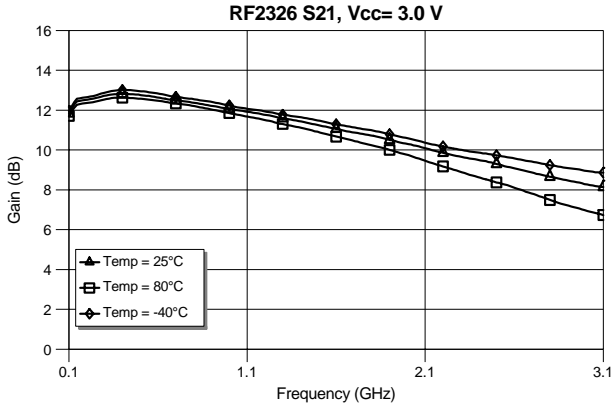


RF2326 S11, Vcc = 2.7 V, Temp = 25°C

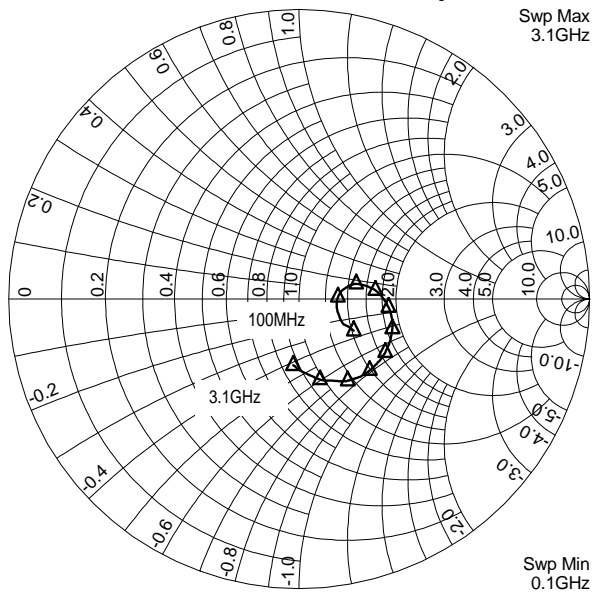


RF2326 S22, Vcc = 2.7 V, Temp= 25°C

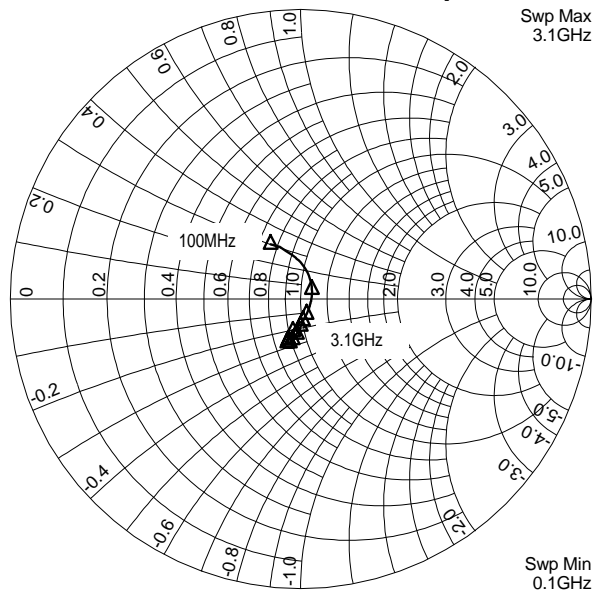


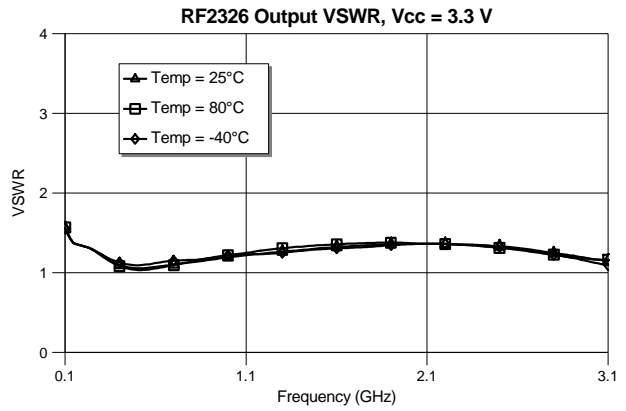
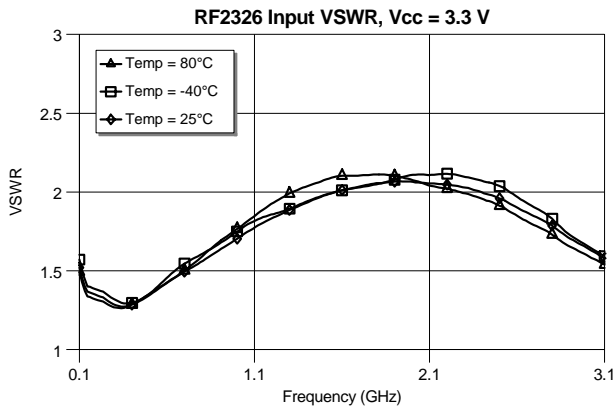
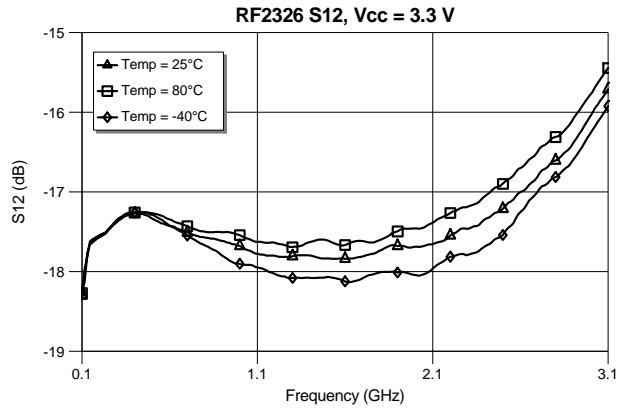
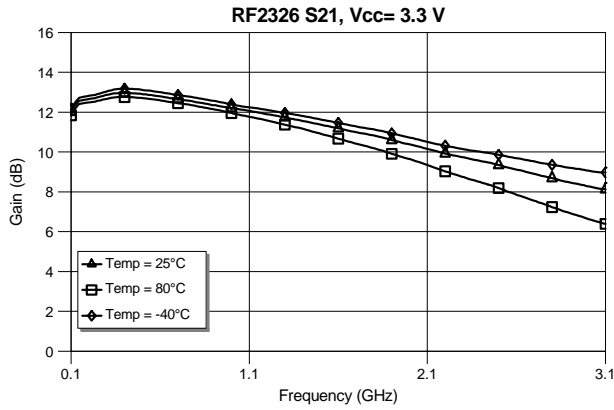


RF2326 S11, Vcc = 3.0 V, Temp = 25°C

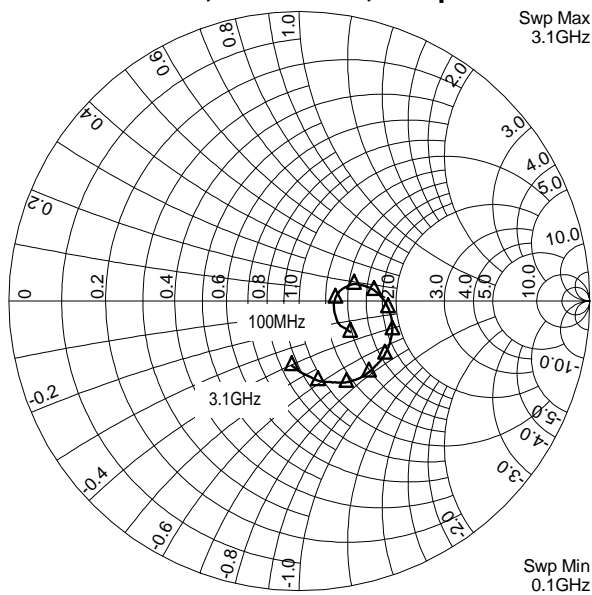


RF2326 S22, Vcc = 3.0 V, Temp= 25°C





RF2326 S11, Vcc = 3.3 V, Temp = 25°C



RF2326 S22, Vcc = 3.3 V, Temp= 25°C

