

# BUK9107-40ATC

TrenchPLUS logic level FET

Rev. 03 — 22 January 2002

Product data

## 1. Description

N-channel enhancement mode field-effect power transistor in a plastic package using TrenchMOS™ technology, featuring very low on-state resistance and TrenchPLUS diodes for clamping, ElectroStatic Discharge (ESD) protection and temperature sensing.

Product availability:

BUK9107-40ATC in SOT426 (D<sup>2</sup>-PAK).

## 2. Features

- Typical on-state resistance 5.8 mΩ
- Q101 compliant
- ESD and overvoltage protection
- Monolithically integrated temperature sensor for overload protection.

## 3. Applications

- Automotive and power switching:
  - ◆ 12 V and 24 V high power motor drives (e.g. Electrical Power Assisted Steering (EPAS))
  - ◆ Protected drive for lamps.

## 4. Pinning information

Table 1: Pinning - SOT426, simplified outline and symbol

Pin	Description	Simplified outline	Symbol
1	gate (g)		
2	anode (a)		
3	drain (d)		
4	cathode (k)		
5	source (s)		
mb	mounting base; connected to drain (d)		

SOT426 (D<sup>2</sup>-PAK)



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## 5. Quick reference data

**Table 2: Quick reference data**

Symbol	Parameter	Conditions	Typ	Max	Unit
$V_{DSR(CL)}$	drain-source clamping voltage	$T_j = 25\text{ °C}; I_{GS(CL)} = -2\text{ mA}; I_D = 1\text{ A}$	50	-	V
$I_D$	drain current (DC)	$T_{mb} = 25\text{ °C}; V_{GS} = 5\text{ V}$	-	140	A
$P_{tot}$	total power dissipation	$T_{mb} = 25\text{ °C}$	-	272	W
$T_j$	junction temperature		-	175	°C
$R_{DSon}$	drain-source on-state resistance	$T_j = 25\text{ °C}; V_{GS} = 5\text{ V}; I_D = 50\text{ A}$	5.8	7	mΩ
		$T_j = 25\text{ °C}; V_{GS} = 4.5\text{ V}; I_D = 50\text{ A}$	6	7.7	mΩ
		$T_j = 25\text{ °C}; V_{GS} = 10\text{ V}; I_D = 50\text{ A}$	5.2	6.2	mΩ
$V_F$	temperature sense diode forward voltage	$T_j = 25\text{ °C}; I_F = 250\text{ μA}$	658	668	mV
$S_F$	temperature sense diode temperature coefficient	$-55\text{ °C} < T_j < +175\text{ °C}; I_F = 250\text{ μA}$	-1.54	-1.68	mV/K

## 6. Limiting values

**Table 3: Limiting values**

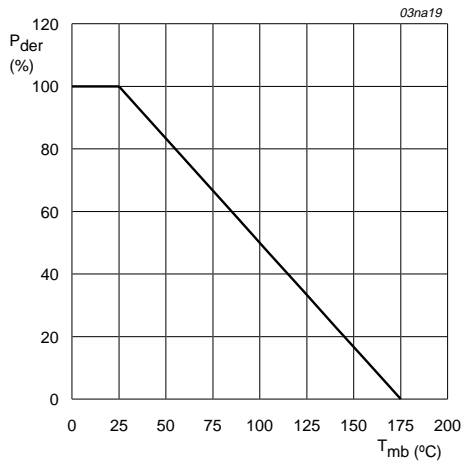
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{DS}$	drain-source voltage (DC)		[1] -	40	V
$V_{DGS}$	drain-gate voltage (DC)	$I_{DG} = 250 \mu A$	[1] -	40	V
$V_{GS}$	gate-source voltage (DC)		[1] -	$\pm 15$	V
$I_D$	drain current (DC)	$T_{mb} = 25 \text{ }^\circ\text{C}$ ; $V_{GS} = 5 \text{ V}$ ; Figure 2 and 3	[2] -	140	A
			[3] -	75	A
		$T_{mb} = 100 \text{ }^\circ\text{C}$ ; $V_{GS} = 5 \text{ V}$ ; Figure 2	[3] -	75	A
$I_{DM}$	peak drain current	$T_{mb} = 25 \text{ }^\circ\text{C}$ ; pulsed; $t_p \leq 10 \mu s$ ; Figure 3	-	560	A
$P_{tot}$	total power dissipation	$T_{mb} = 25 \text{ }^\circ\text{C}$ ; Figure 1	-	272	W
$I_{DG(CL)}$	drain-gate clamping current	$t_p = 5 \text{ ms}$ ; $\delta = 0.01$	-	50	mA
$I_{GS(CL)}$	gate-source clamping current	continuous	-	10	mA
		$t_p = 5 \text{ ms}$ ; $\delta = 0.01$	-	50	mA
$V_{isol(FET-TSD)}$	FET to temperature sense diode isolation voltage		-	$\pm 100$	V
$T_{stg}$	storage temperature		-55	+175	$^\circ\text{C}$
$T_j$	junction temperature		-55	+175	$^\circ\text{C}$
<b>Source-drain diode</b>					
$I_{DR}$	reverse drain current (DC)	$T_{mb} = 25 \text{ }^\circ\text{C}$	[2] -	140	A
			[3] -	75	A
$I_{DRM}$	pulsed reverse drain current	$T_{mb} = 25 \text{ }^\circ\text{C}$ ; pulsed; $t_p \leq 10 \mu s$	-	560	A
<b>Clamping</b>					
$E_{DS(CL)S}$	non-repetitive drain-source clamping energy	unclamped inductive load; $I_D = 75 \text{ A}$ ; $V_{DS} \leq 40 \text{ V}$ ; $V_{GS} = 5 \text{ V}$ ; $R_{GS} = 10 \text{ k}\Omega$ ; starting $T_j = 25 \text{ }^\circ\text{C}$	-	1.4	J
<b>Electrostatic discharge</b>					
$V_{esd}$	electrostatic discharge voltage; pins 1, 3, 5	Human Body Model; $C = 100 \text{ pF}$ ; $R = 1.5 \text{ k}\Omega$	-	6	kV

[1] Voltage is limited by clamping

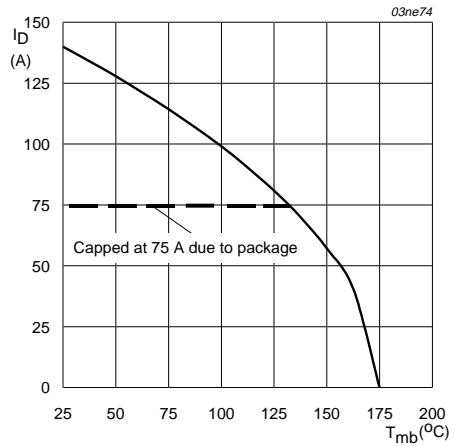
[2] Current is limited by power dissipation chip rating

[3] Continuous current is limited by package.



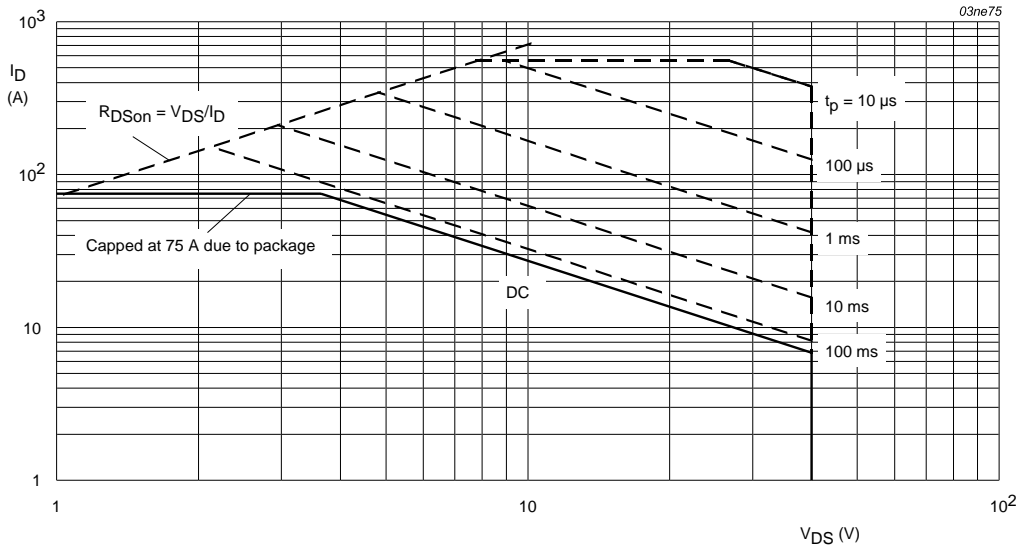
$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100\%$$

Fig 1. Normalized total power dissipation as a function of mounting base temperature.



$V_{GS} \geq 5\text{ V}$

Fig 2. Continuous drain current as a function of mounting base temperature.



$T_{mb} = 25^{\circ}C$ ;  $I_{DM}$  single pulse.

Fig 3. Safe operating area; continuous and peak drain currents as a function of drain-source voltage.

## 7. Thermal characteristics

Table 4: Thermal characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	mounted on printed circuit board; minimum footprint; SOT426 package	-	-	50	K/W
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	Figure 4	-	-	0.55	K/W

### 7.1 Transient thermal impedance

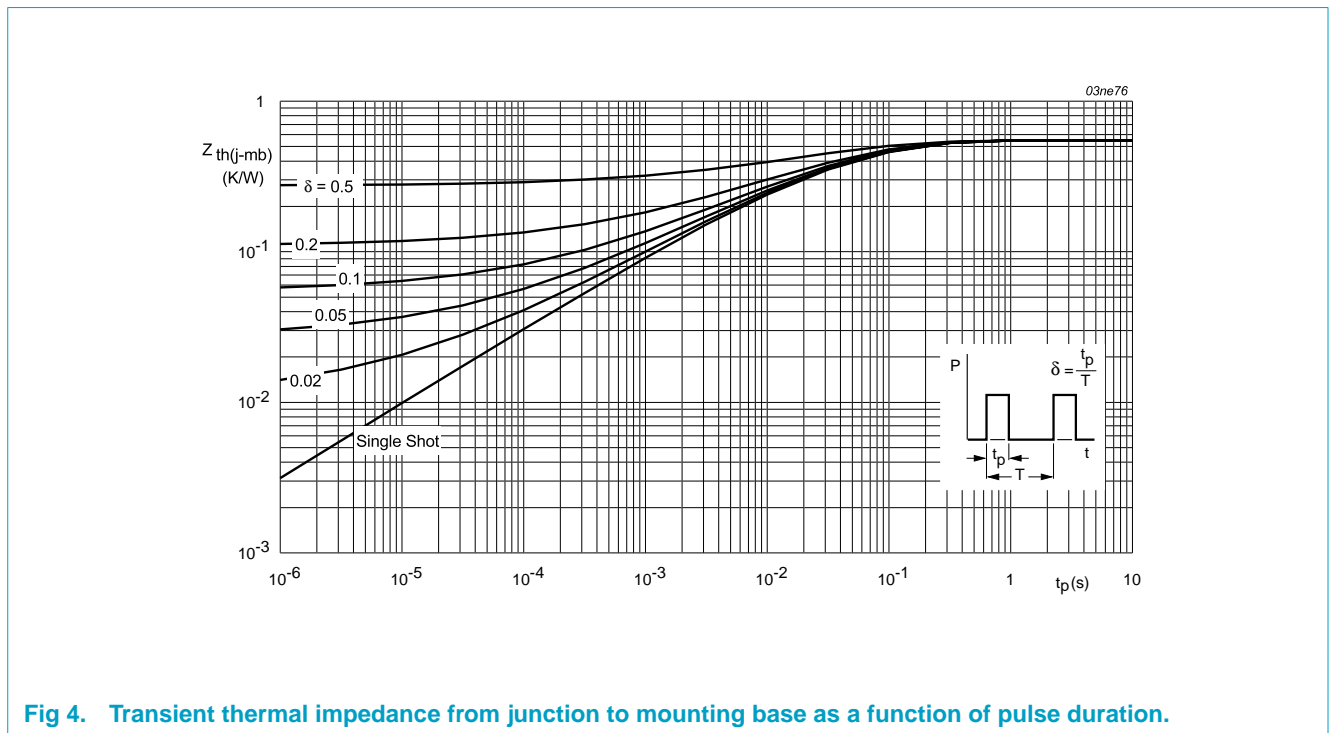


Fig 4. Transient thermal impedance from junction to mounting base as a function of pulse duration.

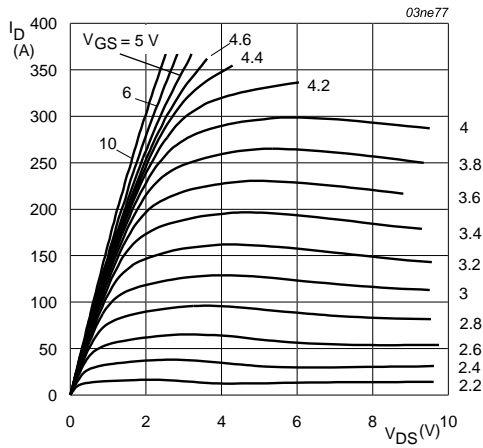
## 8. Characteristics

**Table 5: Characteristics**
 $T_j = 25\text{ °C}$  unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Static characteristics</b>						
$V_{(BR)DG}$	drain-gate zener breakdown voltage	$I_D = 0.25\text{ mA}$ ; $V_{GS} = 0\text{ V}$ $T_j = 25\text{ °C}$	40	-	-	V
		$T_j = -55\text{ °C}$	40	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$I_D = 1\text{ mA}$ ; $V_{DS} = V_{GS}$ ; Figure 9 $T_j = 25\text{ °C}$	1	1.5	2	V
		$T_j = 175\text{ °C}$	0.5	-	-	V
		$T_j = -55\text{ °C}$	-	-	2.3	V
$I_{DSS}$	drain-source leakage current	$V_{DS} = 40\text{ V}$ ; $V_{GS} = 0\text{ V}$ $T_j = 25\text{ °C}$	-	0.1	100	$\mu\text{A}$
		$T_j = 175\text{ °C}$	-	-	250	$\mu\text{A}$
$V_{(BR)GSS}$	gate-source breakdown voltage	$I_G = \pm 1\text{ mA}$ ; $-55\text{ °C} < T_j < 175\text{ °C}$	12	15	-	V
$I_{GSS}$	gate-source leakage current	$V_{GS} = \pm 5\text{ V}$ ; $V_{DS} = 0\text{ V}$	-	5	1000	nA
$R_{DS(on)}$	drain-source on-state resistance	$V_{GS} = 5\text{ V}$ ; $I_D = 50\text{ A}$ ; Figure 7 and 8 $T_j = 25\text{ °C}$	-	5.8	7	m $\Omega$
		$T_j = 175\text{ °C}$	-	-	14	m $\Omega$
		$V_{GS} = 4.5\text{ V}$ ; $I_D = 50\text{ A}$	-	6	7.7	m $\Omega$
		$V_{GS} = 10\text{ V}$ ; $I_D = 50\text{ A}$	-	5.2	6.2	m $\Omega$
$V_F$	temperature sense diode forward voltage	$I_F = 250\text{ }\mu\text{A}$	648	658	668	mV
$S_F$	temperature sense diode temperature coefficient	$I_F = 250\text{ }\mu\text{A}$ ; $-55\text{ °C} < T_j < 175\text{ °C}$	-1.4	-1.54	-1.68	mV/K
$V_{hys}$	temperature sense diode forward voltage hysteresis	$125\text{ }\mu\text{A} < I_F < 250\text{ }\mu\text{A}$	25	32	50	mV
<b>Dynamic characteristics</b>						
$C_{iss}$	input capacitance	$V_{GS} = 0\text{ V}$ ; $V_{DS} = 25\text{ V}$ ; $f = 1\text{ MHz}$ ; Figure 12	-	5836	-	pF
$C_{oss}$	output capacitance		-	958	-	pF
$C_{rss}$	reverse transfer capacitance		-	595	-	pF
$t_{d(on)}$	turn-on delay time	$V_{DD} = 30\text{ V}$ ; $R_L = 1.2\text{ }\Omega$ ;	-	3	-	$\mu\text{s}$
$t_r$	rise time	$V_{GS} = 5\text{ V}$ ; $R_G = 1\text{ k}\Omega$	-	10	-	$\mu\text{s}$
$t_{d(off)}$	turn-off delay time		-	17	-	$\mu\text{s}$
$t_f$	fall time		-	11	-	$\mu\text{s}$
$L_d$	internal drain inductance	measured from upper edge of drain mounting base to centre of die	-	2.5	-	nH

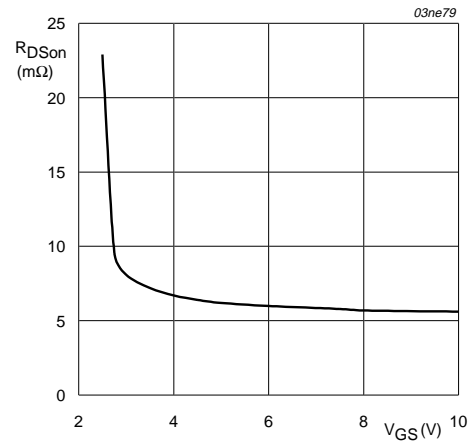
**Table 5: Characteristics...continued***T<sub>j</sub> = 25 °C unless otherwise specified.*

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
L <sub>S</sub>	internal source inductance	measured from source lead to source bond pad	-	7.5	-	nH
<b>Source-drain diode</b>						
V <sub>SD</sub>	source-drain (diode forward) voltage	I <sub>S</sub> = 25 A; V <sub>GS</sub> = 0 V; Figure 19	-	0.85	1.2	V
t <sub>rr</sub>	reverse recovery time	I <sub>S</sub> = 20 A; dI <sub>S</sub> /dt = -100 A/μs	-	85	-	ns
Q <sub>r</sub>	recovered charge	V <sub>GS</sub> = -10 V; V <sub>DS</sub> = 30 V	-	250	-	nC



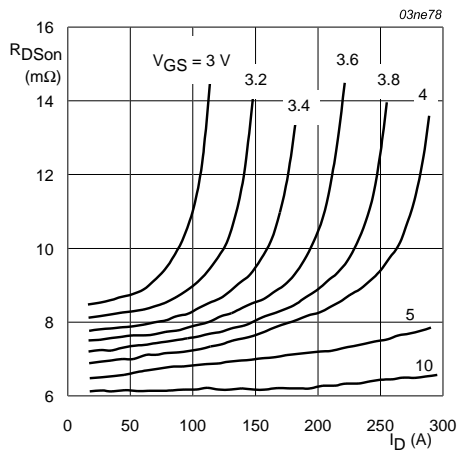
$T_j = 25\text{ }^\circ\text{C}; t_p = 300\text{ }\mu\text{s}$

**Fig 5. Output characteristics: drain current as a function of drain-source voltage; typical values.**



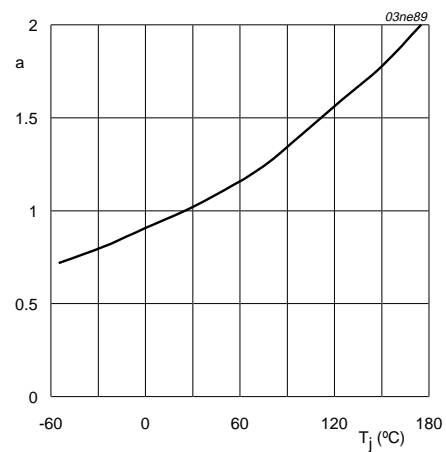
$T_j = 25\text{ }^\circ\text{C}; I_D = 50\text{ A}$

**Fig 6. Drain-source on-state resistance as a function of gate-source voltage; typical values.**



$T_j = 25\text{ }^\circ\text{C}; t_p = 300\text{ }\mu\text{s}$

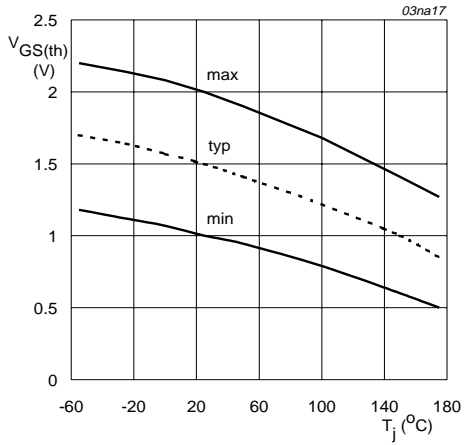
**Fig 7. Drain-source on-state resistance as a function of drain current; typical values.**



$$a = \frac{R_{DSon}}{R_{DSon}(25^\circ\text{C})}$$

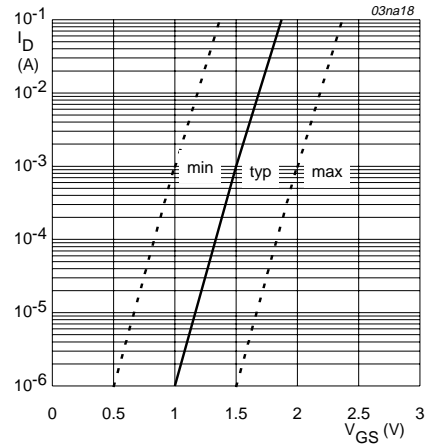
**Fig 8. Normalized drain-source on-state resistance factor as a function of junction temperature.**





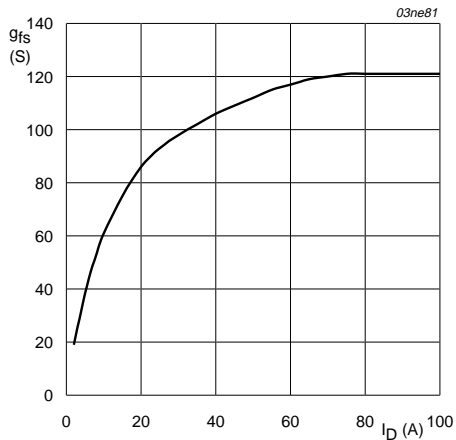
$I_D = 1 \text{ mA}; V_{DS} = V_{GS}$

**Fig 9. Gate-source threshold voltage as a function of junction temperature.**



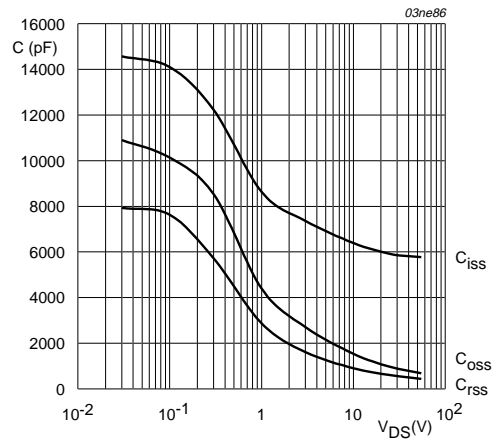
$T_j = 25 \text{ }^{\circ}C; V_{DS} = V_{GS}$

**Fig 10. Sub-threshold drain current as a function of gate-source voltage.**



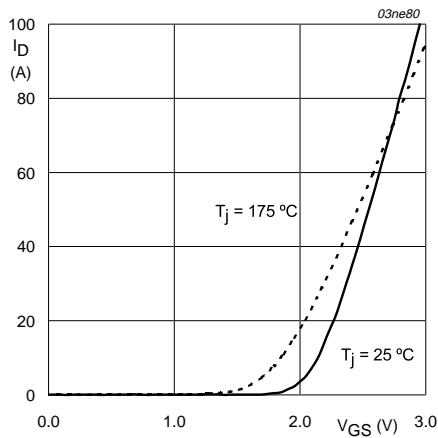
$T_j = 25 \text{ }^{\circ}C; V_{DS} = 25 \text{ V}$

**Fig 11. Forward transconductance as a function of drain current; typical values.**



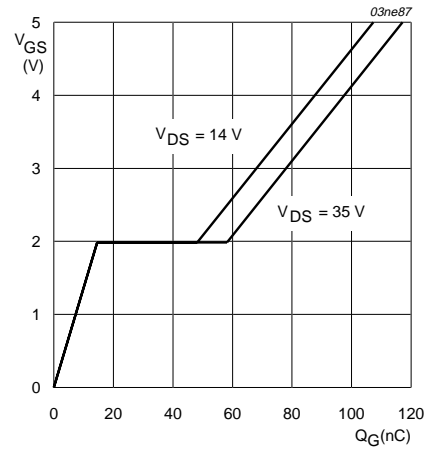
$V_{GS} = 0 \text{ V}; f = 1 \text{ MHz}$

**Fig 12. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values.**



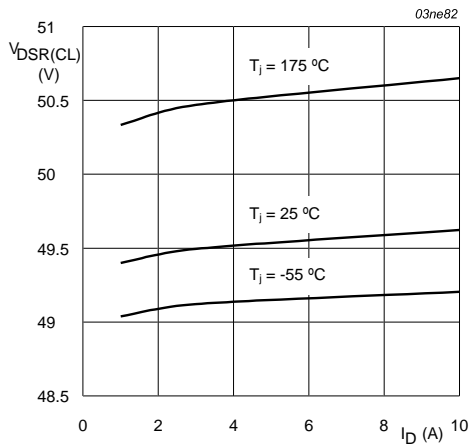
$V_{DS} = 25 \text{ V}$

**Fig 13. Transfer characteristics: drain current as a function of gate-source voltage; typical values.**



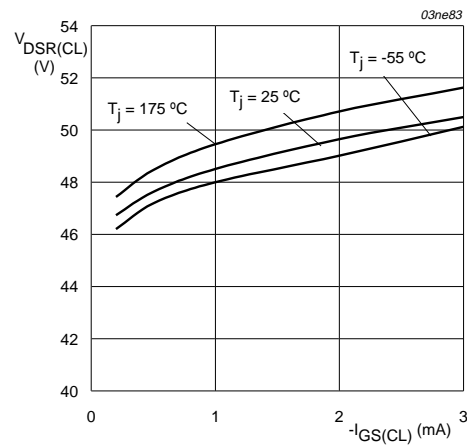
$T_j = 25 \text{ }^\circ\text{C}; I_D = 50 \text{ A}$

**Fig 14. Gate-source voltage as a function of turn-on gate charge; typical values.**



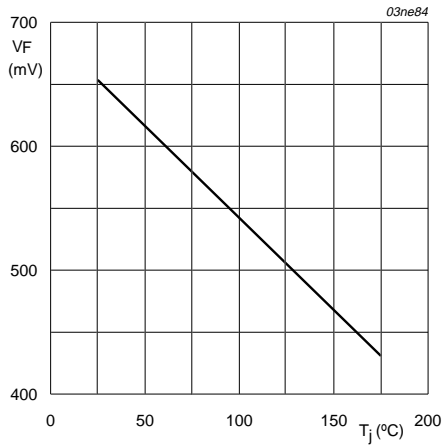
$I_{GS(CL)} = -2 \text{ mA}$

**Fig 15. Drain-source clamping voltage as a function of drain current; typical values.**



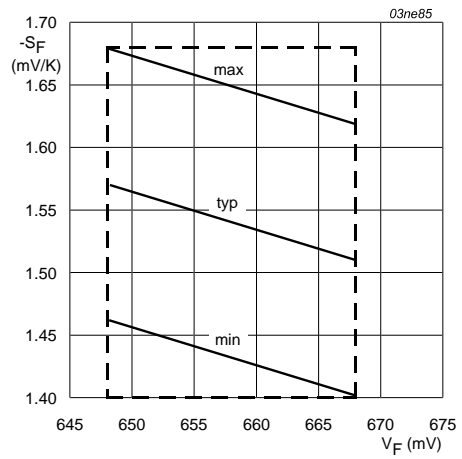
$I_D = 10 \text{ A}$

**Fig 16. Drain-source clamping voltage as a function of gate current; typical values.**



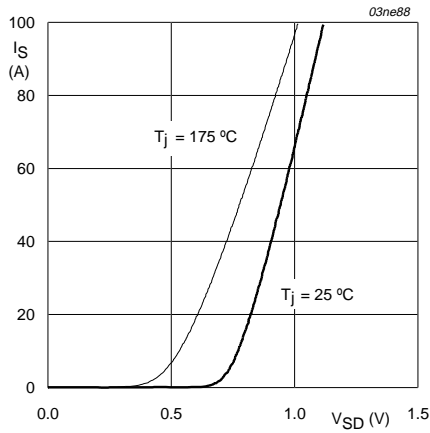
$I_F = 250 \mu A$

**Fig 17. Forward voltage of temperature sense diode as a function of junction temperature; typical values.**



$V_F$  at  $T_j = 25 \text{ }^\circ\text{C}$ ;  $I_F = 250 \mu A$

**Fig 18. Temperature coefficient of temperature sense diode as a function of forward voltage; typical values.**



$V_{GS} = 0 V$

**Fig 19. Reverse diode current as a function of reverse diode voltage; typical values.**

**9. Package outline**

Plastic single-ended surface mounted package (Philips version of D<sup>2</sup>-PAK); 5 leads  
(one lead cropped)

SOT426

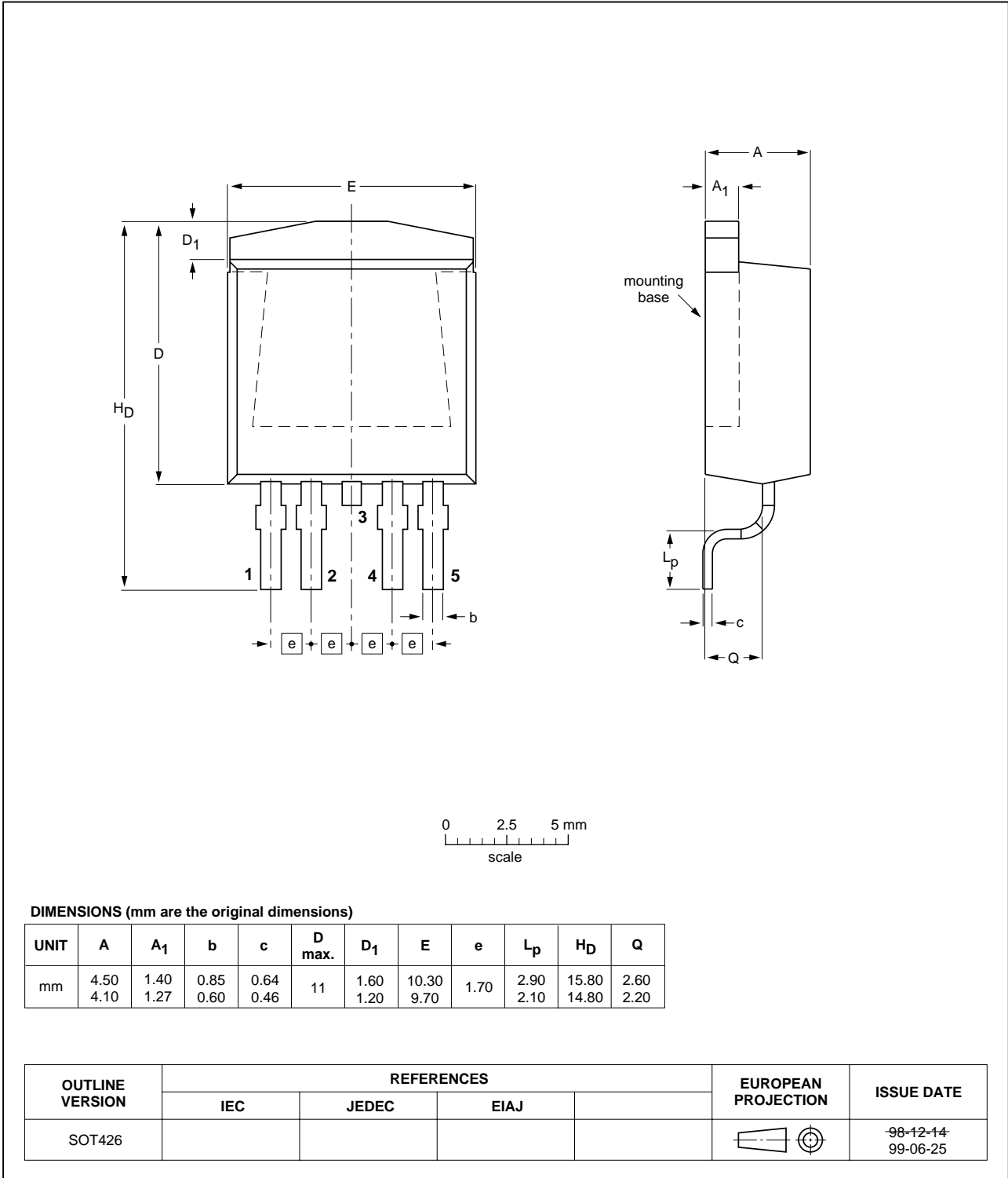
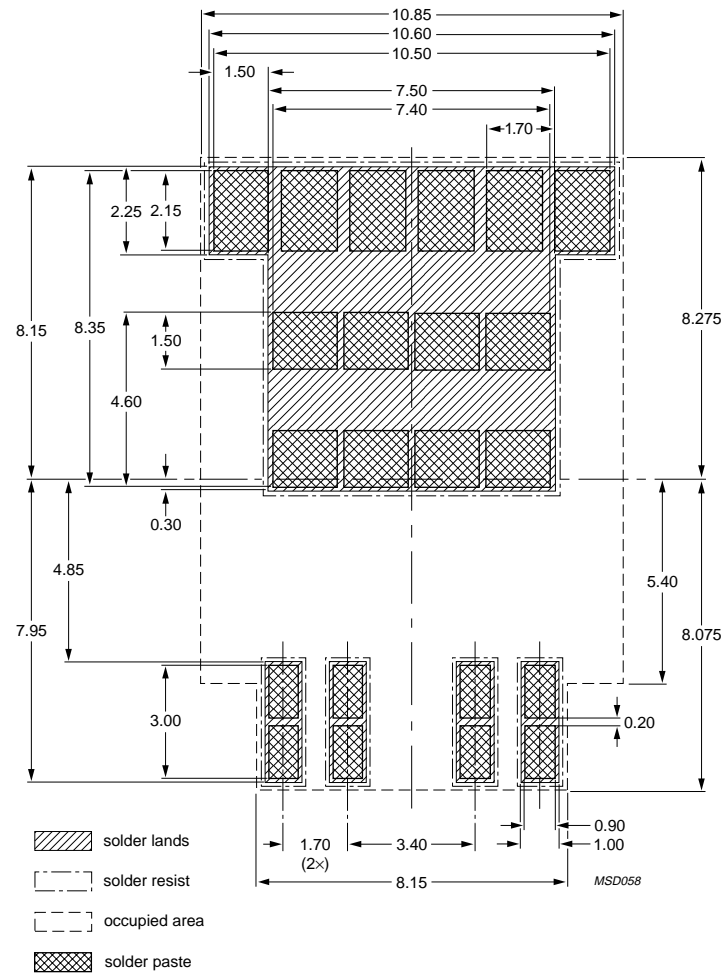


Fig 20. SOT426.

10. Soldering



Dimensions in mm.

Fig 21. Reflow soldering footprint for SOT426.

## 11. Revision history

Table 6: Revision history

Rev	Date	CPCN	Description
03	20020122	-	Product data; third version (9397 750 08724); supersedes second version of 20010829
02	20010829	-	Product data; second version (9397 750 08709); supersedes initial version of 20010814 <ul style="list-style-type: none"><li>• Units of symbol '<math>R_{DSon}</math>' changed from '<math>\mu\Omega</math>' to '<math>m\Omega</math>' in <a href="#">Table 2</a></li><li>• Units of timing parameters changed from 'ns' to '<math>\mu s</math>' in <a href="#">Table 5</a></li><li>• Values of timing parameters changed in <a href="#">Table 5</a></li></ul>
01	20010814	-	Product data; initial version (9397 750 08319)

## 12. Data sheet status

Data sheet status <sup>[1]</sup>	Product status <sup>[2]</sup>	Definition
Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
Product data	Production	This data sheet contains data from the product specification. Philips Semiconductors reserves the right to make changes at any time in order to improve the design, manufacturing and supply. Changes will be communicated according to the Customer Product/Process Change Notification (CPCN) procedure SNW-SQ-650A.

[1] Please consult the most recently issued data sheet before initiating or completing a design.

[2] The product status of the device(s) described in this data sheet may have changed since this data sheet was published. The latest information is available on the Internet at URL <http://www.semiconductors.philips.com>.

## 13. Definitions

**Short-form specification** — The data in a short-form specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.

**Limiting values definition** — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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