



DMN2114SN

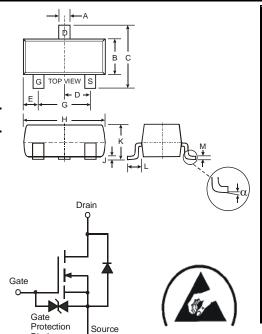
N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

Features

- Low On-Resistance
- Ideal for Notebook Computer, Portable Phone, PCMCIA Cards, and Battery Power Circuits
- Lead Free By Design/RoHS Compliant (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability
- **ESD Protected Gate**
- "Green" Device (Note 3)

Mechanical Data

- Case: SC-59
- Case Material Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Marking Information: See Page 4
- Ordering & Date Code Information: See Page 4
- Weight: 0.008 grams (approximate)



SC-59									
Dim	Min	Max							
Α	0.30	0.50							
В	1.40	1.80							
С	2.50	3.00							
D	0.85	1.05							
E	0.30	0.70							
G	1.70	2.10							
Н	2.70	3.10							
J	_	0.10							
K	1.00	1.40							
L	0.55	0.70							
М	0.10	0.35							
α	0°	8°							
All Dimensions in mm									

ESD protected

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		V _{DSS}	20	V	
Gate-Source Voltage	Continuous	V _{GSS}	±12	V	
Drain Current	Continuous Pulsed	I _D	1.2 4.0	А	
Total Power Dissipation		P_d	500	mW	
Thermal Resistance, Junction to Ambient		$R_{ heta JA}$	250	°C /W	
Operating and Storage Temperature Range		T _i , T _{STG}	-55 to +150	°C	

Diode

EQUIVALENT CIRCUIT

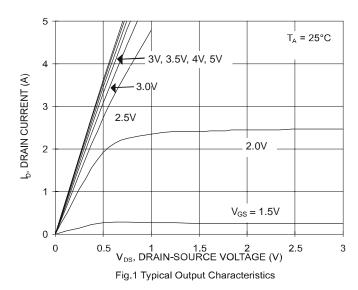
Notes:

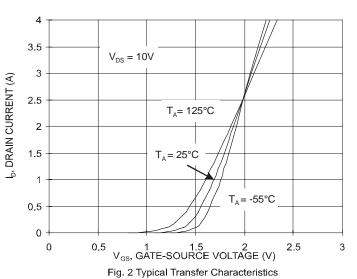
- Pulse width $\leq 300 \mu S$, duty cycle $\leq 2\%$.
- No purposefully added lead.
- Diodes Inc's "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.



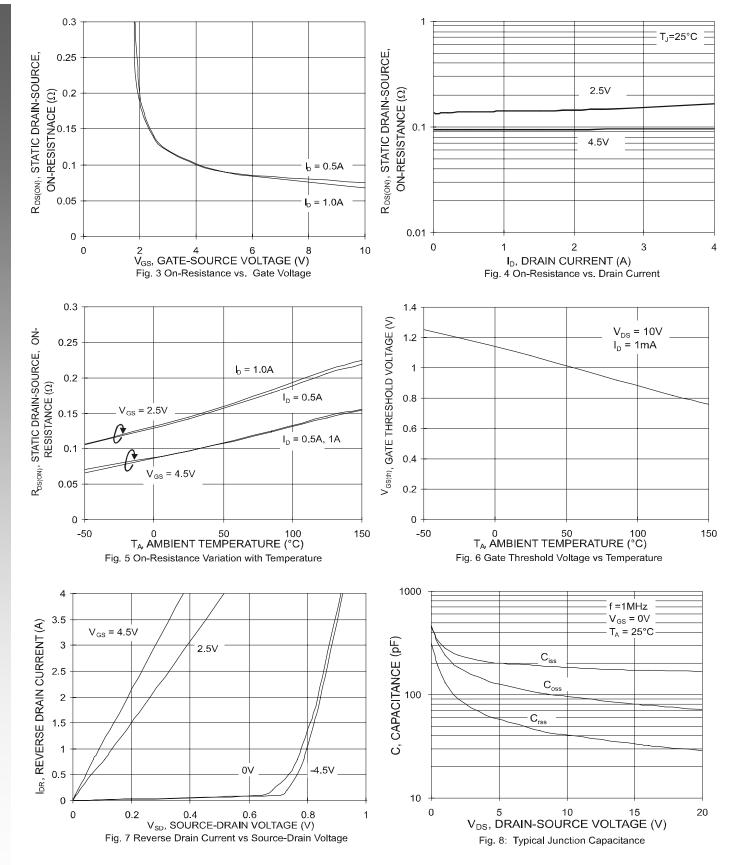
Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic			Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 1)									
Drain-Source Breakdown Voltage		BV _{DSS}	20		_	V	$V_{GS} = 0V, I_D = 250\mu A$		
Zero Gate Voltage Drain Current	@ T _j = 25°C	I _{DSS}	_	_	10	μА	V _{DS} = 24V, V _{GS} = 0V		
Gate-Body Leakage		I _{GSS}	_		±10	μА	$V_{GS} = \pm 12V, V_{DS} = 0V$		
ON CHARACTERISTICS (Note 1)		•		•	•	•			
Gate Threshold Voltage		V _{GS(th)}	0.7	_	1.40	V	$V_{DS} = 10V, I_{D} = 1.0mA$		
Static Drain-Source On-Resistance		R _{DS (ON)}		_	0.100 0.160	Ω	$V_{GS} = 4.5V, I_D = 0.5A$ $V_{GS} = 2.5V, I_D = 0.5A$		
Forward Transfer Admittance		Y _{fs}	_	3.3	_	S	$V_{DS} = 10V, I_{D} = 0.5A$		
Diode Forward Voltage		V _{SD}	_	0.8	1.1	V	$V_{GS} = 0V, I_S = 1.0A$		
DYNAMIC CHARACTERISTICS		•		•	•	•			
Input Capacitance		C _{iss}	_	180		pF			
Output Capacitance		Coss	_	120	_	pF	$V_{DS} = 10V, V_{GS} = 0V,$ f = 1.0MHz		
Reverse Transfer Capacitance	C _{rss}	_	45		pF] - 1.0111.12			
SWITCHING CHARACTERISTICS					_				
Turn-On Delay Time		t _{D(ON)}		10	_	ns			
Turn-Off Delay Time Turn-On Rise Time		t _{D(OFF)}		50	_	ns	$V_{DD} = 10V, I_D = 0.5A,$		
		t _r		15		ns	$V_{GS} = 5.0V, R_{GEN} = 50\Omega$		
Turn-Off Fall Time		t _f		45		ns			









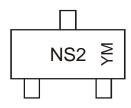


Ordering Information (Note 4)

Device	Packaging	Shipping			
DMN2114SN-7	SC-59	3000/Tape & Reel			

Notes: 4. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



NS2 = Product Type Marking Code YM = Date Code Marking Y = Year ex: T = 2006 M = Month ex: 9 = September

Date Code Key

Year	2006		2007		2008	2009		2010		2011	2012		
Code	Т		U		V W		٧	X		Υ		Z	
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec	
Code	1	2	3	4	5	6	7	8	9	0	N	D	

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