

SRC1202K

NPN Silicon Transistor

Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

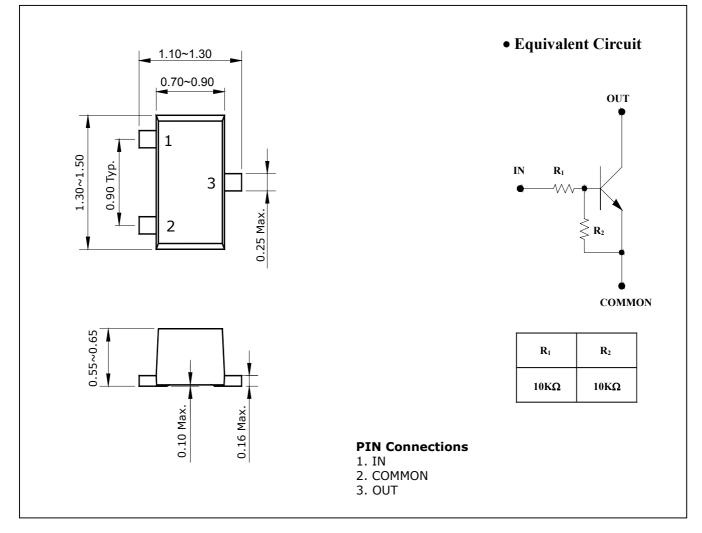
- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

Ordering Information

Type NO.	Marking	Package Code		
SRC1202K	R2	SOT-623F		

Outline Dimensions

unit : mm



SRC1202K

Absolute Maximum Ratings

Absolute Maximum Ratings		(Ta=25°C)		
Characteristic	Symbol	Rating	Unit	
Output voltage	Vo	50	V	
Input voltage	VI	30,-10	V	
Output current	I _O	100	mA	
Power dissipation	P _D	100	mW	
Junction temperature	Tյ	150	°C	
Storage temperature range	T _{stg}	-55 ~ 150	°C	

Electrical Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I _{O(OFF)}	V ₀ =50V, V _I =0	-	-	500	nA
DC current gain	GI	V ₀ =5V, I ₀ =10mA	50	80	-	-
Output voltage	V _{O(ON)}	$I_0=10$ mA, $I_I=0.5$ mA	-	0.1	0.3	V
Input voltage (ON)	V _{I(ON)}	V ₀ =0.2V, I ₀ =5mA	-	1.8	2.4	V
Input voltage (OFF)	V _{I(OFF)}	V ₀ =5V, I ₀ =0.1mA	1.0	1.2	-	V
Transition frequency	f _T *	$V_0=10V$, $I_0=5mA$, f=1MHz	-	200	-	MHz
Input current	II	V _I =5V, I _O =0	-	-	0.88	mA
Input resistor (Input to base)	R ₁	-	7	10	13	KΩ
Input resistor (Base to common)	R ₂	-	7	10	13	KΩ

* : Characteristic of transistor only

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Electrical Characteristic Curves

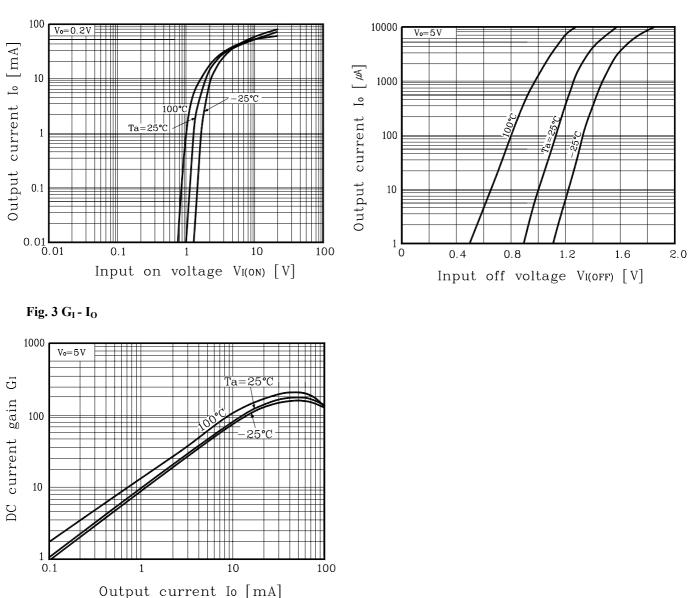


Fig. 1 I₀ - V_{I(ON)}

Fig. 2 I_O - V_{I(OFF)}

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