DISCRETE SEMICONDUCTORS

DATA SHEET

BF763 NPN 2 GHz wideband transistor

Product specification
File under Discrete Semiconductors, SC14

September 1995





NPN 2 GHz wideband transistor

BF763

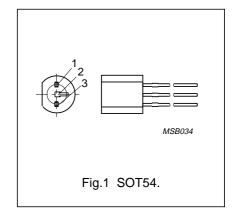
DESCRIPTION

NPN transistor in a plastic SOT54 (TO-92 variant) envelope.

It is primarily intended for use in RF amplifiers and oscillators.

PINNING

PIN DESCRIPTION					
Code: F763					
1	emitter				
2	base				
3	collector				



QUICK REFERENCE DATA

SYMBOL	PARAMETER	PARAMETER CONDITIONS				UNIT
V _{(BR)CEO}	collector-emitter breakdown voltage	open base	15	_	_	V
I _C	DC collector current		-	_	25	mA
P _{tot}	total power dissipation	up to T _{amb} = 60 °C	-	_	360	mW
h _{FE}	DC current gain	$I_C = 5 \text{ mA}; V_{CE} = 10 \text{ V}; T_j = 25 ^{\circ}\text{C}$	25	_	250	
f _T	transition frequency	$I_C = 5 \text{ mA}; V_{CE} = 10 \text{ V}; f = 100 \text{ MHz}$	_	1.8	_	GHz

LIMITING VALUES

In accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	PARAMETER CONDITIONS		MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	15	V
V _{CEO}	collector-emitter voltage	open base	_	25	V
I _C	DC collector current		_	25	mA
P _{tot}	total power dissipation	up to T _{amb} = 60 °C	_	360	mW
T _{stg}	storage temperature		-65	150	°C
T _i	junction temperature		_	150	°C

THERMAL RESISTANCE

SYMBOL	PARAMETER CONDITIONS		THERMAL RESISTANCE
R _{th j-a}	thermal resistance from junction to ambient	in free air	250 K/W

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CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified.

SYMBOL	PARAMETER	PARAMETER CONDITIONS				UNIT
V _{(BR)CEO}	collector-emitter breakdown voltage	age $I_C = 1 \text{ mA}; I_B = 0$		_	_	V
V _{(BR)CBO}	collector-base breakdown voltage	$I_C = 10 \mu\text{A}; I_E = 0$	25	_	_	V
V _{CE sat}	collector-emitter saturation voltage	I _C = 10 mA; I _B = 1 mA	_	_	0.5	٧
I _{CBO}	collector cut-off current	or cut-off current $I_E = 0$; $V_{CB} = 10 \text{ V}$		_	50	nA
h _{FE}	DC current gain	$I_C = 5 \text{ mA}; V_{CE} = 10 \text{ V}$	25	_	250	
f _T	transition frequency	I _C = 5 mA; V _{CE} = 10 V; f = 100 MHz	_	1.8	_	GHz
F	noise figure	$I_C = 5 \text{ mA}$; $V_{CE} = 10 \text{ V}$; $f = 800 \text{ MHz}$;	_	5.0	_	dB
		$T_{amb} = 25 ^{\circ}C; Z_s = 60 \Omega$				

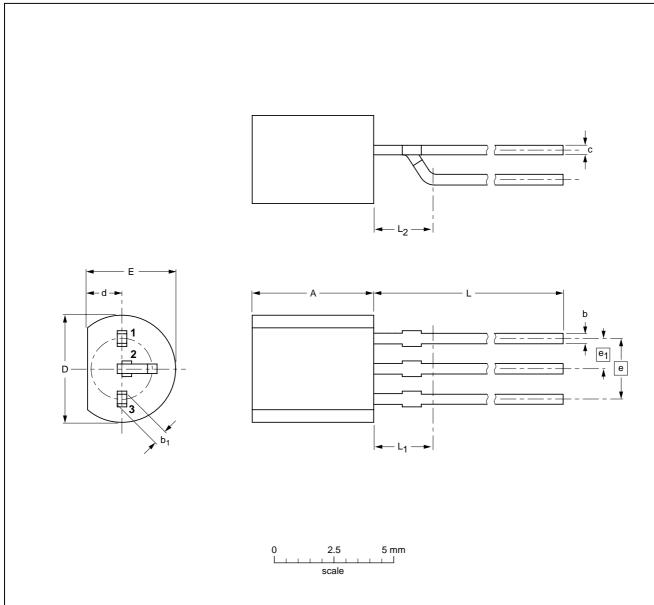
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PACKAGE OUTLINE

Plastic single-ended leaded (through hole) package; 3 leads (on-circle)

SOT54 variant



DIMENSIONS (mm are the original dimensions)

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UNIT	Α	b	b ₁	С	D	d	E	е	e ₁	L	L ₁ ⁽¹⁾ max	L ₂ max
mm	5.2 5.0	0.48 0.40	0.66 0.56	0.45 0.40	4.8 4.4	1.7 1.4	4.2 3.6	2.54	1.27	14.5 12.7	2.5	2.5

Notes

1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

OUTLINE		REFER	ENCES		ISSUE DATE	
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT54 variant		TO-92	SC-43			97-04-14

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DEFINITIONS

Data Sheet Status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification

Application information

Where application information is given, it is advisory and does not form part of the specification.

is not implied. Exposure to limiting values for extended periods may affect device reliability.

LIFE SUPPORT APPLICATIONS

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