



**Advanced Power
Electronics Corp.**

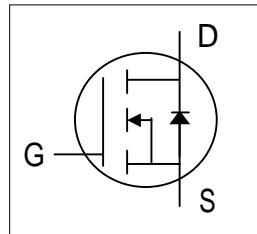
AP03N70F-H

Pb Free Plating Product

N-CHANNEL ENHANCEMENT MODE

POWER MOSFET

- Repetitive Avalanche Rated**
- Fast Switching Speed**
- Simple Drive Requirement**
- RoHS Compliant**

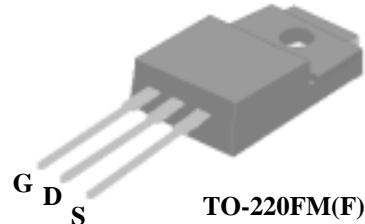


BV_{DSS}	700V
$R_{DS(ON)}$	4.4
I_D	2.5A

Description

AP03N70 series are specially designed as main switching devices for universal 90~265VAC off-line AC/DC converter applications.

TO-220FM type provide high blocking voltage to overcome voltage surge and sag in the toughest power system with the best combination of fast switching,ruggedized design and cost-effectiveness.



The TO-220FM package is universally preferred for all commercial-industrial applications. The device is suited for switch mode power supplies ,DC-AC converters and high current high speed switching circuits.

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	700	V
V_{GS}	Gate-Source Voltage	± 30	V
$I_D @ T_C=25$	Continuous Drain Current, $V_{GS} @ 10V$	2.5	A
$I_D @ T_C=100$	Continuous Drain Current, $V_{GS} @ 10V$	1.6	A
I_{DM}	Pulsed Drain Current ¹	8	A
$P_D @ T_C=25$	Total Power Dissipation	29	W
	Linear Derating Factor	0.23	W/
E_{AS}	Single Pulse Avalanche Energy ²	32	mJ
I_{AR}	Avalanche Current	2.5	A
T_{STG}	Storage Temperature Range	-55 to 150	
T_J	Operating Junction Temperature Range	-55 to 150	

Thermal Data

Symbol	Parameter	Value	Units
R_{thj-c}	Thermal Resistance Junction-case	Max. 4.3	/W
R_{thj-a}	Thermal Resistance Junction-ambient	Max. 65	/W



Electrical Characteristics@ $T_j=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=1\text{mA}$	700	-	-	V
$\text{BV}_{\text{DSS}}/T_j$	Breakdown Voltage Temperature Coefficient	Reference to 25°C , $I_{\text{D}}=1\text{mA}$	-	0.6	-	V/
$R_{\text{DS}(\text{ON})}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=1.6\text{A}$	-	-	4.4	
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}$, $I_{\text{D}}=250\mu\text{A}$	2	-	4	V
g_{fs}	Forward Transconductance	$V_{\text{DS}}=10\text{V}$, $I_{\text{D}}=1.6\text{A}$	-	2	-	S
I_{DSS}	Drain-Source Leakage Current ($T_j=25^\circ\text{C}$)	$V_{\text{DS}}=600\text{V}$, $V_{\text{GS}}=0\text{V}$	-	-	10	μA
	Drain-Source Leakage Current ($T_j=150^\circ\text{C}$)	$V_{\text{DS}}=480\text{V}$, $V_{\text{GS}}=0\text{V}$	-	-	100	μA
I_{GSS}	Gate-Source Leakage	$V_{\text{GS}}=\pm30\text{V}$	-	-	±100	nA
Q_g	Total Gate Charge ³	$I_{\text{D}}=1\text{A}$	-	12	20	nC
Q_{gs}	Gate-Source Charge	$V_{\text{DS}}=480\text{V}$	-	3	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	$V_{\text{GS}}=10\text{V}$	-	4	-	nC
$t_{\text{d}(\text{on})}$	Turn-on Delay Time ³	$V_{\text{DD}}=300\text{V}$	-	8.5	-	ns
t_r	Rise Time	$I_{\text{D}}=2.5\text{A}$	-	6	-	ns
$t_{\text{d}(\text{off})}$	Turn-off Delay Time	$R_G=10\text{ }\Omega$, $V_{\text{GS}}=10\text{V}$	-	19	-	ns
t_f	Fall Time	$R_D=120\text{ }\Omega$	-	8	-	ns
C_{iss}	Input Capacitance	$V_{\text{GS}}=0\text{V}$	-	590	950	pF
C_{oss}	Output Capacitance	$V_{\text{DS}}=25\text{V}$	-	50	-	pF
C_{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	6	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage ³	$I_S=3\text{A}$, $V_{\text{GS}}=0\text{V}$	-	-	1.5	V
t_{rr}	Reverse Recovery Time ²	$I_S=3\text{A}$, $V_{\text{GS}}=0\text{V}$,	-	407	-	ns
Q_{rr}	Reverse Recovery Charge	$dI/dt=100\text{A}/\mu\text{s}$	-	2110	-	nC

Notes:

- 1.Pulse width limited by safe operating area.
- 2.Starting $T_j=25^\circ\text{C}$, $V_{\text{DD}}=50\text{V}$, $L=15\text{mH}$, $R_G=25\text{ }\Omega$, $I_{\text{AS}}=3\text{A}$.
- 3.Pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.

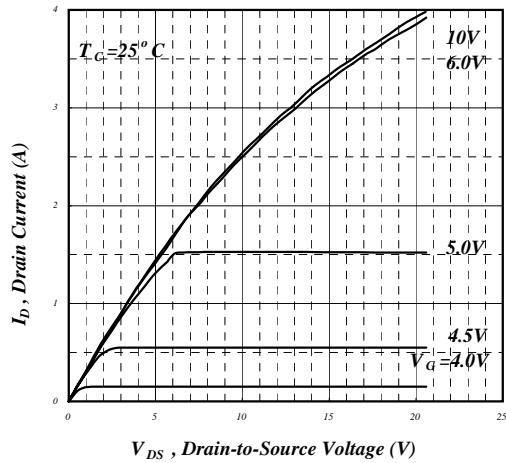


Fig 1. Typical Output Characteristics

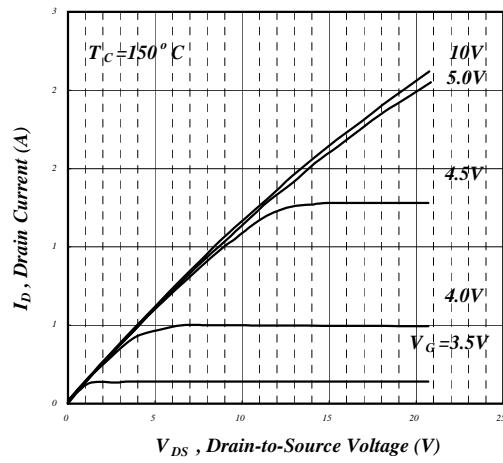


Fig 2. Typical Output Characteristics

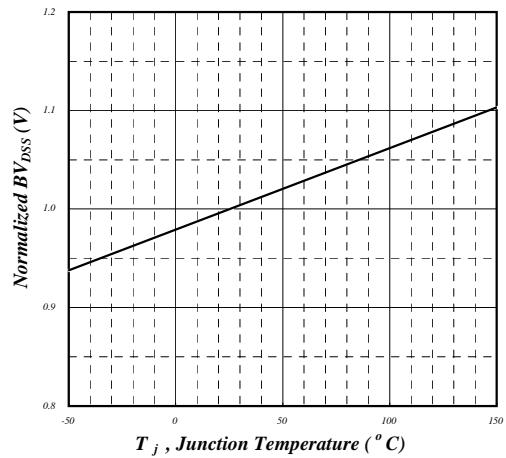
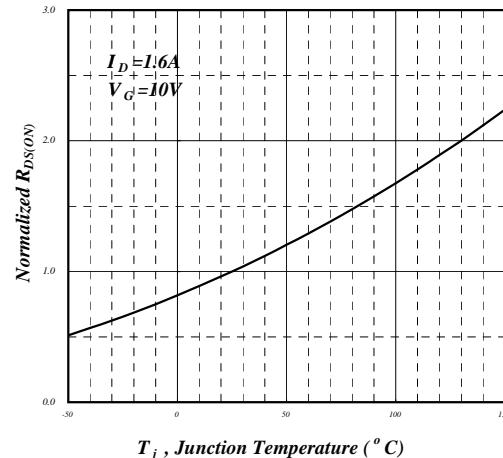
Fig 3. Normalized BV_{DSS} v.s. Junction Temperature

Fig 4. Normalized On-Resistance v.s. Junction Temperature

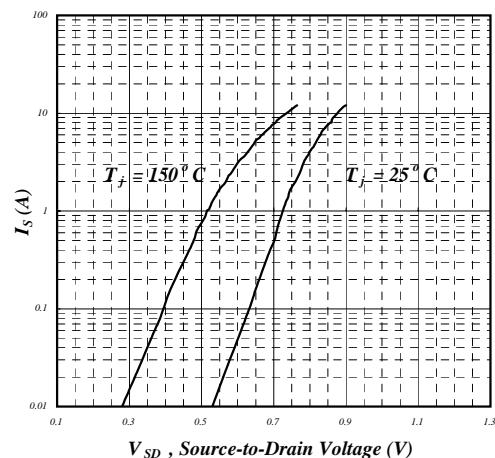


Fig 5. Forward Characteristic of Reverse Diode

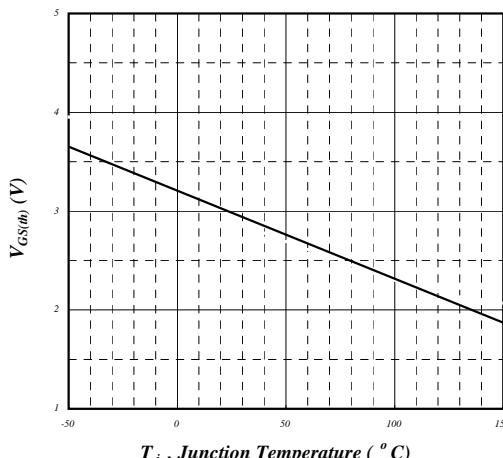


Fig 6. Gate Threshold Voltage v.s. Junction Temperature



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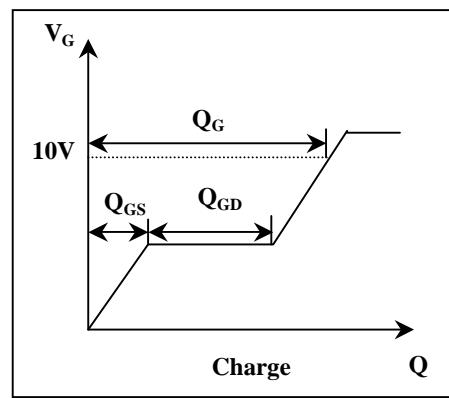
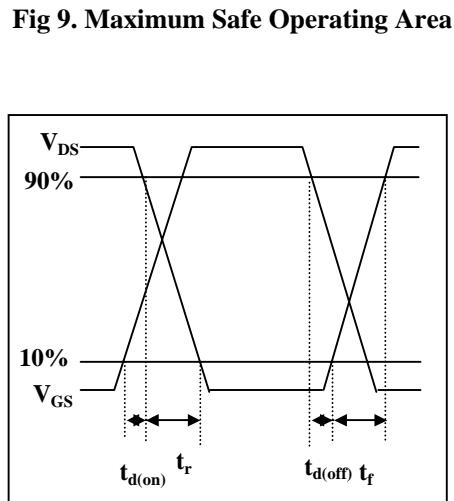
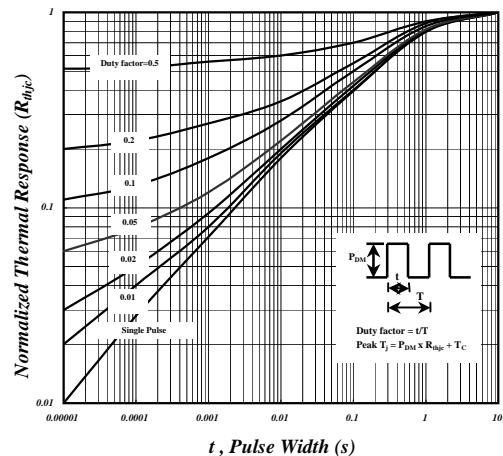
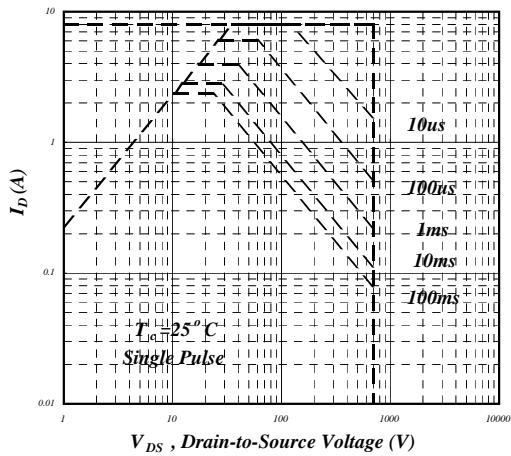
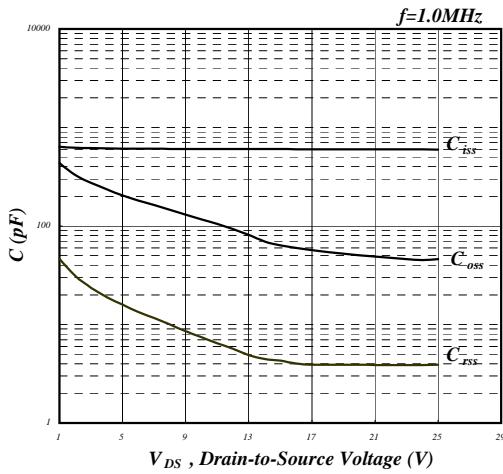
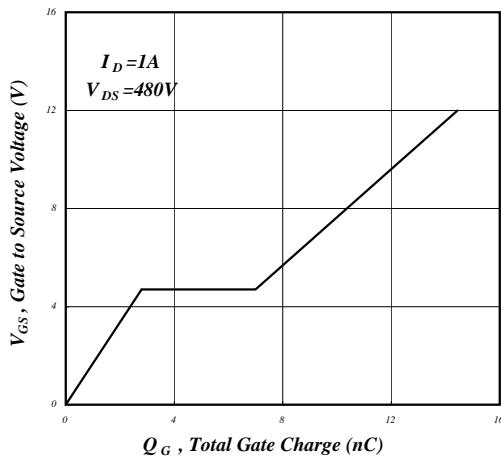


Fig 11. Switching Time Waveform

Fig 12. Gate Charge Waveform