TOSHIBA HN2C10FT

TENTATIVE

TOSHIBA TRANSISTOR SILICON NPN EPITAXIAL PLANAR TYPE

HN2C10FT

VHF~UHF BAND LOW NOISE AMPLIFIER APPLICATIONS

TWO devices are built in to the super-thin and ultra super mini (6pins) package: TU6

MOUNTED DEVICES

	Q1/Q2
Three-pins (SSM) mold products are corresponded	2SC5086

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Collector-Base Voltage	V_{CBO}	20	V
Collector-Emitter Voltage	V _{CEO}	12	V
Emitter-Base Voltage	$V_{ m EBO}$	3	V
Collector Current	$I_{\mathbf{C}}$	80	mA
Base Current	I_{B}	40	mA
Collector Power Dissipation	$P_{\mathbf{C}}$	200	mW
Junction Temperature	T_{j}	125	°C
Storage Temperature Range	$\mathrm{T_{stg}}$	-55~125	°C

2.1 ± 0.1 1.25 ± 0.1 0.65 1.3 ± 0.1 0~0 EMITTER 1 4. **COLLECTOR 2** 2. EMITTER 2 BASE 1 COLLECTOR 1 BASE 2

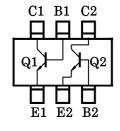
Unit in mm

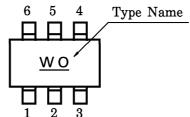
JEDEC EIAJ TOSHIBA

Weight: 0.008g

PIN ASSIGNMENT (TOP VIEW)







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ELECTRICAL CHARACTERISTICS (Q1, Q2) (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Collector Cut-off Current	ICBO	$V_{CB} = 10V, I_{E} = 0$	_	_	1	μ A
Emitter Cut-off Current	I_{EBO}	$V_{EB}=1V, I_{C}=0$	_	_	1	μ A
DC Current Gain	$h_{ extbf{FE}}$	$V_{CE} = 10V$, $I_{C} = 20$ mA	80	_	240	_
Transition Frequency	${ m f_T}$	$V_{CE} = 10V$, $I_{C} = 20mA$	5	7	_	GHz
Insertion Gain	$ S_{21e} ^2(1)$	V _{CE} =10V, I _C =20mA, f=500MHz	_	16.5	_	dB
	$ S_{21e} ^2$ (2)	$V_{CE}=10V$, $I_{C}=20mA$, $f=1GHz$	8	11.5	_]
Noise Figure	NF (1)	V _{CE} =10V, I _C =5mA, f=500MHz	_	1	_	dB
	NF (2)	$V_{CE}=10V$, $I_{C}=5$ mA, $f=1$ GHz	_	1.1	2	
Output Capacitance	C_{ob}	V10V I0	_	0.55	1.05	pF
Reverse Transfer Capacitance	c_{re}	$V_{\mathrm{CB}} = 10 \mathrm{V}, \ \mathrm{I_E} = 0, \ \mathrm{f} = 1 \mathrm{MHz} \ \mathrm{(Note)}$	_	0.6	1.1	

(Note) C_{re} is measured by 3 terminal method capacitance bridge.