TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $\pi$ -MOSV)

# 2SK2889

## Chopper Regulator, DC–DC Converter and Motor Drive Applications

- Low drain-source ON resistance  $RDS(ON) = 0.54 \Omega$  (typ.)
- High forward transfer admittance  $|Y_{fs}| = 9.0 \text{ S (typ.)}$
- Low leakage current  $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DSS} = 600 \ V)$
- Enhancement mode  $: V_{th} = 2.0 \sim 4.0 \text{ V} (V_{DS} = 10 \text{ V}, \text{ ID} = 1 \text{ mA})$

#### Absolute Maximum Ratings (Ta = 25°C)

Characteri	stics	Symbol	Rating	Unit
Drain-source voltage		V <sub>DSS</sub>	600	V
Drain-gate voltage (R	<sub>GS</sub> = 20 kΩ)	V <sub>DGR</sub>	600	V
Gate-source voltage		V <sub>GSS</sub>	±30	V
Drain current	DC (Note 1)	۱ <sub>D</sub>	10	A
	Pulse (Note 1)	I <sub>DP</sub>	40	А
Drain power dissipatio	n (Tc = 25°C)	PD	100	W
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	363	mJ
Avalanche current		I <sub>AR</sub>	10	А
Repetitive avalanche e	energy (Note 3)	E <sub>AR</sub>	10	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature range		T <sub>stg</sub>	-55~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

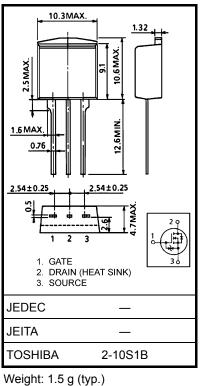
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	1.25	°C / W
Thermal resistance, channel to ambient	R <sub>th (ch−a)</sub>	83.3	°C / W

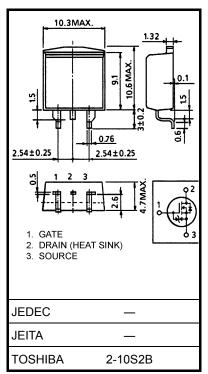
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V\_DD = 90 V, T\_{ch} = 25 °C (initial), L = 6.36 mH, R\_G = 25  $\Omega,$  I\_AR = 10 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.





Weight: 1.5 g (typ.)

Unit: mm

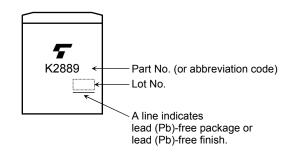
Electrical Characteristics (Ta = 25°C)

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I <sub>GSS</sub>	$V_{GS}$ = ±25 V, $V_{DS}$ = 0 V	_	_	±10	μA
Gate-source bro	eakdown voltage	V <sub>(BR)</sub> GSS	I <sub>G</sub> = ±10 μA, V <sub>DS</sub> = 0 V	±30		_	V
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	600	_	_	V
Gate threshold v	/oltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R <sub>DS (ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5 A	_	0.54	0.75	Ω
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 5 A	3.0	9.0	_	S
Input capacitance	e	C <sub>iss</sub>		_	2040	_	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	230		pF
Output capacitance		C <sub>oss</sub>			590	_	
Switching time	Rise time	tr	$V_{GS} \stackrel{10V}{}_{0V} \int_{U} \int$	_	22	_	
	Turn-on time	t <sub>on</sub>		_	58	_	ns
	Fall time	t <sub>f</sub>		_	36	_	115
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , t <sub>w</sub> =10 $\mu$ s	_	190	_	
Total gate charge (gate-source plus gate-drain)		Qg			45	_	
Gate-source charge		Q <sub>gs</sub>	V <sub>DD</sub> ≈ 400 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A		25	_	nC
Gate-drain ("miller") Charge		Q <sub>gd</sub>			20	_	

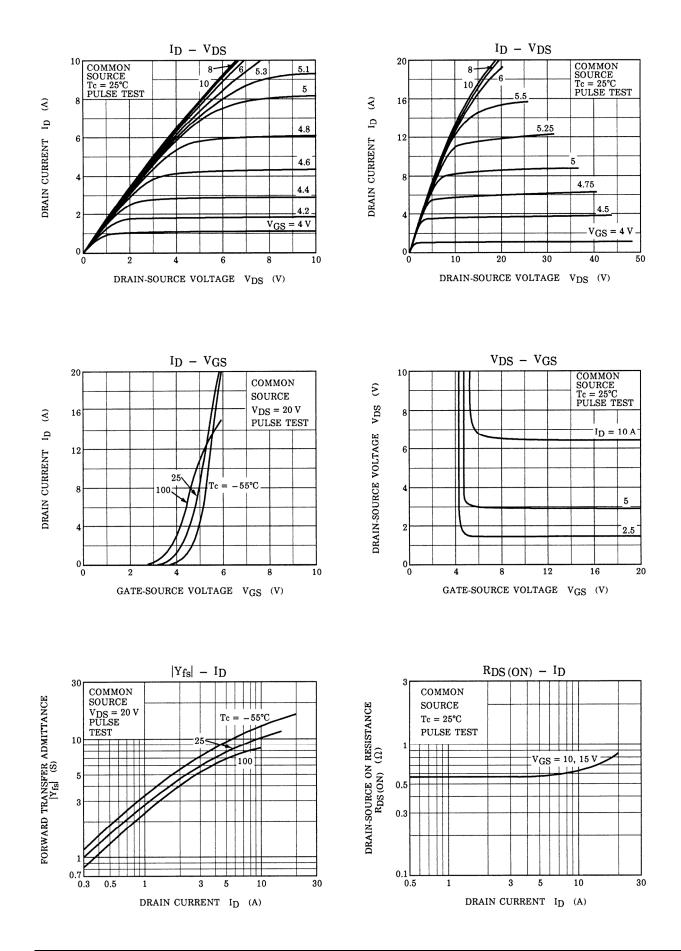
#### Source–Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	—	_	_	10	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	40	А
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 10 A, V <sub>GS</sub> = 0 V	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 10 A, V <sub>GS</sub> = 0 V		1300		ns
Reverse recovery charge	Q <sub>rr</sub>	dI <sub>DR</sub> / dt = 100 A / µs		16		nC

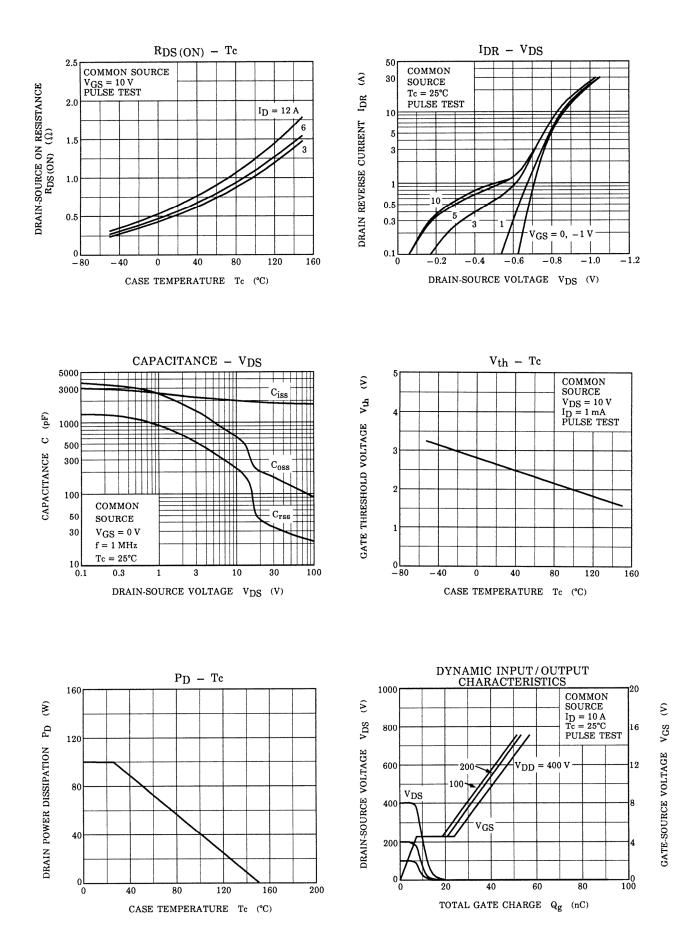
#### Marking

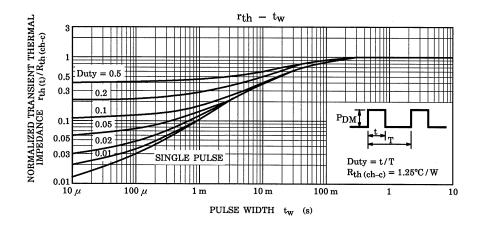


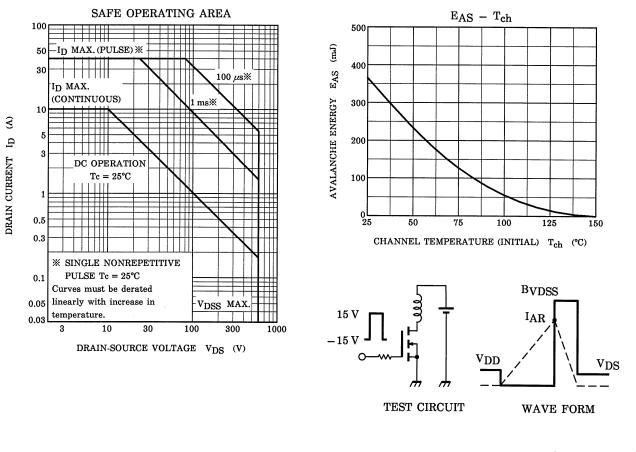
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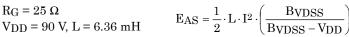


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