TOSHIBA Field Effect Transistor Silicon N Channel Junction Type

2SK363

For Audio Amplifier, Analog Switch, Constant Current and Impedance Converter Applications

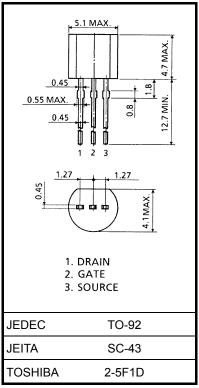
- High breakdown voltage: $V_{GDS} = -40 V$
- High input impedance: $I_{GSS} = -1.0 \text{ nA} (max) (V_{GS} = -30 \text{ V})$
- Low RDS (ON): RDS (ON) = 20 Ω (typ.) (IDSS = 15 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Gate-drain voltage	V _{GDS}	-40	V
Gate current	lG	10	mA
Drain power dissipation	PD	400	mW
Junction temperature	Tj	125	°C
Storage temperature range	T _{stg}	-55~125	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual

reliability data (i.e. reliability test report and estimated failure rate, etc).



Weight: 0.21 g (typ.)

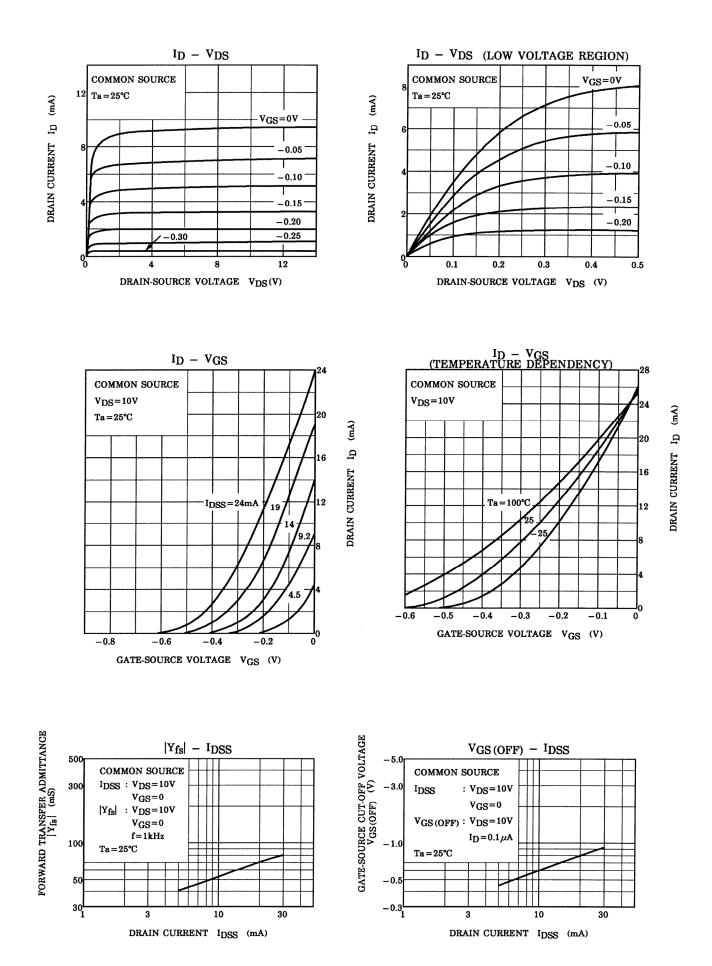
Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate cut-off current	I _{GSS}	$V_{GS} = -30 \text{ V}, \text{ V}_{DS} = 0$	_		-1.0	nA
Gate-drain breakdown voltage	V (BR) GDS	$V_{DS} = 0, I_G = -100 \ \mu A$	-40	_	_	V
Drain current	I _{DSS} (Note 1)	$V_{DS}=10~V,~V_{GS}=0$	5.0		30	mA
Gate-source cut-off voltage	V _{GS (OFF)}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 0.1 \ \mu\text{A}$	-0.3	_	-1.2	V
Forward transfer admittance	Y _{fs}	V_{DS} = 10 V, V_{GS} = 0, f = 1 kHz (Note 2)	25	60	_	mS
Input capacitance	C _{iss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	75	_	pF
Reverse transfer capacitance	C _{rss}	$V_{GD} = -10 \ V, \ I_D = 0, \ f = 1 \ MHz$	_	15		pF
Drain-source ON resistance	R _{DS (ON)}	$V_{DS} = 10 \text{ mV}, V_{GS} = 0 \qquad (\text{Note 2})$	_	20		Ω

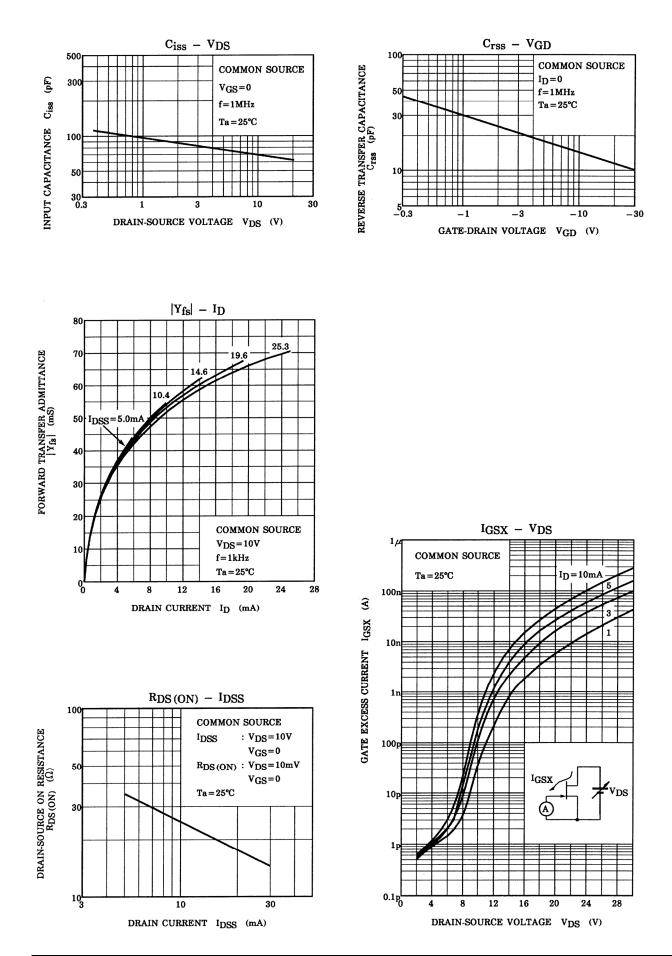
Note 1: I_{DSS} classification GR: 5.0~10.0 mA, BL: 8.0~16.0 mA, V: 14.0~30.0 mA

Note 2: Condition of the typical value $I_{DSS} = 15 \text{ mA}$

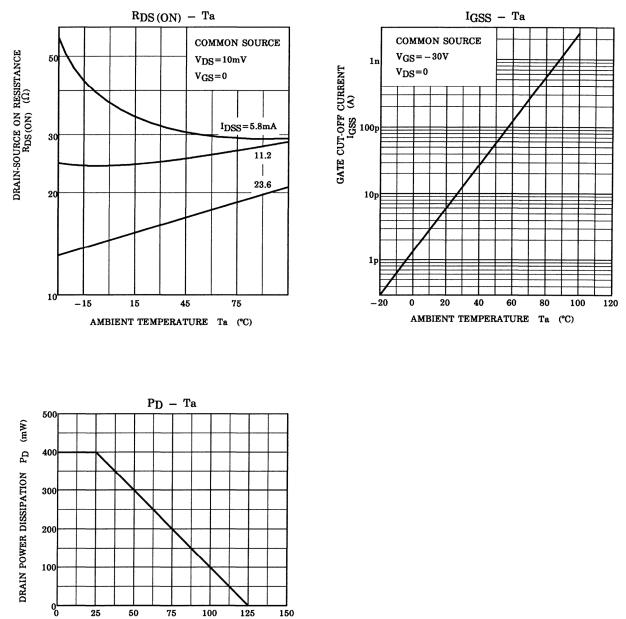
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AMBIENT TEMPERATURE Ta (°C)

RESTRICTIONS ON PRODUCT USE

20070701-EN GENERAL

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 In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc.
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