

- ▶ K-Bus transceiver
- LIN- / K-Bus transceiver
- LIN transceiver + VREG
- LIN transceiver
- LIN transceiver + VREG + WD
- Single wire CAN transceiver
- CAN transceiver

▶ K-Bus transceiver

E910.05

FEATURES

- ▶ Supply voltage range VS 6.9V to 19V
- ▶ Supply voltage range VDD 4.5V to 5.5V
- ▶ Data rate up to 9.600 Baud
- ▶ Bus-line input voltage range from -24V to +30V (+40V for t < 0.5s)
- ▶ Internal monitoring of prohibited conditions
- ▶ Output slewrate control to reduce EMI
- ▶ Very low standby current (30µA typical)
- ▶ Useable as diagnostic interface to ISO 9141
- ▶ -40°C to +125°C operating temperature
- ▶ SO14n package

APPLICATION

- ▶ Automotive low speed bus systems

DESCRIPTION

The IC is designed for communication via bidirectional serial data channels. In addition to the level conversion of transmit and receive signals from the micro processor's voltage level to the 12V bus level, the device includes plausibility checks in order to guarantee undisturbed bus communication in case of corrupted data from the micro controller.

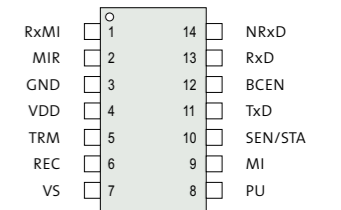
Undefined states are avoided during low voltage conditions by means of a Power-on-Reset which blocks the outputs. The inputs feature internal pull-up and pull-down current sources to maintain defined levels.

The bus pins TRM and REC feature a wide input voltage range from -24V to +40V independent of VS and VDD. This ensures that in case of missing ground or power supply connection the operation of other devices using the bus is not affected.

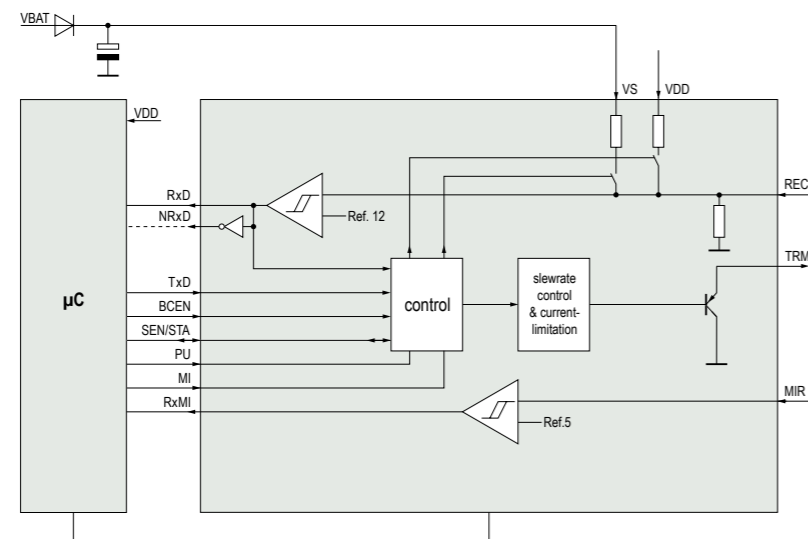
PINNING

Pin	Name	Description
1	RxMI	Receive data from MI-Bus
2	MIR	MI-Bus input
3	GND	Ground
4	VDD	+5V supply
5	TRM	Transmitter output
6	REC	K-Bus input
7	VS	+12V supply
8	PU	Switching pull-up between VDD and VS
9	MI	MI-mode, disables slewrate control on TRM
10	SEN/STA	Status of transmit path
11	TxD	Transmit data to TRM
12	BCEN	Enable bit compare function
13	RxD	Receive data from K-Bus
14	NRxD	Inverted RxD

PACKAGE



BLOCK DIAGRAM



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