

isc Silicon NPN Power Transistor

2SD334

DESCRIPTION

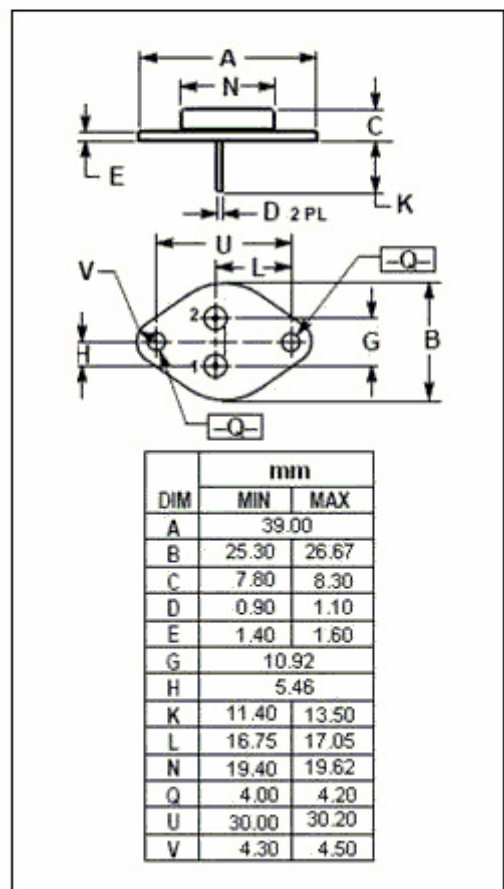
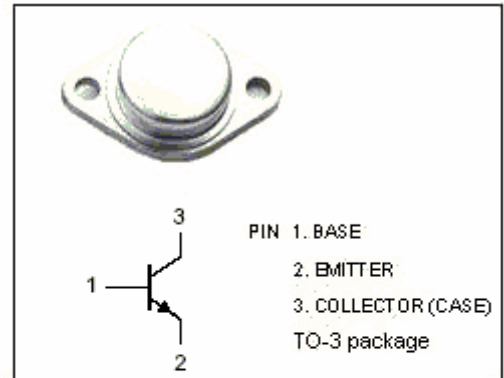
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = 80V$ (Min)
- Wide Area of Safe Operation

APPLICATIONS

- Designed for high power amplifier applications.

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ C$)

SYMBOL	PARAMETER	MAX	UNIT
V_{CBO}	Collector-Base Voltage	110	V
V_{CEO}	Collector-Emitter Voltage	80	V
V_{EBO}	Emitter-Base Voltage	7	V
I_C	Collector Current-Continuous	6	A
I_B	Base Current-Continuous	3	A
P_C	Collector Power Dissipation @ $T_C=25^\circ C$	75	W
T_j	Junction Temperature	150	$^\circ C$
T_{stg}	Storage Temperature Range	-65~150	$^\circ C$



isc Silicon NPN Power Transistor**2SD334****ELECTRICAL CHARACTERISTICS** $T_C=25^{\circ}\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage	$I_C=50\text{mA}$; $I_B=0$	80			V
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage	$I_E=1\text{mA}$; $I_C=0$	7			V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C=5\text{A}$; $I_B=0.5\text{A}$			2	V
$V_{BE(on)}$	Base-Emitter On Voltage	$I_C=1\text{A}$; $V_{CE}=4\text{V}$			2.5	V
I_{CBO}	Collector Cutoff Current	$V_{CB}=40\text{V}$; $I_E=0$			50	μA
		$V_{CB}=110\text{V}$; $I_E=0$			1	mA
h_{FE}	DC Current Gain	$I_C=1\text{A}$; $V_{CE}=4\text{V}$	40		260	

◆ **h_{FE} Classifications**

R	O	Y
40-80	70-150	130-260