

# HVD328C

# Variable Capacitance Diode for VHF tuner

REJ03G0219-0200 Rev.2.00 Mar 31, 2006

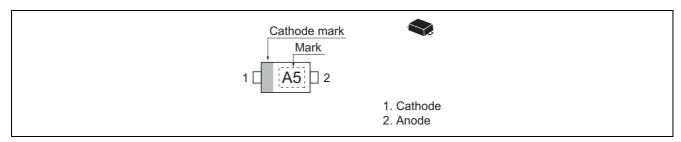
#### **Features**

- Low voltage type (tuning voltage 1 to 10 V), it is suitable for ET without DC/DC converter.
- High capacitance ratio (n = 14.5 min) and suitable for wide band tuner.
- Low series resistance and good C-V linearity.
- Super small Flat Lead Package (SFP) is suitable for surface mount design.

### **Ordering Information**

Type No.	Laser Mark	Package Name	Package Code
HVD328C	A5	SFP	PUSF0002ZB-A

# **Pin Arrangement**



# **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit
Reverse voltage	$V_R$	15	V
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

#### **Electrical Characteristics**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I <sub>R1</sub>	_	_	10	nA	V <sub>R</sub> = 10 V
	I <sub>R2</sub>	_	_	100		V <sub>R</sub> = 10 V, Ta = 60°C
Capacitance	C <sub>1</sub>	41.0	_	45.0	pF	V <sub>R</sub> = 1 V, f = 1 MHz
	C <sub>10</sub>	2.6	_	2.9		V <sub>R</sub> = 10 V, f = 1 MHz
Capacitance ratio	n	14.5	_	_	_	C <sub>1</sub> / C <sub>10</sub>
Series resistance	r <sub>s</sub>	_	_	1.2	Ω	V <sub>R</sub> = 5 V, f = 470 MHz
Matching error	∆C/C *1	_	_	2.0	%	V <sub>R</sub> = 1 to 10 V, f = 1 MHz

Notes: 1. C.C system (Continuous Connected taping system) enable to make any 10 pcs of C/C continuous in a reel , expect extention to another group.

Calculate Matching

$$\Delta C/C = \frac{(Cmax - Cmin)}{Cmin} \times 100 \text{ (\%)}$$

2. For SFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

### **Main Characteristic**

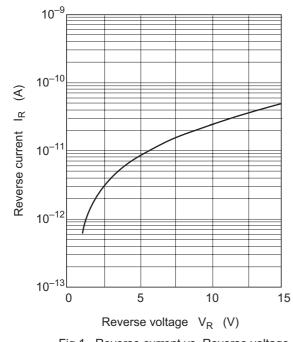


Fig.1 Reverse current vs. Reverse voltage

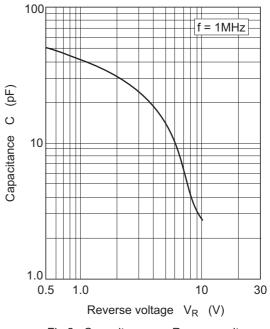
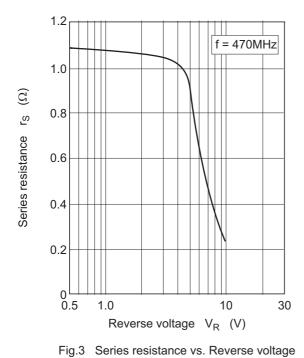
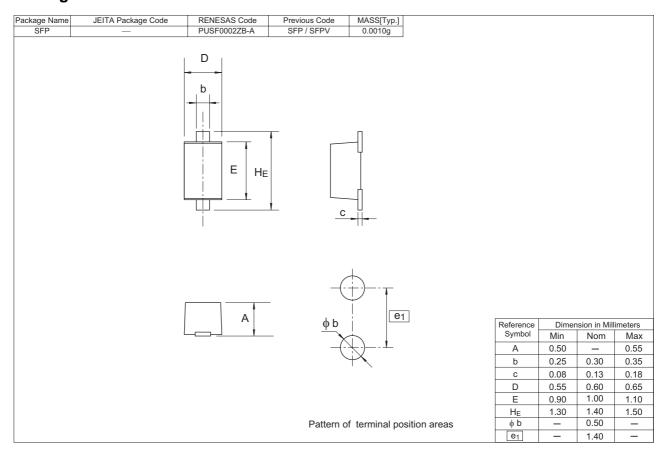


Fig.2 Capacitance vs. Reverse voltage



# **Package Dimensions**



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Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
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Neitesas Technology (offangriar) 63., Ed. Unit 204, 205, AZIACenter, No. 1233 Lujiazui Ring Rd, Pudong District, Shanghai, China 200120 Tel: <86> (21) 5877-1818, Fax: <86> (21) 6887-7898

Renesas Technology Hong Kong Ltd.
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Renesas Technology Singapore Pte. Ltd.
1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd. Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

Renesas Technology Malaysia Sdn. Bhd
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jalan Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: <603> 7955-9390, Fax: <603> 7955-9510

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