

**Silicon NPN Power Transistors**

**2SD1344**

**DESCRIPTION**

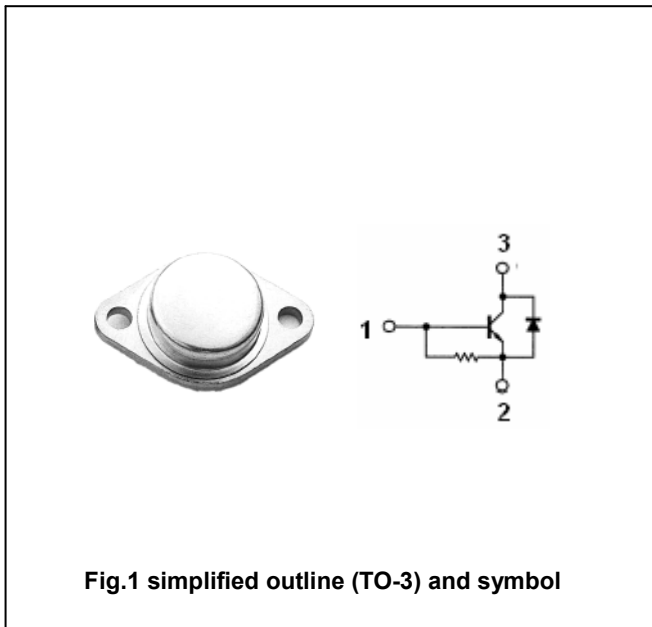
- With TO-3 package
- High voltage
- Built-in damper diode

**APPLICATIONS**

- For color TV horizontal deflection output applications

**PINNING(see fig.2)**

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector



**Absolute maximum ratings(Ta=□)**

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V <sub>CBO</sub>	Collector-base voltage	Open emitter	1500	V
V <sub>CEO</sub>	Collector-emitter voltage	Open base	600	V
V <sub>EBO</sub>	Emitter-base voltage	Open collector	5	V
I <sub>C</sub>	Collector current		6	A
P <sub>T</sub>	Total power dissipation	T <sub>C</sub> =25□	50	W
T <sub>j</sub>	Junction temperature		150	□
T <sub>stg</sub>	Storage temperature		-40~150	□

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## CHARACTERISTICS

Tj=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>CEO(SUS)</sub>	Collector-emitter sustaining voltage	I <sub>C</sub> =100m A; I <sub>B</sub> =0	600			V
V <sub>(BR)EBO</sub>	Emitter-base breakdown voltage	I <sub>E</sub> =200m A; I <sub>C</sub> =0	5			V
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =4A; I <sub>B</sub> =0.8 A			5.0	V
V <sub>BEsat</sub>	Base-emitter saturation voltage	I <sub>C</sub> =4A; I <sub>B</sub> =0.8 A			1.5	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =800V; I <sub>E</sub> =0			10	μA
h <sub>FE-1</sub>	DC current gain	I <sub>C</sub> =0.5A ; V <sub>CE</sub> =5V	10			
h <sub>FE-2</sub>	DC current gain	I <sub>C</sub> =3A ; V <sub>CE</sub> =5V	5			
V <sub>F</sub>	Diode forward voltage	I <sub>F</sub> =4A			2.0	V

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PACKAGE OUTLINE



Fig.2 Outline dimensions