

PART NUMBERING GUIDE

Environmental/Mechanical Specifications on page F5

OAC 100 48 A T - 14.000MHz

Package
OAC = 14 Pin Dip / 5.0Vdc / HCMOS-TTL
OBC = 8 Pin Dip / 5.0Vdc / HCMOS-TTL

Pin One Connection
Blank = No Connect, T = Tri State Enable High

Output Symmetry
Blank = 40/60%, A = 45/55%

Inclusive Stability
100= +/-100ppm, 50= +/-50ppm, 30= +/-30ppm, 25= +/-25ppm,
20= +/-20ppm, 15= +/-15ppm, 10= ±10ppm

Operating Temperature Range
Blank = 0°C to 70°C, 27 = -20°C to 70°C, 48 = -40°C to 85°C

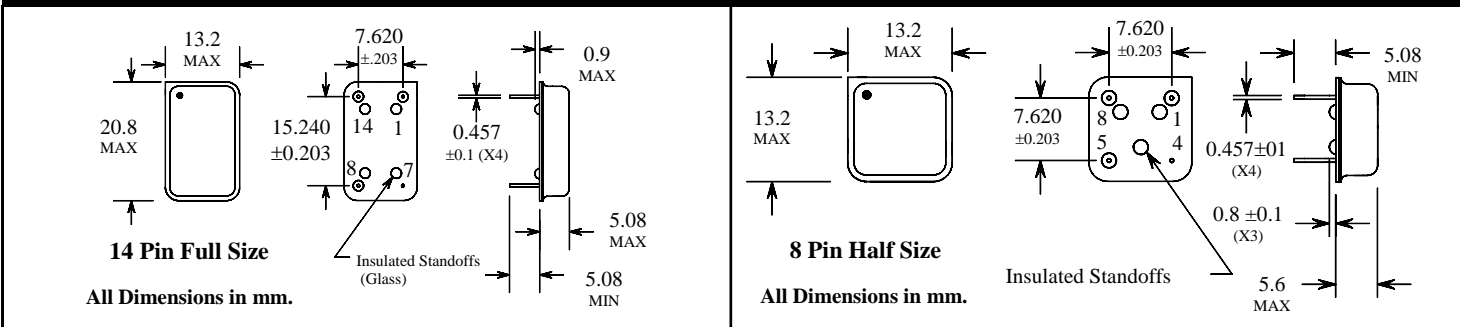
ELECTRICAL SPECIFICATIONS

Revision: 1997-A

Frequency Range	3.579545MHz to 14.31818MHz	
Operating Temperature Range	0°C to 70°C / -40°C to 85°C	
Storage Temperature Range	-55°C to 125°C	
Supply Voltage	5.0Vdc ±10%, 3.3Vdc ±10%	
Input Current	3.579545MHz to 5.000MHz 5.001MHz to 9.000MHz 9.001MHz to 14.31818MHz	1mA Maximum 1.5mA Maximum 2.0mA Maximum
Frequency Tolerance / Stability	Inclusive of Operating Temperature Range, Supply Voltage and Load	±100ppm, ±50ppm, ±30ppm, ±25ppm, ±20ppm, ±15ppm or ±10ppm (20, 15, 10 = 0°C to 70°C Only)
Output Voltage Logic High (Voh)	w/TTL Load w/HCMOS Load	2.4Vdc Minimum Vdd -0.5Vdc Minimum
Output Voltage Logic Low (Vol)	w/TTL Load w/HCMOS Load	0.4Vdc Maximum 0.5Vdc Maximum
Rise Time / Fall Time	0.4Vdc to 2.4Vdc w/TTL Load; 20% to 80%	15nSeconds Maximum
Duty Cycle	@1.4Vdc w/TTL Load; @50% w/HCMOS Load @1.4Vdc w/TTL Load or w/HCMOS Load	50 ±10% (Standard) 50±5% (Optional)
Load Drive Capability	15pF CMOS Load Maximum	
Pin 1 Tristate Input Voltage	No Connection V _{IH} V _{IL}	Enables Output +2.2Vdc Minimum to Enable Output +0.8Vdc Maximum to Disable Output
Aging (@ 25°C)	±5ppm / year Maximum	
Start Up Time	10mSeconds Maximum	
Absolute Clock Jitter	±100pSeconds Maximum	
One Sigma Clock Jitter	±25pSeconds Maximum	

MECHANICAL DIMENSIONS

Marking Guide on page F3-F4



Pin 1: No Connect or Tri-State Pin 7: Case Ground	Pin 8: Output Pin 14: Supply Voltage	Pin 1: No Connect or Tri-State Pin 4: Case Ground	Pin 5: Output Pin 8: Supply Voltage
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