

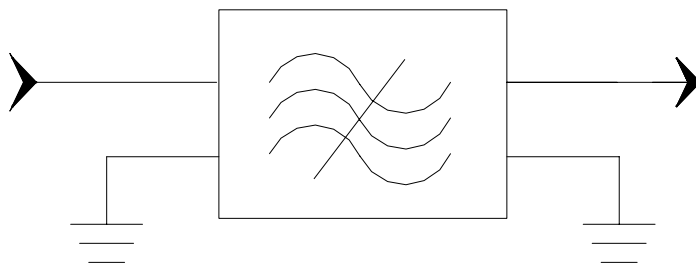
### Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	89.9	90	90.1
Insertion Loss	dB		35.5	37
1.5dB Bandwidth	MHz	24.26	24.3	
20dB Bandwidth	MHz		24.96	25.05
30dB Bandwidth	MHz		25.09	25.15
40dB Bandwidth	MHz		25.22	25.4
Ultimate Rejection( $f_0 \pm 15\text{MHz}$ )	dB	45	50	
Passband Variation	dB		0.8	1.5
Absolute Delay	usec		3.74	4
Substrate Material		YZ-LiNbO3		
Ambient Temperature	°C	25		
Package Size		DIP3512 (35.0x12.8x4.7mm <sup>3</sup> )		

#### Notes:


1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

### Matching Configuration

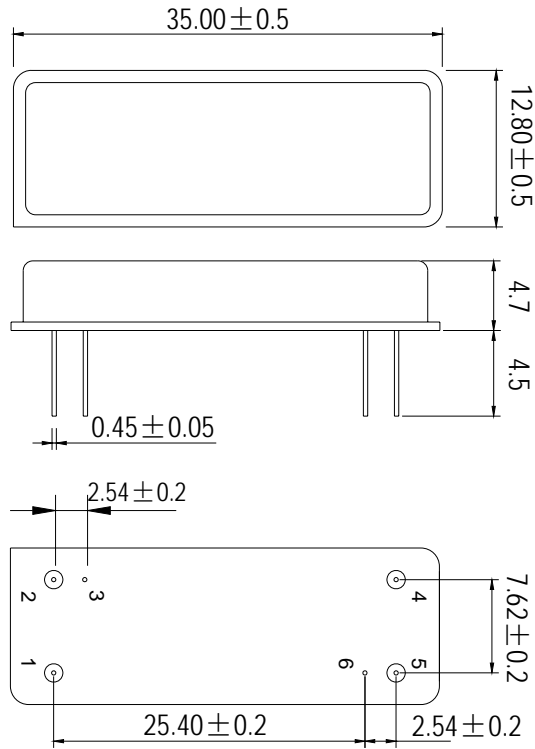


**Source/Load Impedance=50 ohm**

Notes - Component values may change depending on board layout.

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*Package Dimension*

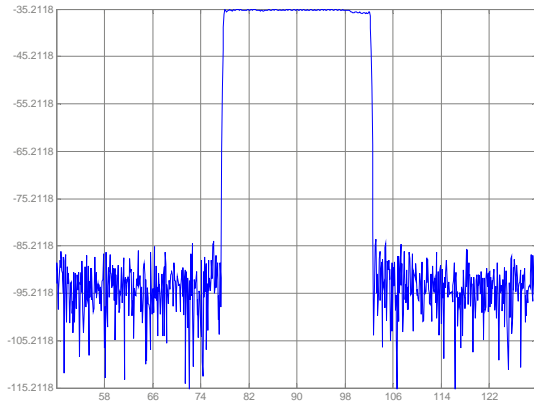


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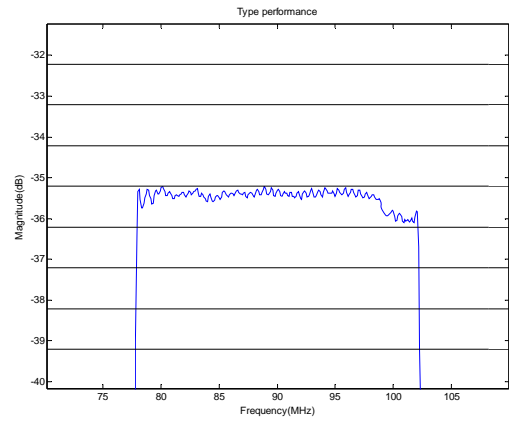
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*Typical Performance*

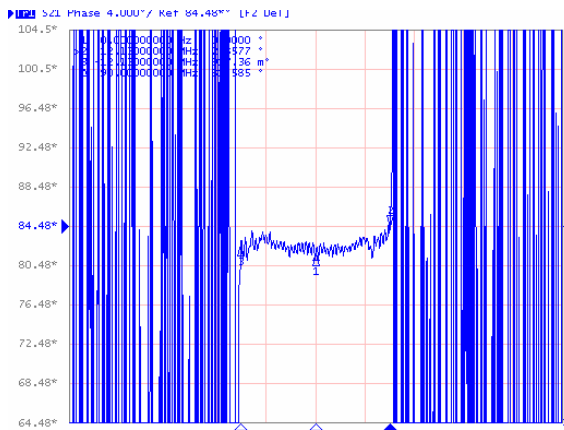
Frequency Respond



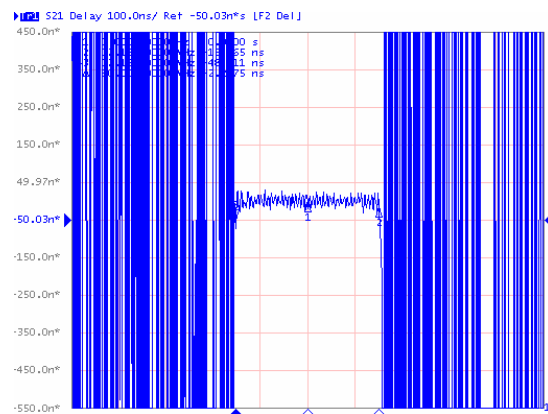
Passband Respond



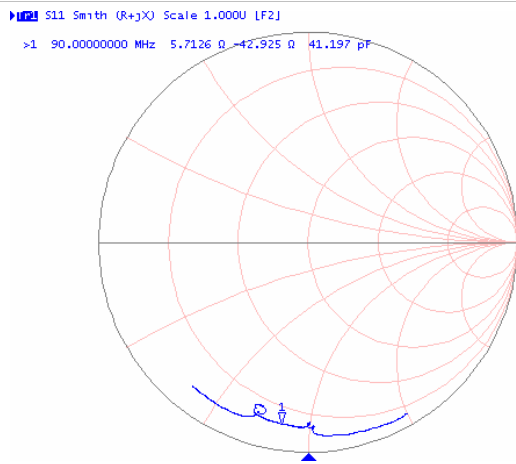
Phase Linearity ( $f_0 \pm 12.13\text{MHz}$ )



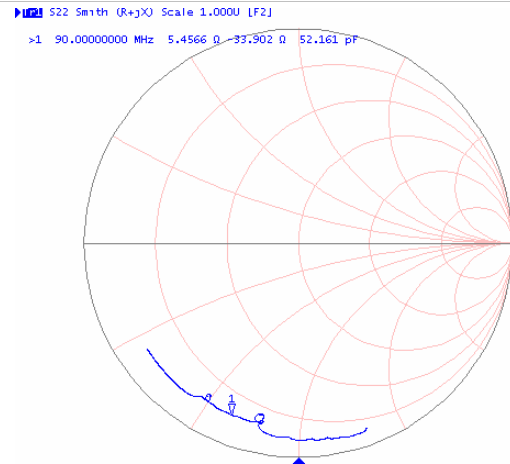
Group delay variation ( $f_0 \pm 12.13\text{MHz}$ )



Simth Chart S11



Simth Chart S22



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