

Single N-channel MOSFET

ELM13420CA-S

■ General description

ELM13420CA-S uses advanced trench technology to provide excellent $R_{ds(on)}$, low gate charge and operation with gate voltages as low as 1.8V.

■ Features

- $V_{ds}=20V$
- $I_d=6A$ ($V_{gs}=10V$)
- $R_{ds(on)} < 24m\Omega$ ($V_{gs}=10V$)
- $R_{ds(on)} < 27m\Omega$ ($V_{gs}=4.5V$)
- $R_{ds(on)} < 42m\Omega$ ($V_{gs}=2.5V$)
- $R_{ds(on)} < 55m\Omega$ ($V_{gs}=1.8V$)

■ Maximum absolute ratings

Parameter	Symbol	Limit	Unit	Note
Drain-source voltage	V_{ds}	20	V	
Gate-source voltage	V_{gs}	± 12	V	
Continuous drain current	I_d	6	A	1
		5		
Pulsed drain current	I_{dm}	25	A	2
Power dissipation	P_d	1.4	W	1
		0.9		
Junction and storage temperature range	T_j, T_{stg}	-55 to 150	°C	

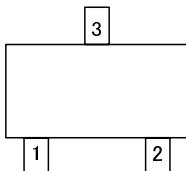
■ Thermal characteristics

Parameter		Symbol	Typ.	Max.	Unit	Note
Maximum junction-to-ambient	$t \leq 10s$	$R_{\theta ja}$	70	90	°C/W	1
Maximum junction-to-ambient	Steady-state		100	125	°C/W	
Maximum junction-to-lead	Steady-state	$R_{\theta jl}$	63	80	°C/W	3

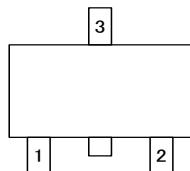
■ Pin configuration

■ Circuit

SOT-23 (TOP VIEW)

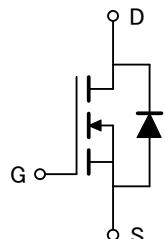


(Without extra bar)



(With extra bar)

Pin No.	Pin name
1	GATE
2	SOURCE
3	DRAIN



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■ Electrical characteristics

$T_a=25^\circ C$

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
STATIC PARAMETERS						
Drain-source breakdown voltage	BVdss	$I_d=250\mu A, V_{gs}=0V$	20			V
Zero gate voltage drain current	Idss	Vds=16V			1	μA
		Vgs=0V	Tj=55°C		5	
Gate-body leakage current	Igss	Vds=0V, Vgs=±12V			100	nA
Gate threshold voltage	Vgs(th)	Vds=Vgs, Id=250 μA	0.5	0.7	1.0	V
On state drain current	Id(on)	Vgs=4.5V, Vds=5V	25			A
Static drain-source on-resistance	Rds(on)	Vgs=10V		19	24	$m\Omega$
		Id=6A	Tj=125°C	29	35	
		Vgs=4.5V, Id=5A		22	27	$m\Omega$
		Vgs=2.5V, Id=4A		35	42	$m\Omega$
		Vgs=1.8V, Id=2A		45	55	$m\Omega$
Forward transconductance	Gfs	Vds=5V, Id=3.8A		24		S
Diode forward voltage	Vsd	Is=1A, Vgs=0V		0.75	1.00	V
Max. body-diode continuous current	Is				2	A
DYNAMIC PARAMETERS						
Input capacitance	Ciss	Vgs=0V, Vds=10V, f=1MHz		630		pF
Output capacitance	Coss			164		pF
Reverse transfer capacitance	Crss			137		pF
Gate resistance	Rg	Vgs=0V, Vds=0V, f=1MHz		1.5		Ω
SWITCHING PARAMETERS						
Total gate charge	Qg	Vgs=4.5V, Vds=10V, Id=6A		8.8		nC
Gate-source charge	Qgs			1.0		nC
Gate-drain charge	Qgd			3.7		nC
Turn-on delay time	td(on)	Vgs=5V, Vds=10V Rl=1.7 Ω , Rgen=6 Ω		5.5		ns
Turn-on rise time	tr			14.0		ns
Turn-off delay time	td(off)			29.0		ns
Turn-off fall time	tf			10.2		ns
Body diode reverse recovery time	trr	$If=6A, dl/dt=100A/\mu s$		15.2		ns
Body diode reverse recovery charge	Qrr	$If=6A, dl/dt=100A/\mu s$		6.3		nC

NOTE :

1. The value of $R\theta_{ja}$ is measured with the device mounted on 1in² FR-4 board of 2oz. Copper, in still air environment with $T_a=25^\circ C$. The value in any given applications depends on the user's specific board design, The current rating is based on the $t \leq 10s$ thermal resistance rating.
2. Repetitive rating, pulse width limited by junction temperature.
3. The $R\theta_{ja}$ is the sum of the thermal impedance from junction to lead $R\theta_{jl}$ and lead to ambient.
4. The static characteristics in Figures 1 to 6 are obtained using 80 μs pulses, duty cycle 0.5%max.
5. These tests are performed with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_a=25^\circ C$. The SOA curve provides a single pulse rating.

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■ Typical electrical and thermal characteristics

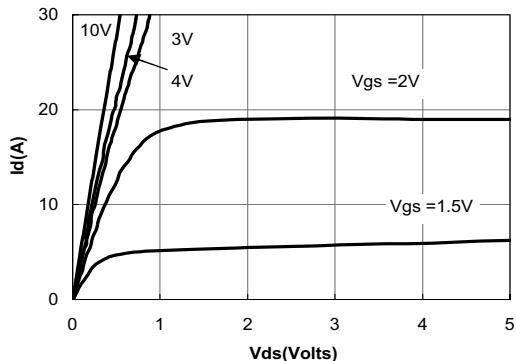


Figure 1: On-Regions Characteristics

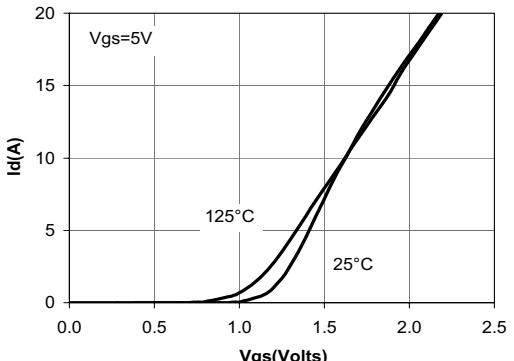


Figure 2: Transfer Characteristics

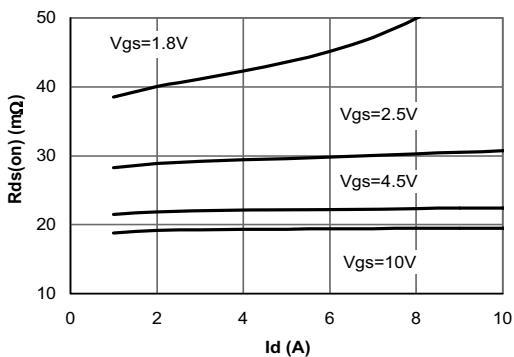


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

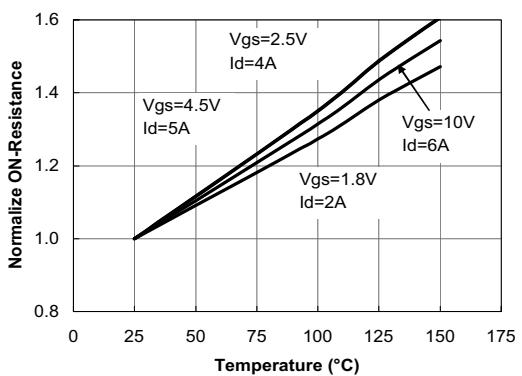


Figure 4: On-Resistance vs. Junction Temperature

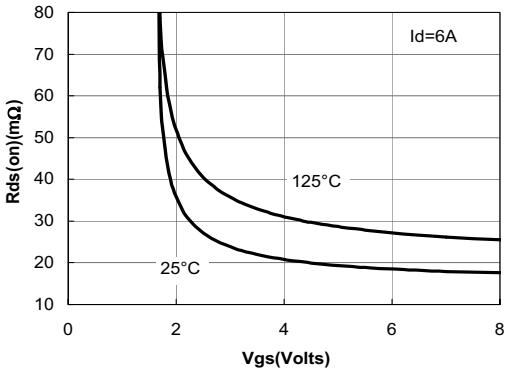


Figure 5: On-Resistance vs. Gate-Source Voltage

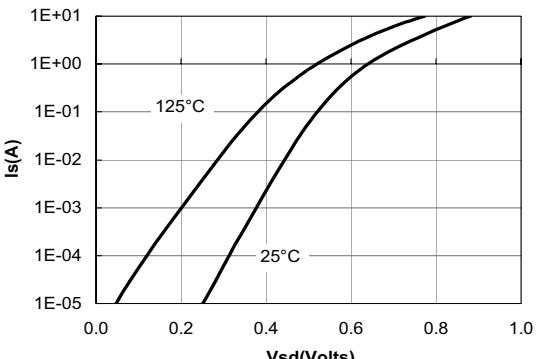


Figure 6: Body-Diode Characteristics

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