

VOLTAGE CONTROLLED OSCILLATOR

SURFACE MOUNT MODEL: DCSR500-8

OPTIMIZED FREQUENCY

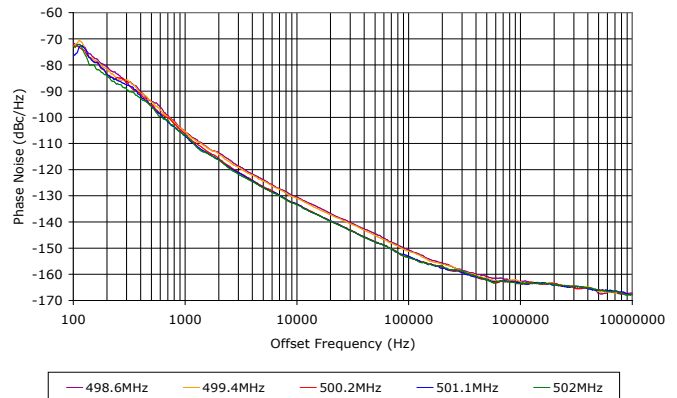
500 MHz

FEATURES:

- ▶ Optimized Tuning Frequency Band
- ▶ Exceptional Phase Noise Performance
- ▶ Small Size, Surface Mount
- ▶ Lead Free REL-PRO® Technology
- ▶ Planar Resonator Construction



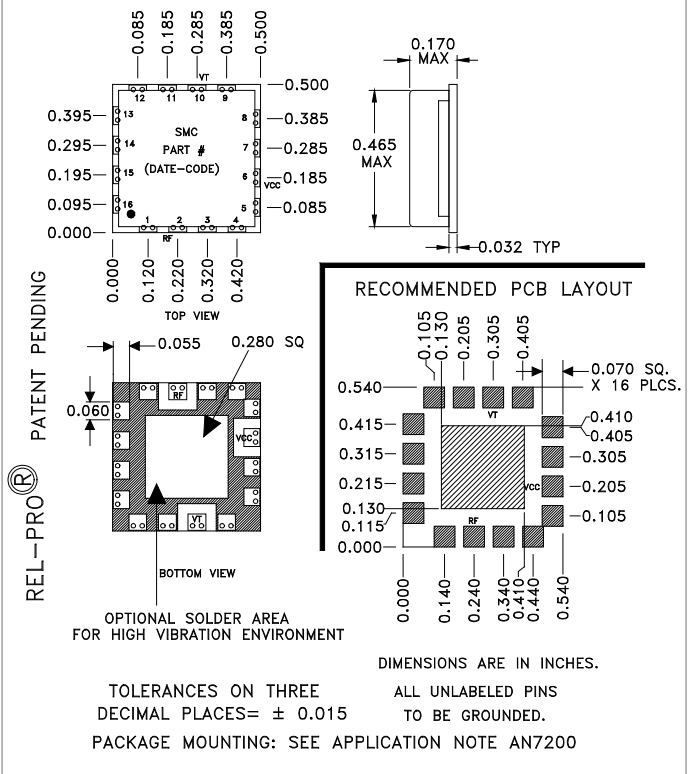
Phase Noise



SPECIFICATIONS (Rev. B 11/07/08)

Frequency	500 MHz
Tuning Voltage	0.5 - 8 VDC
Bias Voltage	+8 VDC @ 42 mA (Max.)
Output Power	+3.5 dBm (Min.)
Tuning Sensitivity	1 - 1.3 MHz/V (Typ.)
Output Impedance	50 Ohms (Nom.)
Harmonic Suppression	26 dB (Typ.)
Frequency Pulling	0.5 MHz (Typ. @ 1.75 VSWR)
Frequency Pushing	0.5 MHz/V (Typ.)
Tuning Port Capacitance	115 pF (Typ.)
Phase Noise @ 10 kHz	-132 dBc/Hz (Typ.)
Operating Temperature Range	-40 to +85 °C

Package # 278LF



Absolute Maximum Ratings

Storage Temp. Range	-55 to +125 °C
Bias Voltage	+8.5 V
Tuning Voltage	+10 V
DC Voltage Applied to RF Out	± 25 V

This product is manufactured under one or more of the following patents: Approved: 5,160,810; 5,122,621; 5,390,349; 5,416,449; 5,650,754; 5,805,431; 6,525,623; 6,850,575; 7,088,189; 7,180,381; 7,196,591; 7,262,670; 7,265,642; 7,292,113
 Pending: 60/493075; 60/501371 & 60/501790; 60/527957 & 60/528670; 60/563481; 60/564173; 60/589090; 60/601823; 60/605791; EV31369834;
 11/259766; 60/622485 & 60/710310; 10/937525/60/736901; 60/732787

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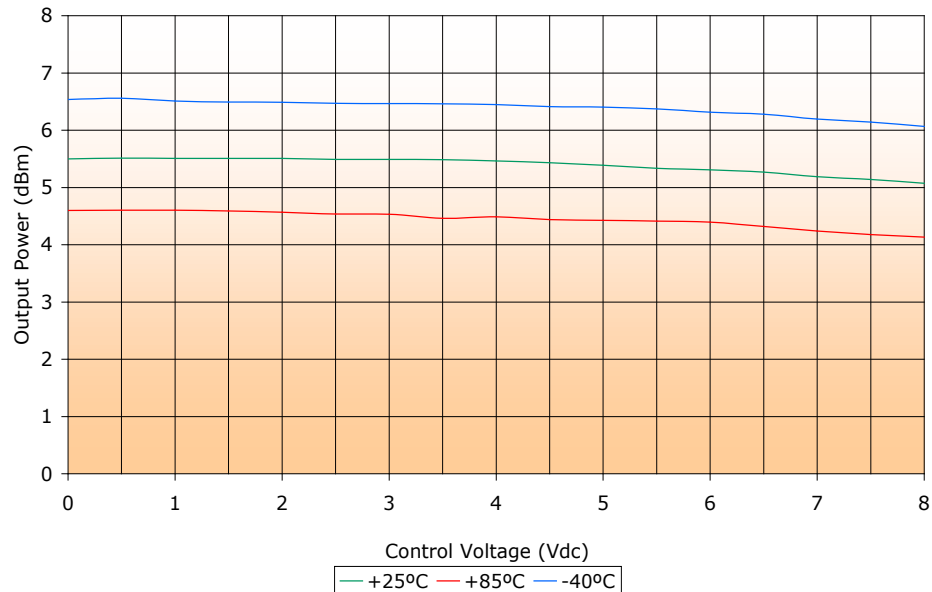
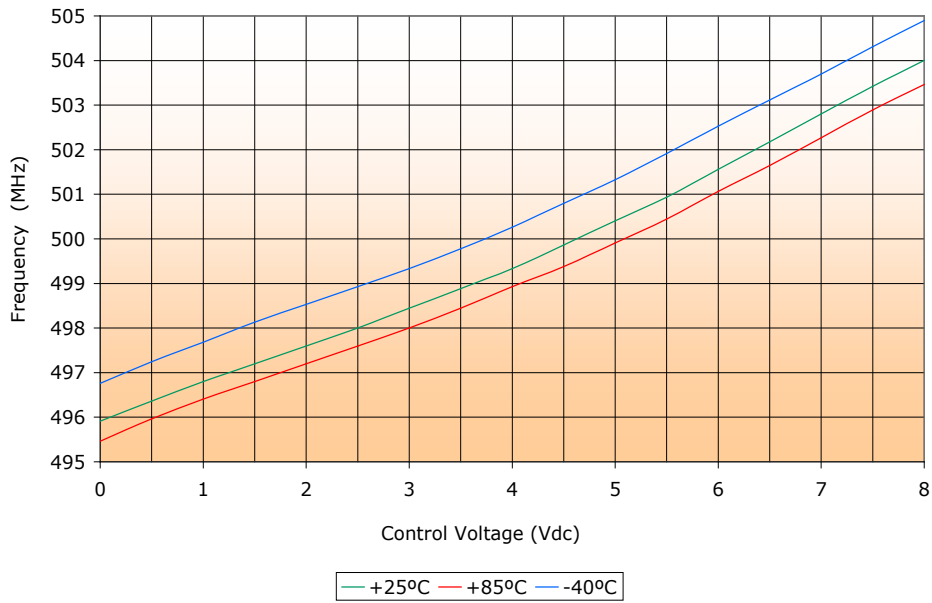


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PERFORMANCE PLOTS



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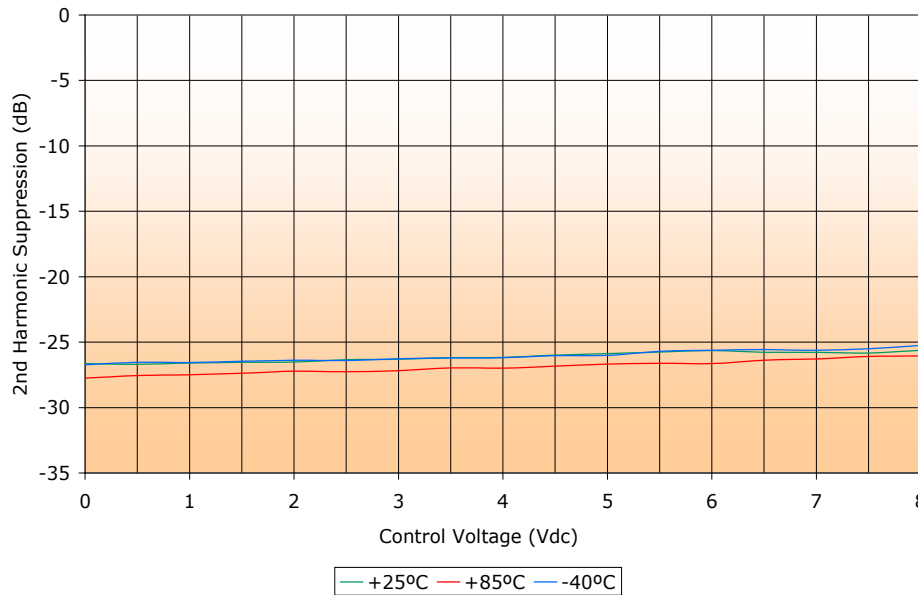
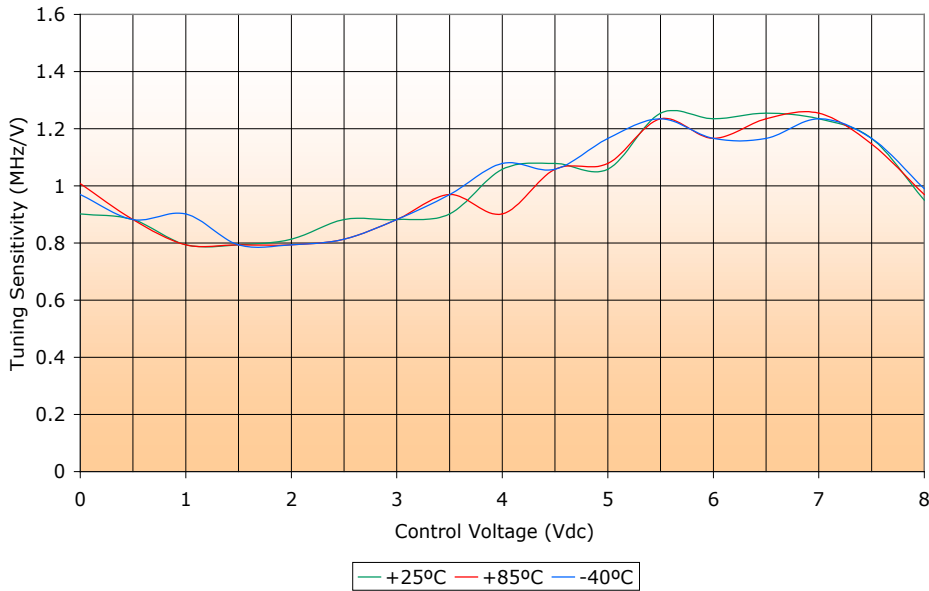


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