

#### **INFRARED EMITTING DIODES**

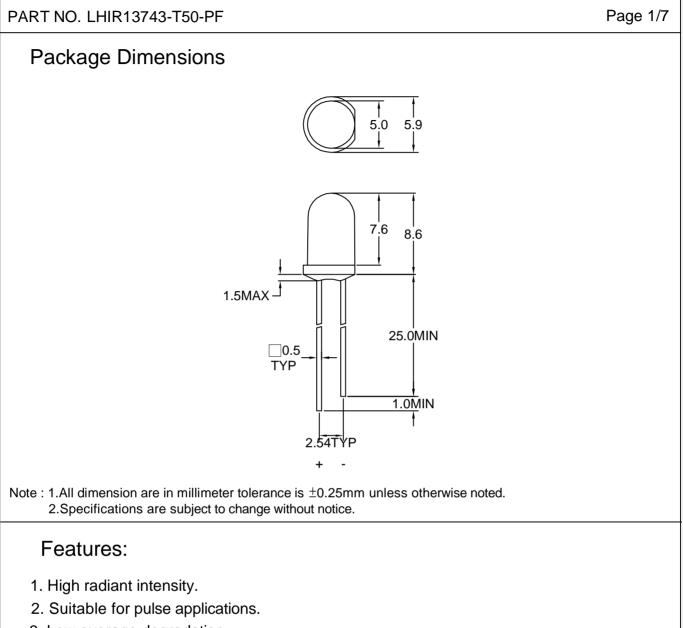


## LHIR13743-T50-PF

# DATA SHEET

- DOC. NO : QW0905-LHIR13743-T50-PF
- REV. : <u>A</u>
- DATE : 02 Jan 2006





3. Low average degradation.

#### **Descriptions:**

The LHIR13743-T50-PF series are super-high efficiency Gallium Aluminum Arsenide infrared emitting diodes encapsulated in water clear plastic T-1 3/4 package individually

**Device Selection Guide:** 

| PART NO          | MATERIAL    | LENS COLOR  |  |
|------------------|-------------|-------------|--|
| LHIR13743-T50-PF | AlGaAs/GaAs | Water Clear |  |



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## Absolute Maximum Ratings at Ta=25 $^\circ\!\mathrm{C}$

| Parameter                                     | Symbol | Ratings   | UNIT |
|---|--------|-----------|------|
| Parameter                                     | Symbol | HIR       |      |
| Forward Current                               | lF     | 50        | mA   |
| Peak Forward Current<br>(300PPS,10 μ s Pulse) | IFP    | 1         | А    |
| Power Dissipation                             | PD     | 100       | mW   |
| Reverse Voltagev                              | Vr     | 5         | V    |
| Operating Temperature                         | Topr   | -40 ~ +85 | °C   |
| Storage Temperature                           | Tstg   | -40 ~ +85 | °C   |

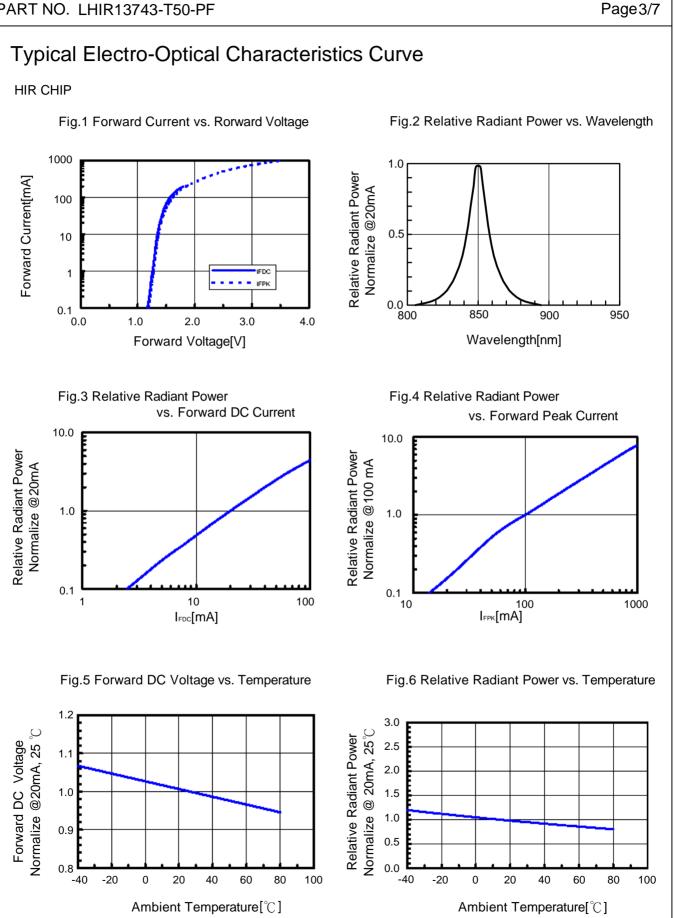
### Electrical Optical Characteristics (Aa=25°C)

| PARAMETER                  | SYMBOL             | Min. | Тур. | Max. | UNIT               | TEST CONDITION |
|----------------------------|--------------------|------|------|------|--------------------|----------------|
| Radiant Intensity          | Le                 | 6.0  | 12.5 |      | mW/sr              | IF=50mA        |
| Aperture Radiant Incidence | Ee                 | 1.0  | 1.8  |      | mW/cm <sup>2</sup> | IF=50mA        |
| Peak Wavelength            | $\lambda$ peak     |      | 850  |      | nm                 | IF=20mA        |
| Spectral Line Half Width   | $	riangle \lambda$ |      | 50   |      | nm                 | IF=20mA        |
| Forward Voltage            | VF                 |      | 1.5  | 2.0  | V                  | IF=50mA        |
| Reverse Current            | IR                 |      |      | 100  | $\mu$ A            | VR=5V          |
| Viewing Angle              | 2 <i>θ</i> 1/2     |      | 60   |      | deg                |                |

Note : 1.The forward voltage data did not including  $\pm 0.1V$  testing tolerance. 2. The radiant intensity data did not including  $\pm 15\%$  testing tolerance.



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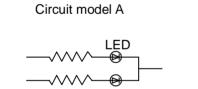
#### Storage time:

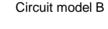
- 1.The operation of Temperatures and RH are : 5  $^\circ\!\mathrm{C}$  ~35  $^\circ\!\mathrm{C}$  ,RH<60%.
- 2.Once the package is opened, the products should be used within a week. Otherwise, they should be kept in a damp proof box with descanting agent. Considering the tape life, we suggest our customers to use our products within a year(from production date).
- 3.If opened more than one week in an atmosphere 5  $^\circ C\,$  ~ 35  $^\circ C$  ,RH<60%, they should be treated at 60  $^\circ C\,\pm 5\,^\circ C$  fo r 15hrs.

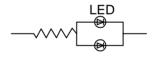
#### Drive Method:

LED is a current operated device, and therefore, require some kind of current limiting incorporated into the driver circuit. This current limiting typically takes the form of a current limiting resistor placed in series with the LED.

Consider worst case voltage variations than could occur across the current limiting resistor. The forwrd current should not be allowed to change by more than 40 % of its desired value.







(A) Recommended circuit.

(B) The difference of brightness between LED could be found due to the VF-IF characteristics of LED.

#### Cleaning:

Use alcohol-based cleaning solvents such as isopropyl alcohol to clean the LED.

### ESD(Electrostatic Discharge):

Static Electricity or power surge will damage the LED. Use of a conductive wrist band or anti-electrosatic glove is recommended when handing these LED. All devices, equipment and machinery must be properly grounded.

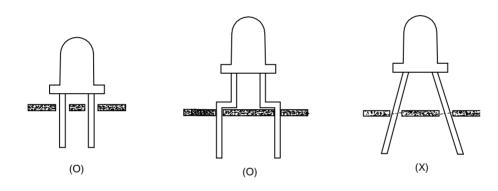


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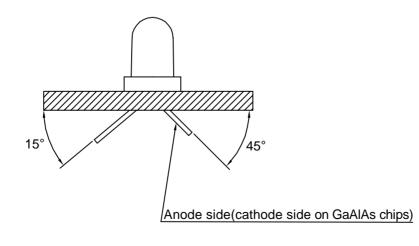
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## Mounting:

1. If the leads are subjected to stress during soldering a printed circuit board, illumination failure may result immediately or later during use. For this reason, make sure that the intervals between the installation holes in the board are equal to the intervals between the leads (after forming if done) so that no stress is applied to the lead.



2. The LED lamps are designed for high-density mounting and have a structure which can alleviate mechanical stress due to clinching . Nevertheless , take care to avoid the occurrence of residual mechanical stress due to clinching .





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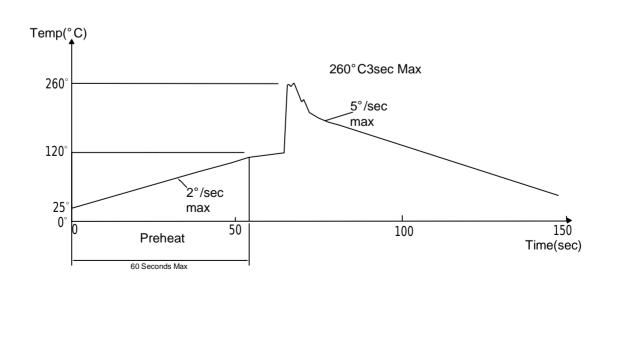
#### Soldering Condition(Pb-Free)

#### 1.Iron:

Soldering Iron:30W Max Temperature 350° C Max Soldering Time:3 Seconds Max(One Time) Distance:2mm Min(From solder joint to body)

#### 2. Wave Soldering Profile

Dip Soldering Preheat: 120°C Max Preheat time: 60seconds Max Ramp-up 2°C/sec(max) Ramp-Down:-5°C/sec(max) Solder Bath:260°C Max Dipping Time:3 seconds Max Distance:2mm Min(From solder joint to body)





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## Reliability Test:

| Test Item                              | Test Condition   | Description   | Reference<br>Standard  |
|--|--|---|--|
| Operating Life Test                    | 1.Under Room Temperature<br>2.If=20mA<br>3.t=1000 hrs (-24hrs, +72hrs) | This test is conducted for the purpose<br>of detemining the resisance of a part<br>in electrical and themal stressed.   | MIL-STD-750: 1026<br>MIL-STD-883: 1005<br>JIS C 7021: B-1                      |
| High Temperature<br>Storage Test       | 1.Ta=105 ℃±5℃<br>2.t=1000 hrs (-24hrs, +72hrs)                         | The purpose of this is the resistance of the device which is laid under ondition of high temperature for hours.   | MIL-STD-883:1008<br>JIS C 7021: B-10   |
| Low Temperature<br>Storage Test        | 1.Ta=-40 °C ±5 °C<br>2.t=1000 hrs (-24hrs, +72hrs)                     | The purpose of this is the resistance<br>of the device which is laid under<br>condition of low temperature for hours.   | JIS C 7021: B-12   |
| High Temperature<br>High Humidity Test | 1.Ta=65 ℃ ±5℃<br>2.RH=90 %~95 %<br>3.t=240hrs ±2hrs                    | The purpose of this test is the resistance of the device under tropical for hous.   | MIL-STD-202:103B<br>JIS C 7021: B-11   |
| Thermal Shock Test                     | 1.Ta=105 ℃±5℃&-40 ℃±5℃<br>(10min) (10min)<br>2.total 10 cycles         | The purpose of this is the resistance of the device to sudden extreme changes in high and low temperature.  | MIL-STD-202: 107D<br>MIL-STD-750: 1051<br>MIL-STD-883: 1011                    |
| Solder Resistance<br>Test              | 1.T.Sol=260 ℃±5℃<br>2.Dwell time= 10±1sec.                             | This test intended to determine the<br>thermal characteristic resistance<br>of the device to sudden exposures<br>at extreme changes in temperature<br>when soldering the lead wire. | MIL-STD-202: 210A<br>MIL-STD-750: 2031<br>JIS C 7021: A-1                      |
| Solderability Test                     | 1.T.Sol=230 ℃±5℃<br>2.Dwell time=5±1sec                                | This test intended to see soldering well performed or not.  | MIL-STD-202: 208D<br>MIL-STD-750: 2026<br>MIL-STD-883: 2003<br>JIS C 7021: A-2 |