

Silicon Power Transistors

2SC4448

DESCRIPTION

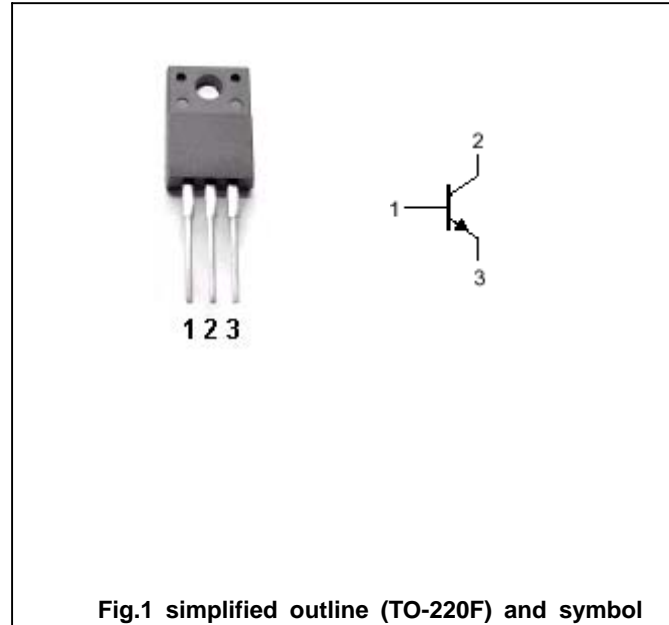
- With TO-220F package
- High voltage ,high frequency

APPLICATIONS

- Chroma output applications for HDTV
- Video output applications for high resolution display

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	emitter



Absolute maximum ratings(Ta=25)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	250	V
V_{CEO}	Collector-emitter voltage	Open base	250	V
V_{EBO}	Emitter-base voltage	Open collector	5	V
I_C	Collector current		150	mA
I_{CM}	Collector current-peak		300	mA
I_B	Base current		50	mA
P_C	Collector dissipation	$T_a=25$	2	W
P_C	Collector dissipation	$T_C=25$	10	W
T_j	Junction temperature		150	
T_{stg}	Storage temperature		-55~150	

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CHARACTERISTICS

T_j=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEsat}	Collector-emitter saturation voltage	I _C =50mA I _B =5mA			1.0	V
V _{BEsat}	Base-emitter saturation voltage	I _C =50mA I _B =5mA			1.0	V
I _{CBO}	Collector cut-off current	V _{CB} =200V I _E =0			100	μ A
I _{EBO}	Emitter cut-off current	V _{EB} =5V; I _C =0			10	μ A
h _{FE-1}	DC current gain	I _C =10mA ; V _{CE} =10V	40		200	
h _{FE-2}	DC current gain	I _C =100mA ; V _{CE} =10V	20			
f _T	Transition frequency	I _C =40mA ; V _{CE} =10V		240		MHz
C _{OB}	Collector output capacitance	f=1MHz; V _{CB} =30V		3.3		pF

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PACKAGE OUTLINE

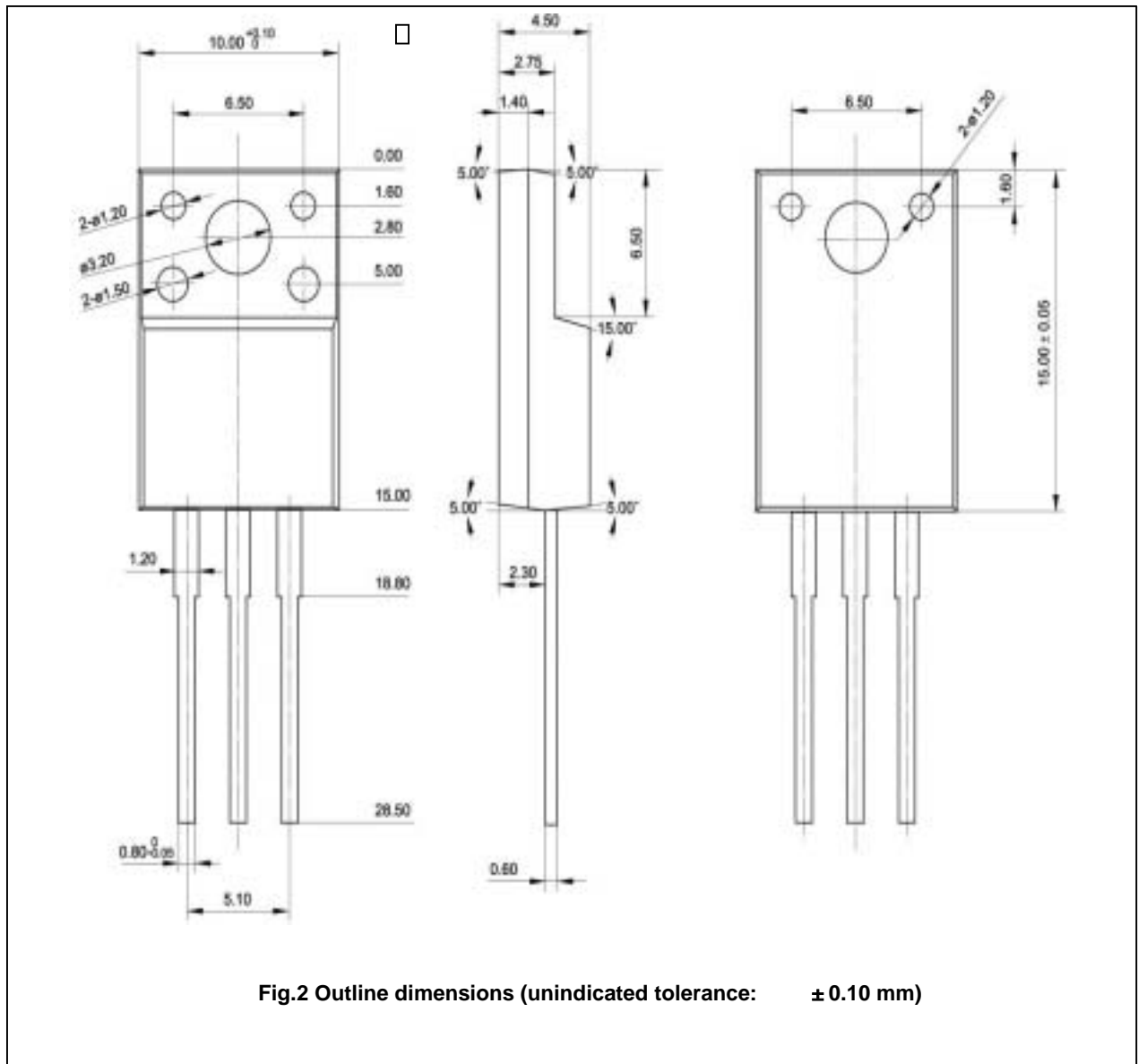


Fig.2 Outline dimensions (unindicated tolerance: ± 0.10 mm)