



**CMLT7820G**

**SURFACE MOUNT  
PICOmini™  
VERY LOW V<sub>CE(SAT)</sub>  
PNP SILICON TRANSISTOR**

**PICOmini™**



**SOT-563 CASE**

**Central™  
Semiconductor Corp.**

**DESCRIPTION:**

The CENTRAL SEMICONDUCTOR CMLT7820G is a very low V<sub>CE(SAT)</sub> PNP Transistor, designed for applications where small size and efficiency are the prime requirements. Packaged in a space saving PICOmini™ SOT-563 surface mount package, this component provides performance characteristics suitable for the most demanding size constrained applications.

**MARKING CODE: 78G**

**FEATURES:**

- Device is **Halogen Free** by design
- High Current (I<sub>C</sub>=1.0A)
- V<sub>CE(SAT)</sub>=0.34V MAX @ I<sub>C</sub>=1.0A
- PICOmini™ SOT563 surface mount package
- Complementary NPN device **CMLT3820G**

**APPLICATIONS:**

- DC/DC Converters
- Voltage Clamping
- Protection Circuits
- Battery powered Cell Phones, Pagers, Digital Cameras, PDAs, Laptops, etc.

**MAXIMUM RATINGS:** (T<sub>A</sub>=25°C)

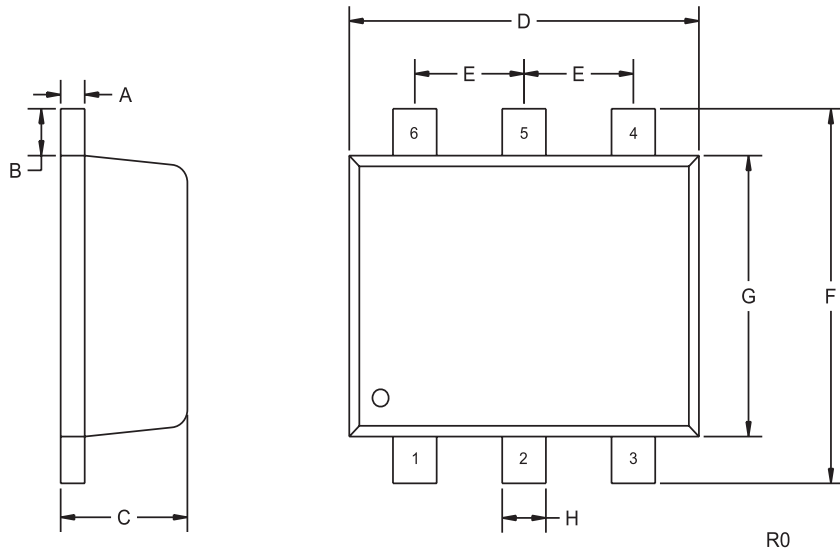
Collector-Base Voltage
Collector-Emitter Voltage
Emitter-Base Voltage
Continuous Collector Current
Peak Collector Current
Base Current
Power Dissipation
Operating and Storage Junction Temperature
Thermal Resistance

SYMBOL		UNITS
V <sub>CBO</sub>	80	V
V <sub>CEO</sub>	60	V
V <sub>EBO</sub>	5.0	V
I <sub>C</sub>	1.0	A
I <sub>CM</sub>	2.0	A
I <sub>B</sub>	300	mA
P <sub>D</sub>	250	mW
T <sub>J</sub> , T <sub>stg</sub>	-65 to +150	°C
θ <sub>JA</sub>	500	°C/W

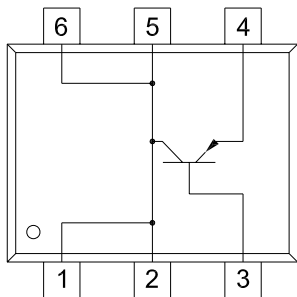
**ELECTRICAL CHARACTERISTICS:** (T<sub>A</sub>=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I <sub>CBO</sub>	V <sub>CB</sub> =60V		100	nA
I <sub>EBO</sub>	V <sub>EB</sub> =5.0V		100	nA
BV <sub>CBO</sub>	I <sub>C</sub> =100µA	80		V
BV <sub>CEO</sub>	I <sub>C</sub> =10mA	60		V
BV <sub>EBO</sub>	I <sub>E</sub> =100µA	5.0		V
V <sub>CE(SAT)</sub>	I <sub>C</sub> =100mA, I <sub>B</sub> =1.0mA		0.175	V
V <sub>CE(SAT)</sub>	I <sub>C</sub> =500mA, I <sub>B</sub> =50mA		0.18	V
V <sub>CE(SAT)</sub>	I <sub>C</sub> =1.0A, I <sub>B</sub> =100mA		0.34	V
V <sub>BE(SAT)</sub>	I <sub>C</sub> =1.0A, I <sub>B</sub> =50mA		1.1	V
V <sub>BE(ON)</sub>	V <sub>CE</sub> =5.0V, I <sub>C</sub> =1.0A		0.9	V
h <sub>FE</sub>	V <sub>CE</sub> =5.0V, I <sub>C</sub> =1.0mA	200		
h <sub>FE</sub>	V <sub>CE</sub> =5.0V, I <sub>C</sub> =500mA	150		
h <sub>FE</sub>	V <sub>CE</sub> =5.0V, I <sub>C</sub> =1.0A	100		
f <sub>T</sub>	V <sub>CE</sub> =10V, I <sub>C</sub> =50mA	150		MHz
C <sub>ob</sub>	V <sub>CB</sub> =10V, I <sub>E</sub> =0, f=1.0MHz		15	pF

**SOT-563 CASE - MECHANICAL OUTLINE**



**PIN CONFIGURATION**



SYMBOL	DIMENSIONS			
	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.004	0.007	0.10	0.18
B	0.008		0.20	
C	0.022	0.024	0.56	0.60
D	0.059	0.067	1.50	1.70
E	0.020		0.50	
F	0.061	0.067	1.55	1.70
G	0.047		1.20	
H	0.006	0.012	0.15	0.30

SOT-563 (REV: R0)

**LEAD CODE:**

- 1) COLLECTOR
  - 2) COLLECTOR
  - 3) BASE
  - 4) EMITTER
  - 5) COLLECTOR
  - 6) COLLECTOR
- (Pins 1, 2, 5 and 6 are common.)

**MARKING CODE: 78G**

R1 (30-September 2008)