



Shantou Huashan Electronic Devices Co.,Ltd.

PNP SILICON TRANSISTOR

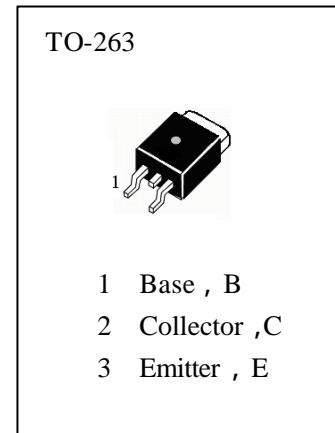
HP127W

APPLICATIONS

PNP Epitaxial Darlington Transistor. High DC Current Gain.
Monolithic Construction with Built-In Base-Emitter Shunt Resistors.

ABSOLUTE MAXIMUM RATINGS ($T_a=25^\circ C$)

T_{stg}	—Storage Temperature.....	-55~150
T_j	—Junction Temperature.....	150
P_c	—Collector Dissipation($T_c=25^\circ C$).....	65W
P_c	—Collector Dissipation ($T_a=25^\circ C$)	2W
V_{CBO}	—Collector-Base Voltage.....	-100V
V_{CEO}	—Collector-Emitter Voltage.....	-100V
V_{EBO}	—Emitter-Base Voltage.....	-5V
I_c	—Collector Current(DC).....	-5A
I_c	—Collector Current (Pulse)	-8A
I_b	—Base Current.....	-120mA



ELECTRICAL CHARACTERISTICS ($T_a=25^\circ C$)

Symbol	Characteristics	Min	Typ	Max	Unit	Test Conditions
BVCBO	Collector-Base Breakdown Voltage	-100			V	$I_c=-1\text{mA}, I_e=0$
BVCEO	Collector-Emitter Breakdown Voltage	-100			V	$I_c=-5\text{mA}, I_b=0$
HFE	*DC Current Gain	1000				$V_{ce}=-3\text{V}, I_c=-0.5\text{A}$
VCE(sat1)	*Collector- Emitter Saturation Voltage			-2.0	V	$I_c=-3\text{A}, I_b=-12\text{mA}$
VCE(sat2)	*Collector- Emitter Saturation Voltage			-4.0	V	$I_c=-3\text{A}, I_b=-20\text{mA}$
VBE(ON)	*Base-Emitter On Voltage			-2.5	V	$V_{ce}=-3\text{V}, I_c=-3\text{A}$
ICEO	Collector Cut-off Current			-0.5	mA	$V_{cb}=-50\text{V}, I_b=0$
ICBO	Collector Cut-off Current			-0.2	mA	$V_{cb}=-100\text{V}, I_e=0$
IEBO	Emitter Cut-off Current			-2.0	mA	$V_{eb}=-5\text{V}, I_c=0$
Cob	Output Capacitance			300	pF	$V_{cb}=-10\text{V}, I_e=0, f=0.1\text{MHz}$

*Pulse Test : PW 300 μs , Duty cycle 2%



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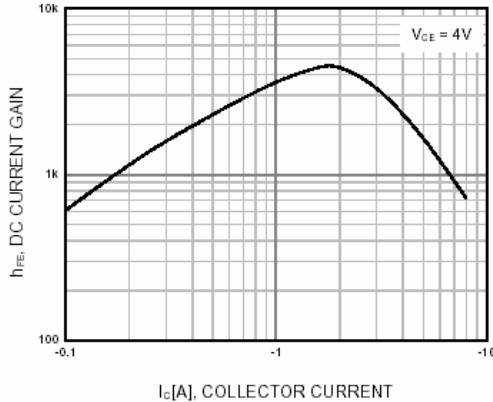


Figure 1. DC current Gain

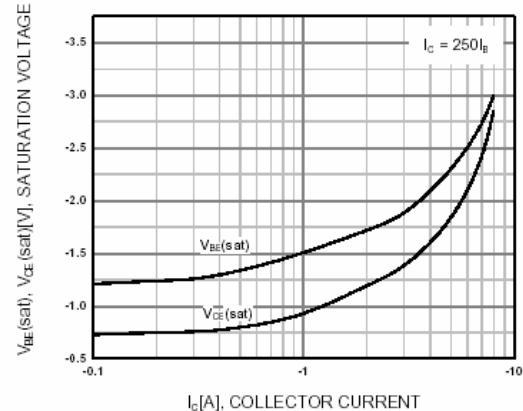


Figure 2. Base-Emitter Saturation Voltage
Collector-Emitter Saturation Voltage

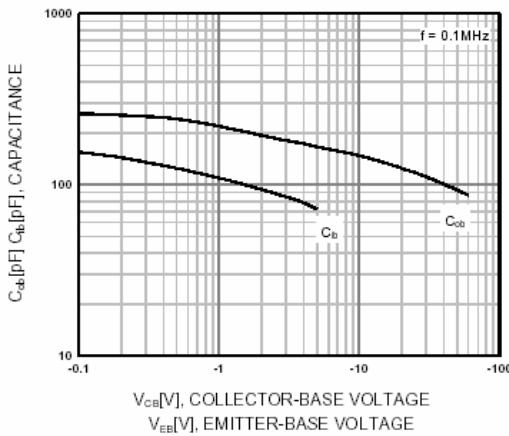


Figure 3. Output and Input Capacitance
vs. Reverse Voltage

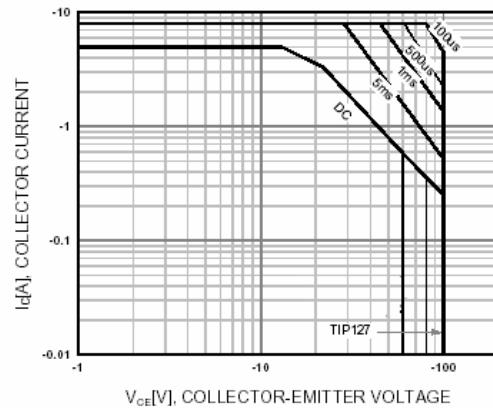


Figure 4. Safe Operating Area

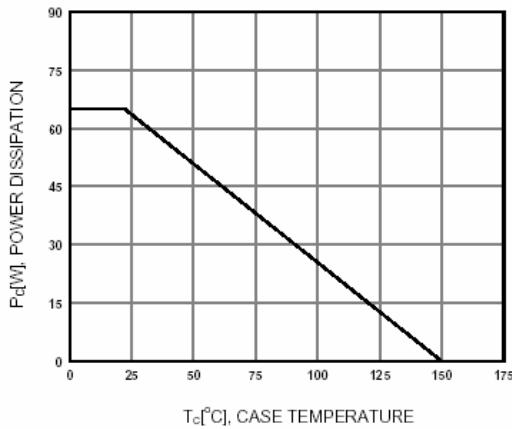


Figure 5. Power Derating