TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (Ultra-High-Speed U-MOSⅢ)

TK55D10J1

Switching Regulator Applications

· High-Speed switching

• Low gate charge: Q_g = 110 nC (typ.)

• Low drain-source ON resistance: $R_{DS (ON)} = 8.4 \text{ m}\Omega \text{ (typ.)}$

High forward transfer admittance: |Y_{fs}| = 110 S (typ.)

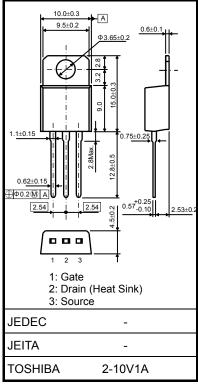
• Low leakage current: I_{DSS} = 10 μA (max) (V_{DS} = 100 V)

• Enhancement mode: V_{th} = 1.1 to 2.3 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics			Symbol	Rating	Unit
Drain-source voltage			V_{DSS}	100	V
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)			V_{DGR}	100	V
Gate-source voltage			V_{GSS}	±20	V
Drain current	DC	(Note 1)	I _D	55	А
	Pulse	(Note 1)	I_{DP}	210	A
Drain power dissipation (Tc = 25°C)			P_{D}	140	W
Single pulse avalanche energy (Note 2)			E _{AS}	382	mJ
Avalanche current			I _{AR}	55	Α
Repetitive avalanche energy (Note 3)			E _{AR}	9.4	mJ
Channel temperature			T _{ch}	150	°C
Storage temperature range			T _{stg}	-55 to 50	°C

Unit: mm



Weight: 1.35 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	0.89	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	83.3	°C/W

Note 1: Ensure that the channel and lead temperatures do not exceed 150°C.

Note 2: $V_{DD} = 25$ V, $T_{ch} = 25$ °C, L = 200 μH , $I_{AR} = 55$ A , $R_G = 1\Omega$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

Internal Connection



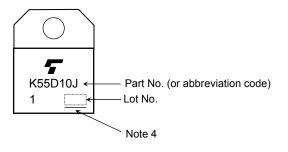
Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА
Drain cut-OFF cu	rrent	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V	_	_	10	μА
Drain-source breakdown voltage		V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	100	_		V
		V (BR) DSX	I _D = 10 mA, V _{GS} = -20 V	55	_	_	
Gate threshold vo	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	1.1	_	2.3	V
Drain-source ON resistance		R _{DS} (ON)	V _{GS} = 4.5 V, I _D = 27A	_	9.0	12.0	m0
			V _{GS} = 10 V, I _D = 27A	_	8.4	10.5	mΩ
Forward transfer admittance		Y _{fs}	V _{DS} = 10 V, I _D = 27 A		110	_	S
Input capacitance		C _{iss}		_	5700	_	pF
Reverse transfer capacitance		C _{rss}	V _{DS} = 10V, V _{GS} = 0 V, f = 1 MHz	_	390	_	
Output capacitance		C _{oss}		_	1000	_	
Switching time	Rise time	t _r	V_{GS} $0 V$ $V_{DD} \simeq 50 V$	_	7	_	- ns
	Turn-ON time	t _{on}		_	30		
	Fall time	t _f		_	20		
	Turn-OFF time	t _{off}	Duty \leq 1%, t _W = 10 μs	_	130		
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \simeq 80 \text{ V}, V_{GS} = 5 \text{ V}, I_D = 55 \text{A}$	_	63	_	
			$V_{DD} \simeq 80 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 55 \text{A}$	_	110	_]
Gate-source charge 1		Q _{gs1}			17		nC
Gate-drain ("miller") charge		Q _{gd}	$V_{DD} \approx 80 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 55A$		32		
Gate switch charge		Qsw		_	38		

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I_{DR}	_	_	_	55	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	220	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 55 A, V _{GS} = 0 V	_	-0.9	-1.2	V
Reverse recovery time	t _{rr}	$I_{DR} = 55 \text{ A}, V_{GS} = 0 \text{ V},$	_	67	_	ns
Reverse recovery charge	Qrr	dI _{DR} /dt = 50 A/μs	_	84	_	nC

Marking

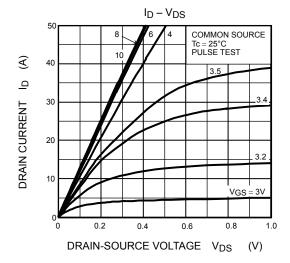


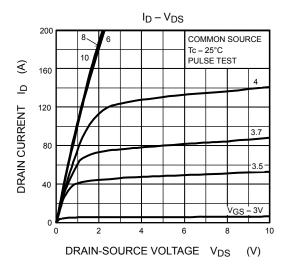
Note 4: A line under a Lot No. identifies the indication of product Labels.

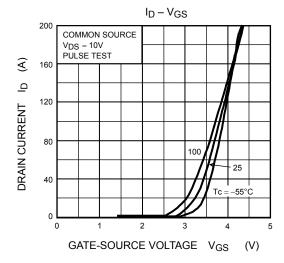
Not underlined: [[Pb]]/INCLUDES > MCV

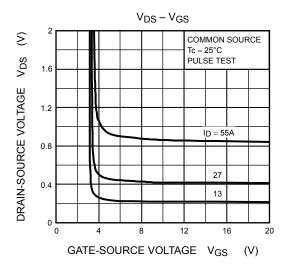
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

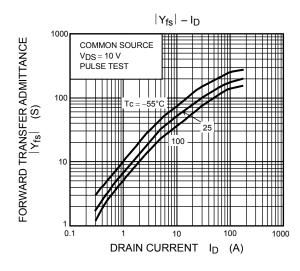
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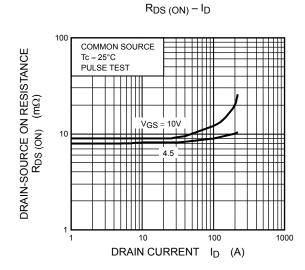


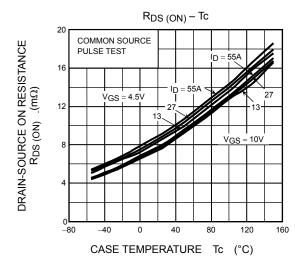


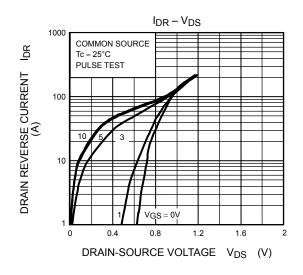


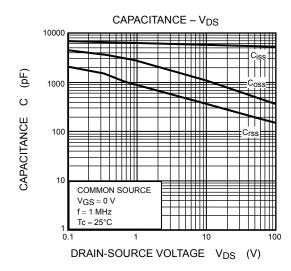


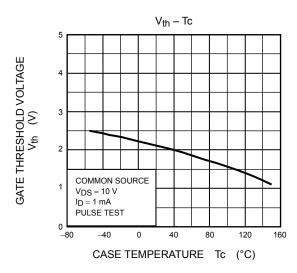


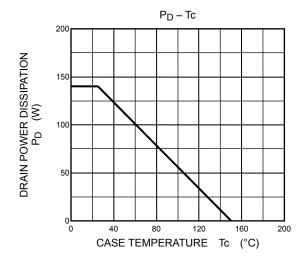


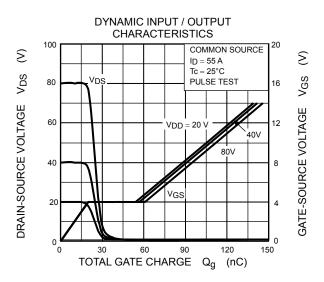


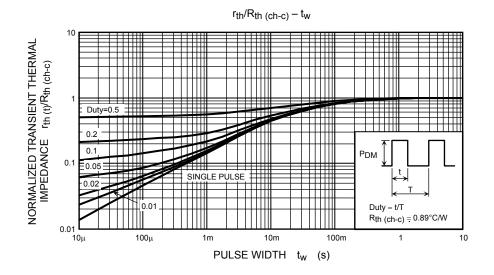


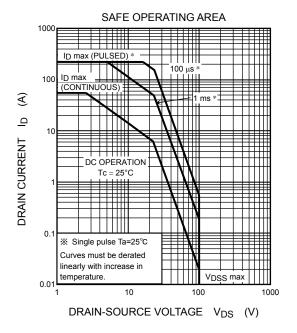


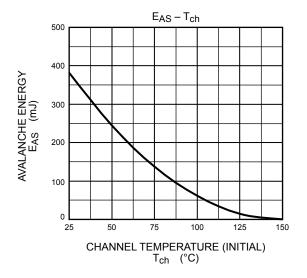


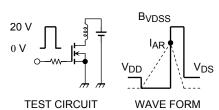












$$\begin{aligned} R_G &= 1\Omega \\ V_{DD} &= 25 \text{ V, L} = 200 \mu H \end{aligned} \qquad E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{B \text{ VDSS}}{B \text{ VDSS} - \text{ VDD}} \right)$$

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