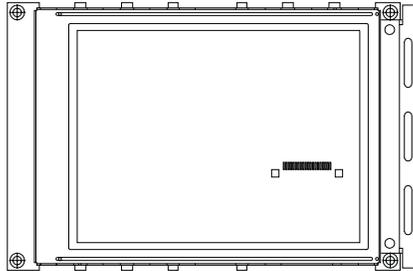


320 x 240 Dots Graphic LCD



FEATURES

- Built-in SED 1335 controller and SRAM
- Built-in negative voltage
- 1/240 duty cycle
- Touch screen option (analog type)
- Temperature compensation option

MECHANICAL DATA		
ITEM	STANDARD VALUE	UNIT
Module Dimension	166.8 x 109.0	mm
Viewing Area	122.0 x 92.0	mm
Dot Size	0.34 x 0.34	mm
Dot Pitch	0.36 x 0.36	mm
Mounting Hole	152.0 x 101.0	mm

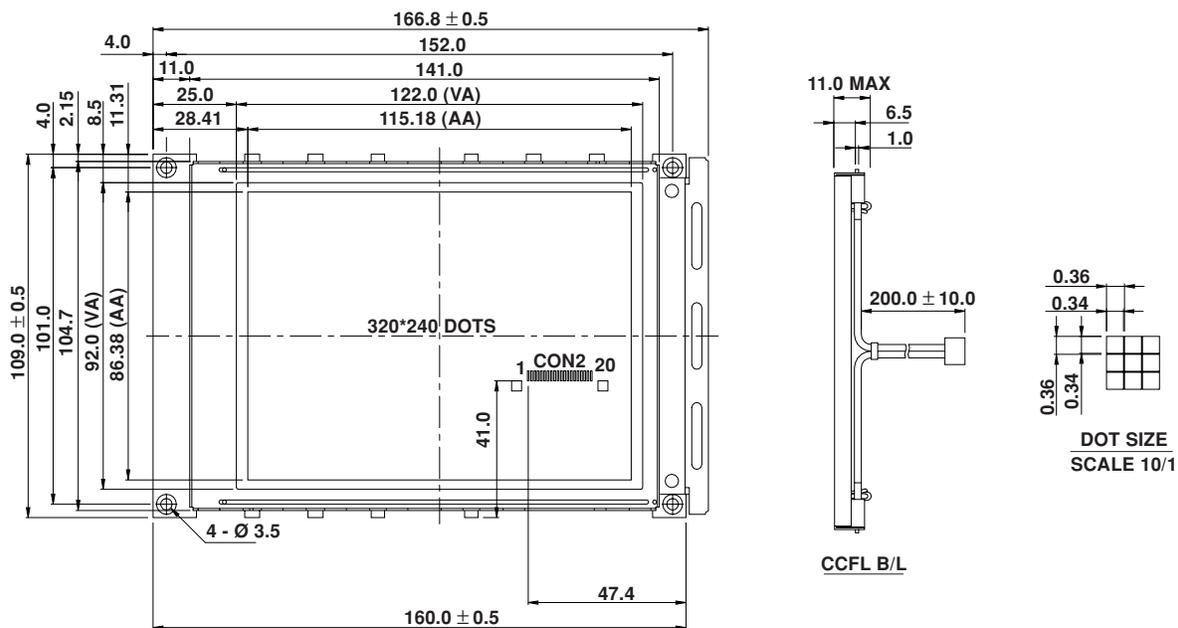
ABSOLUTE MAXIMUM RATING					
ITEM	SYMBOL	STANDARD VALUE			UNIT
		MIN.	TYP.	MAX.	
Power Supply	VDD-VSS	4.75	5.0	5.25	V
Input Voltage	VI	- 0.3	-	VDD	V

NOTE: VSS = 0 Volt, VDD = 5.0 Volt

ELECTRICAL SPECIFICATIONS						
ITEM	SYMBOL	CONDITION	STANDARD VALUE			UNIT
			MIN.	TYP.	MAX.	
Input Voltage	VDD	L level	$0.7V_{DD}$	-	V_{DD}	V
	VIO	H level	0	-	$0.3V_{DD}$	V
Supply Current	IDD	VDD = 5V	-	100	105	mA
Recommended LC Driving Voltage for Normal Temp. Version Module	VDD - V0	0°C	22.0	23.0	24.0	V
		25°C	21.3	22.2	23.0	
		50°C	19.5	20.8	22.1	
CCFL Starting Voltage	VFLS	25°C	-	600	-	Vrms
CCFL Driving Voltage	VFLD	25°C	-	268	-	Vrms
CCFL Driving Current	IFLD	VFQ = 450Vrms 30KHz	-	5.0	-	mArms

PIN NUMBER	SYMBOL	FUNCTION
1	Vss	Ground
2	Vdd	Power Supply for Logic
3	Vo	Driving Voltage for LCD
4	Ao	Data Type Select
5	WR	8080 Family: Write Signal, 6800 Family: R/W Signal
6	RD	8080 Family: Read Signal, 6800 Family: Enable Clock
7	DB0	Data Bus Line
8	DB1	Data Bus Line
9	DB2	Data Bus Line
10	DB3	Data Bus Line
11	DB4	Data Bus Line
12	DB5	Data Bus Line
13	DB6	Data Bus Line
14	DB7	Data Bus Line
15	CS	Chip Select, Active L
16	RES	Controller Rest Signal Active L
17	Vee	Negative Voltage Output
18	SEL1	H: 68, L: 80
19	FGND	Frame Ground
20	NC	No Connection

DIMENSIONS in millimeters





Disclaimer

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