

SANYO Semiconductors DATA SHEET

P-Channel Silicon MOSFET

ECH8617—General-Purpose Switching Device Applications

Features

- · Ultrahigh-speed switching.
- · 4V drive.
- · Composite type, facilitating high-density mounting.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		-100	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ID		-1.5	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	-12	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (900mm ² X0.8mm) 1unit	1.3	W
Total Power Dissipation	PT	Mounted on a ceramic board (900mm ² X0.8mm)	1.5	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions		Ratings		
			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	I _D =-1mA, V _G S=0V	-100			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =-100V, V _{GS} =0V			-1	μΑ
Gate-to-Source Leakage Current	IGSS	VGS=±16V, VDS=0V			±10	μΑ
Cutoff Voltage	VGS(off)	V _{DS} =-10V, I _D =-1mA	-1.2		-2.6	V
Forward Transfer Admittance	yfs	V _{DS} =-10V, I _D =-1A	1.8	3.1		S
Static Drain-to-Source On-State Resistance	RDS(on)1	ID=-1A, VGS=-10V		400	520	mΩ
	R _{DS} (on)2	I _D =-0.5A, V _G S=-4V		460	645	mΩ
Input Capacitance	Ciss	V _{DS} =-20V, f=1MHz		630		pF
Output Capacitance	Coss	V _{DS} =-20V, f=1MHz		41		pF
Reverse Transfer Capacitance	Crss	V _{DS} =-20V, f=1MHz		38		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit.		10.0		ns
Rise Time	tr	See specified Test Circuit.		5.5		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		91		ns
Fall Time	tf	See specified Test Circuit.		27		ns

Marking: FK Continued on next page.

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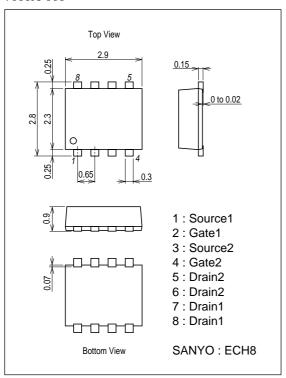
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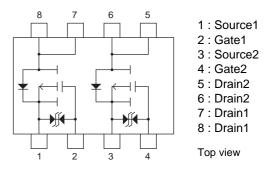
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Total Gate Charge	Qg	V _{DS} =-50V, V _{GS} =-10V, I _D =-1.5A		14.7		nC
Gate-to-Source Charge	Qgs	V _{DS} =-50V, V _{GS} =-10V, I _D =-1.5A		1.6		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =-50V, V _{GS} =-10V, I _D =-1.5A		2.8		nC
Diode Forward Voltage	VSD	IS=-1.5A, VGS=0V		-0.80	-1.2	V

Package Dimensions

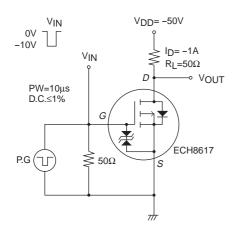
unit : mm 7011A-001

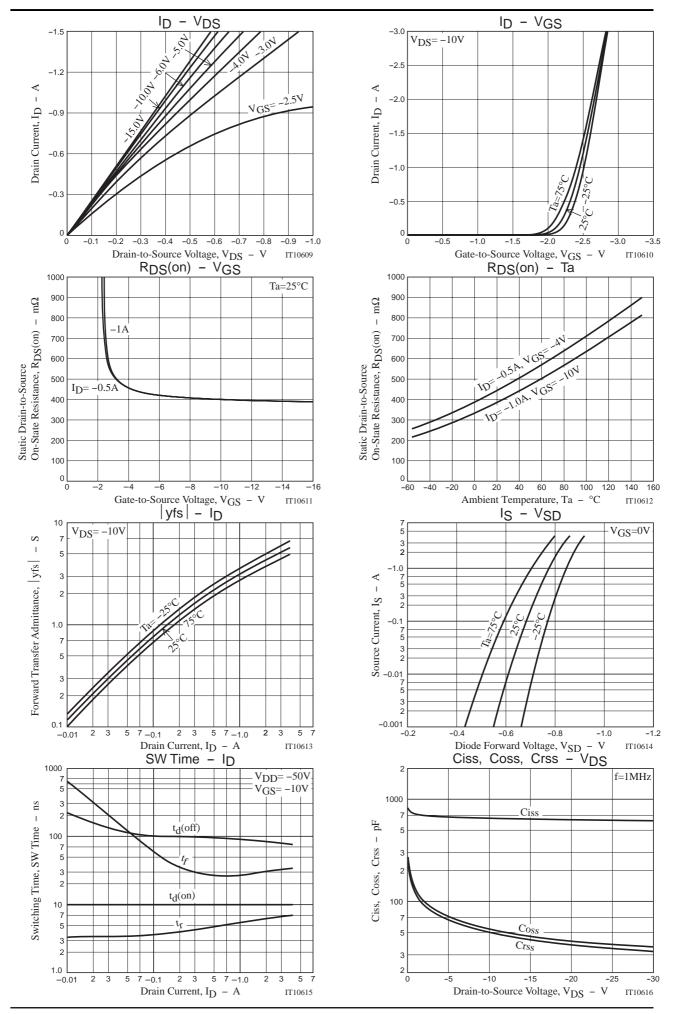


Electrical Connection

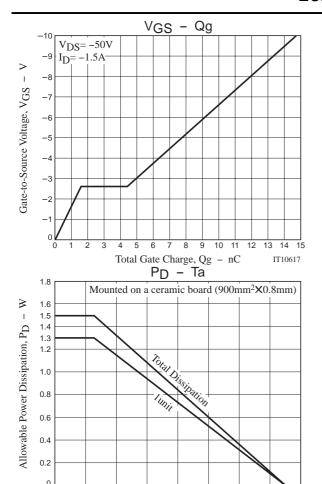


Switching Time Test Circuit





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80

Ambient Temperature, Ta

100

120

− °C

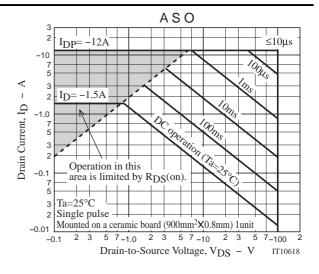
140

160

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0

20



Note on usage: Since the ECH8617 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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