





DMN3730UFB 30V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	R _{DS(on)}	Ι _D Τ _A = 25°C
30V	460mΩ @ V _{GS} = 4.5V	0.9A
30 V	560mΩ @ V _{GS} = 2.5V	0.7A

Description and Applications

This MOSFET has been designed to minimize the on-state resistance $(R_{DS(on)})$ and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Load switch
- Portable applications
- Power Management Functions

Features and Benefits

- 0.5mm ultra low profile package for thin application
- 0.6mm² package footprint, 10 times smaller than SOT23
- Low V_{GS(th)}, can be driven directly from a battery
- Low R_{DS(on)}
- "Lead Free", RoHS Compliant (Note 1)
- Halogen and Antimony Free. "Green" Device (Note 2)
- ESD Protected Gate 2kV
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

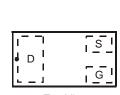
- Case: DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.001 grams (approximate)

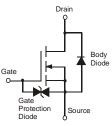




DFN1006-3

Bottom View





Top View Internal Schematic

Equivalent Circuit

Ordering Information (Note 3)

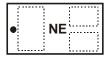
Part Number	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
DMN3730UFB-7	NE	7	8	3000

Notes: 1. No purposefully added lead

2. Diodes Inc's "Green" policy can be found on our website at http://www.diodes.com.

3. For packaging details, go to our website at http://www.diodes.com.

Marking Information



NE = Product Type Marking Code Dot Denotes Drain Side





DMN3730UFB

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	30	V
Gate-Source Voltage			V _{GSS}	±8	V
		(Note 5)	I	0.91	
Continuous Drain Current	$V_{GS} = 4.5V$	T _A =70°C (Note 5)	ID	0.73	А
		(Note 4)		0.75	
Pulsed Drain Current (Note 6)		I _{DM}	3	A	

Thermal Characteristics @T_A = 25°C unless otherwise specified

Characteristic		Symbol	Value	Unit	
Power Dissipation	(Note 5)		0.69	W	
	(Note 4)	PD	0.47	vv	
Thermal Desistance Junction to Ambient	(Note 5)	P	180	°C/W	
Thermal Resistance, Junction to Ambient	(Note 4)	R _{0JA}	258	C/W	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C	

Electrical Characteristics @T_A = 25°C unless otherwise specified

			_				
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS			•	•	•		
Drain-Source Breakdown Voltage	BV _{DSS}	30	-	-	V	$V_{GS} = 0V, I_D = 10\mu A$	
Zero Gate Voltage Drain Current $T_J = 25^{\circ}C$	IDSS	-	-	1	μΑ	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	-	-	3	μΑ	$V_{GS} = \pm 8V, V_{DS} = 0V$	
ON CHARACTERISTICS							
Gate Threshold Voltage	V _{GS(th)}	0.45	-	0.95	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	
				460		$V_{GS} = 4.5V, I_D = 200mA$	
Static Drain-Source On-Resistance (Note 7)	R _{DS (ON)}	-	-	560	mΩ	$V_{GS} = 2.5V, I_D = 100mA$	
				730		$V_{GS} = 1.8V, I_D = 75mA$	
Forward Transfer Admittance	Y _{fs}	40	-	-	mS	$V_{DS} = 3V, I_{D} = 10mA$	
Diode Forward Voltage (Note 7)	V _{SD}	-	0.7	1.2	V	$V_{GS} = 0V, I_{S} = 300mA$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	-	64.3	-	pF		
Output Capacitance	C _{oss}	-	6.1	-	pF	$V_{DS} = 25V, V_{GS} = 0V,$ f = 1.0MHz	
Reverse Transfer Capacitance	Crss	-	4.5	-	pF		
Gate Resistance	Rg	-	70	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge	Qg	-	1.6	-	nC		
Gate-Source Charge	Q _{gs}	-	0.2	-	nC	−V _{GS} = 4.5V, V _{DS} = 15V, −I _D = 1A	
Gate-Drain Charge	Q _{gd}	-	0.2	-	nC	ID = IA	
Turn-On Delay Time	t _{D(on)}	-	3.5	-	ns		
Turn-On Rise Time	tr	-	2.8	-	ns	$V_{DS} = 10V, I_{D} = 1A$	
Turn-Off Delay Time	t _{D(off)}	-	38	-	ns	$V_{GS} = 10V, R_G = 6\Omega$	
Turn-Off Fall Time	tf	-	13	-	ns		

4. For a device surface mounted on a minimum recommended pad layout of an FR4 PCB, in still air conditions; the device is measured when operating in Notes: steady-state condition.

5. Same as note 4, except the device measured at t \leq 10 sec.

6. Same as note 4, except the device is pulsed at duty cycle of 1% for a pulse width of 10μ s.

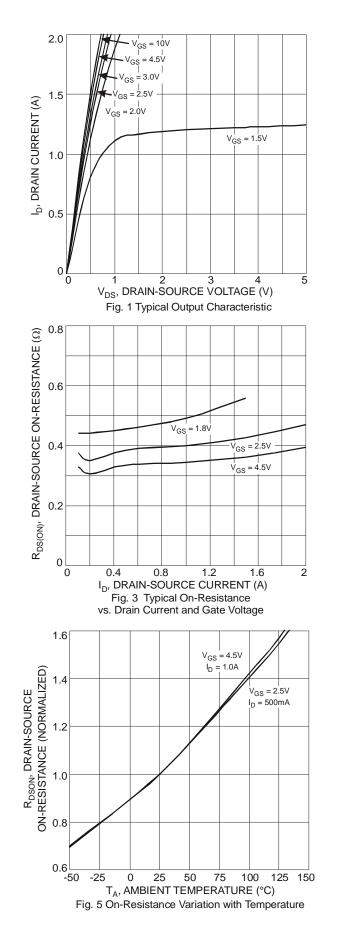
7. Measured under pulsed conditions to minimize self-heating effect. Pulse width \leq 300 μ s; duty cycle \leq 2%

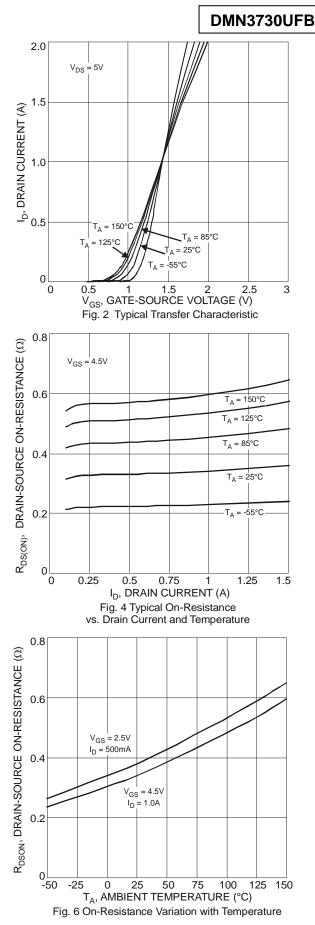
8. For design aid only, not subject to production testing.



A Product Line of Diodes Incorporated

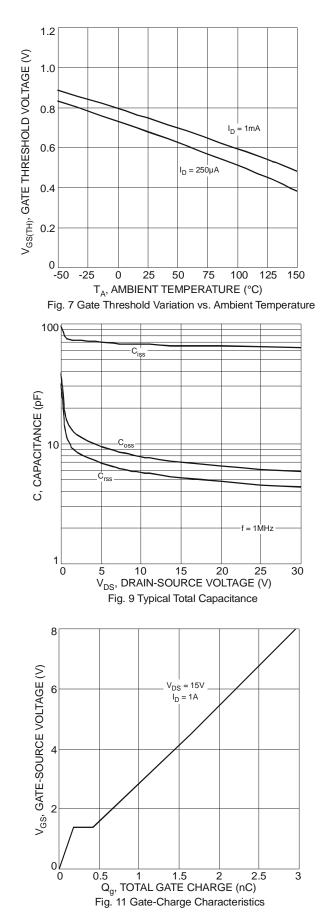


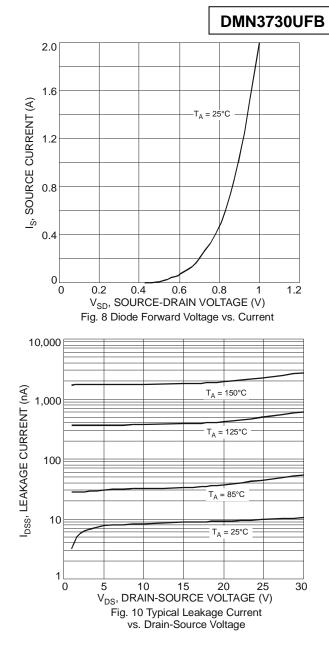








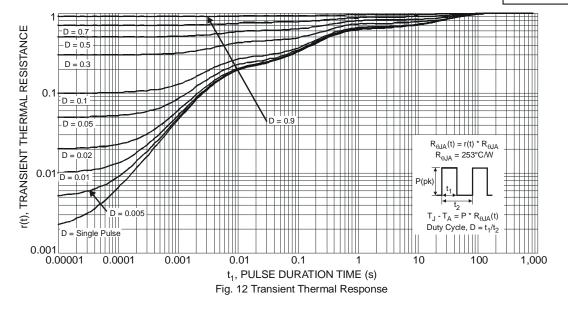




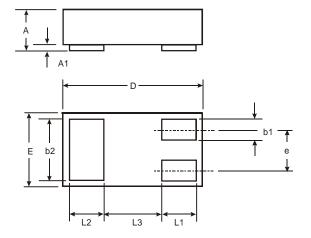




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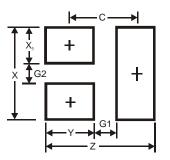


Package Outline Dimensions



DFN1006-3					
Dim	Min	Max	Тур		
Α	0.47	0.53	0.50		
A1	0	0.05	0.03		
b1	0.10	0.20	0.15		
b2	0.45	0.55	0.50		
D	0.95	1.075	1.00		
Ε	0.55	0.675	0.60		
е			0.35		
L1	0.20	0.30	0.25		
L2	0.20	0.30	0.25		
L3		_	0.40		
All Dimensions in mm					

Suggested Pad Layout



Dimensions	Value (in mm)
Z	1.1
G1	0.3
G2	0.2
Х	0.7
X1	0.25
Y	0.4
С	0.7





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