

GI1386

PNP EPITAXIAL SILICON TRANSISTOR

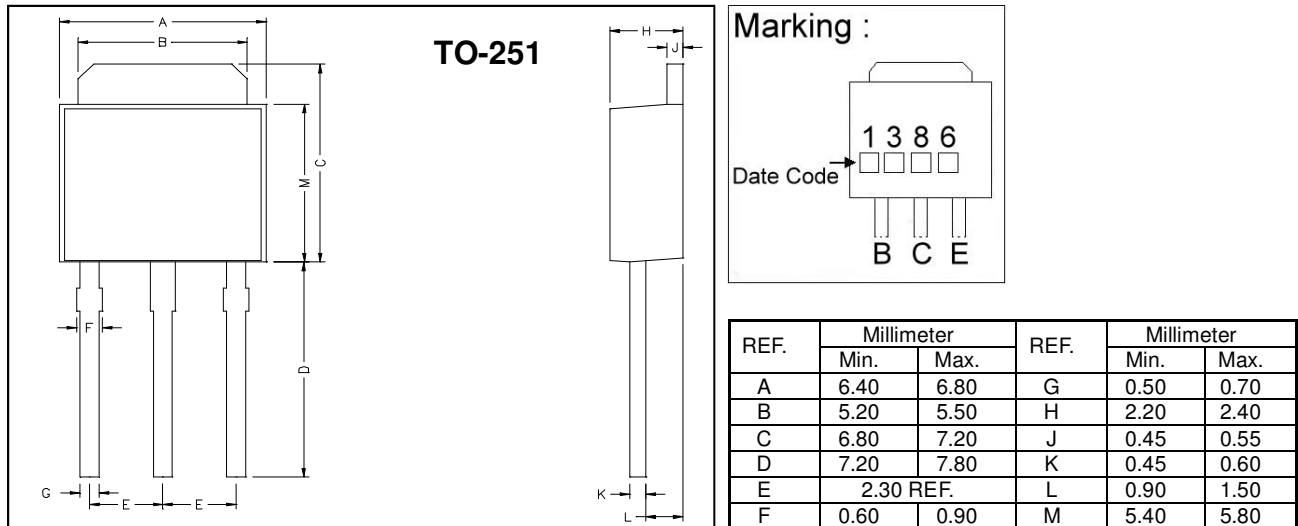
Description

The GI1386 is designed for low frequency applications.

Features

- Low $V_{CE(sat)} = -0.55V$ (Typ.) ($I_C/I_B = -4A/-0.1A$)
- Excellent DC current gain characteristics

Package Dimensions



Absolute Maximum Ratings at $T_a = 25^\circ C$

Parameter	Symbol	Ratings	Unit
Junction Temperature	T_j	+150	$^\circ C$
Storage Temperature	T_{stg}	-55~+150	$^\circ C$
Collector to Base Voltage	V_{CBO}	-30	V
Collector to Emitter Voltage	V_{CEO}	-20	V
Emitter to Base Voltage	V_{EBO}	-6	V
Collector Current	I_C	-5	A
*Collector Current (Pulse)	I_C	-10	A
Total Power Dissipation ($T_c=25^\circ C$)	P_D	20	W

Electrical Characteristics ($T_a = 25^\circ C$)

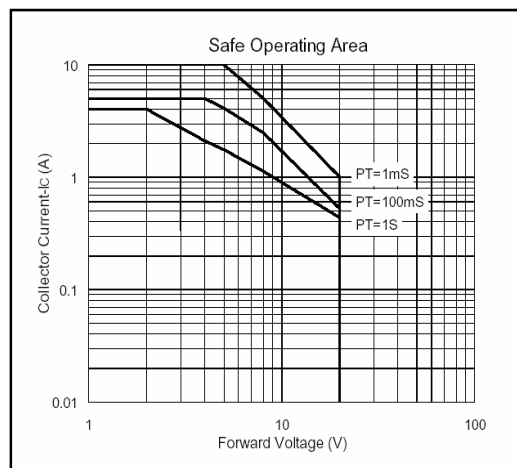
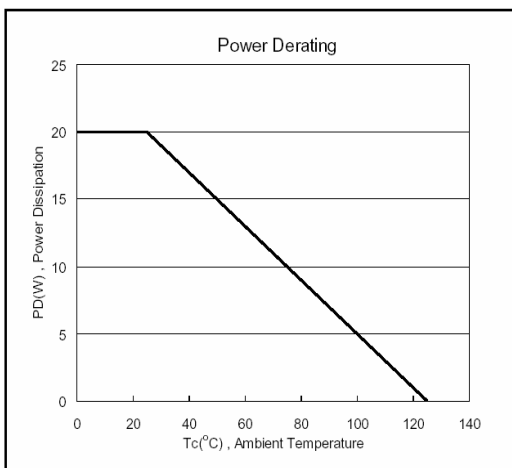
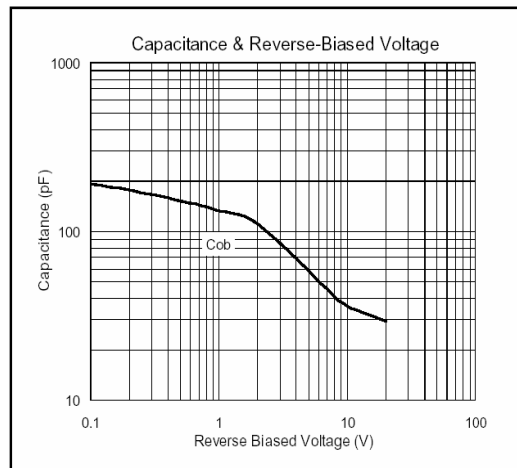
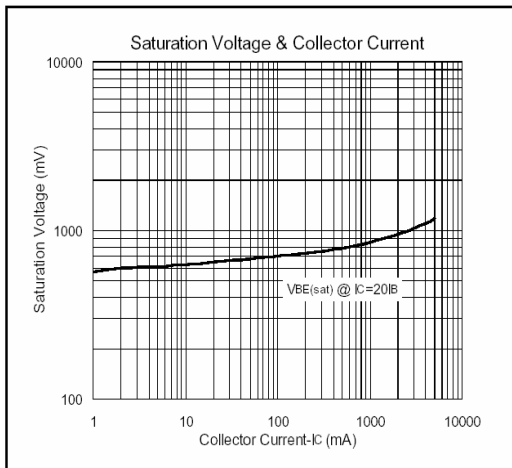
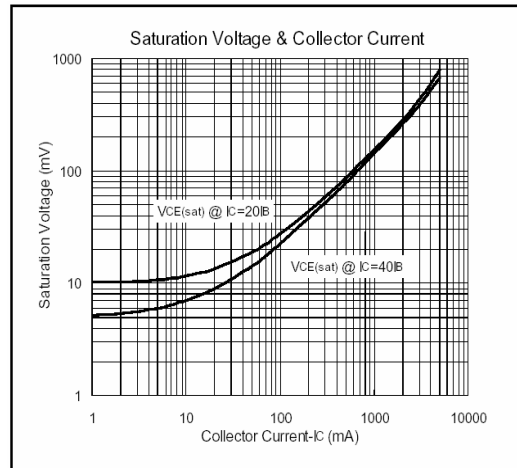
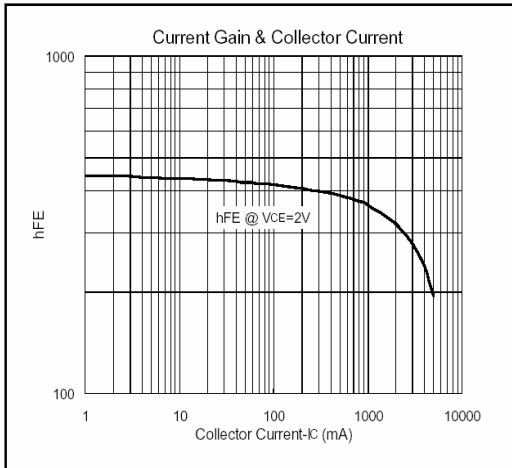
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
V_{CBO}	-30	-	-	V	$I_C = -50\mu A, I_E = 0$
V_{CEO}	-20	-	-	V	$I_C = -1mA, I_B = 0$
V_{EBO}	-6	-	-	V	$I_E = -50\mu A, I_C = 0$
I_{CBO}	-	-	-500	nA	$V_{CB} = -20V, I_E = 0$
I_{EBO}	-	-	-500	nA	$V_{EB} = -5V, I_C = 0$
* $V_{CE(sat)}$	-	-	-1	V	$I_C = -4A, I_B = -0.1A$
* h_{FE}	82	-	580		$V_{CE} = -2V, I_C = -0.5A$
f_T	-	120	-	MHz	$V_{CE} = -6V, I_E = 50mA, f = 30MHz$
C_{ob}	-	60	-	pF	$V_{CB} = -20V, I_E = 0, f = 1MHz$

* Pulse Test: Pulse Width $\leq 380\mu s$, Duty Cycle $\leq 2\%$

Classification Of h_{FE}

Rank	P	Q	R	E
Range	82 - 180	120 - 270	180 - 390	370 - 580

Characteristics Curve



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