



# AU9226

**USB KVM Controller**

Technical Reference Manual



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**<Memo>**

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# 1. Introduction

## 1.1 Description

AU9226 is a highly integrated single chip USB KVM controller offering users the possibility of sharing keyboard, mouse and monitor among up to two PCs. Not like traditional PS2 KVM devices, AU9226 uses USB as its upstream and downstream interface to support its driver-free and plug-n-play features, which allow users to easily install and use the KVM.

Typical application of AU9226 is under an environment, in which only one set of monitor, keyboard and mouse is available. Applying such kind of devices in a computing environment will not only result in a centralized control, but also reduce the power and space consumption.

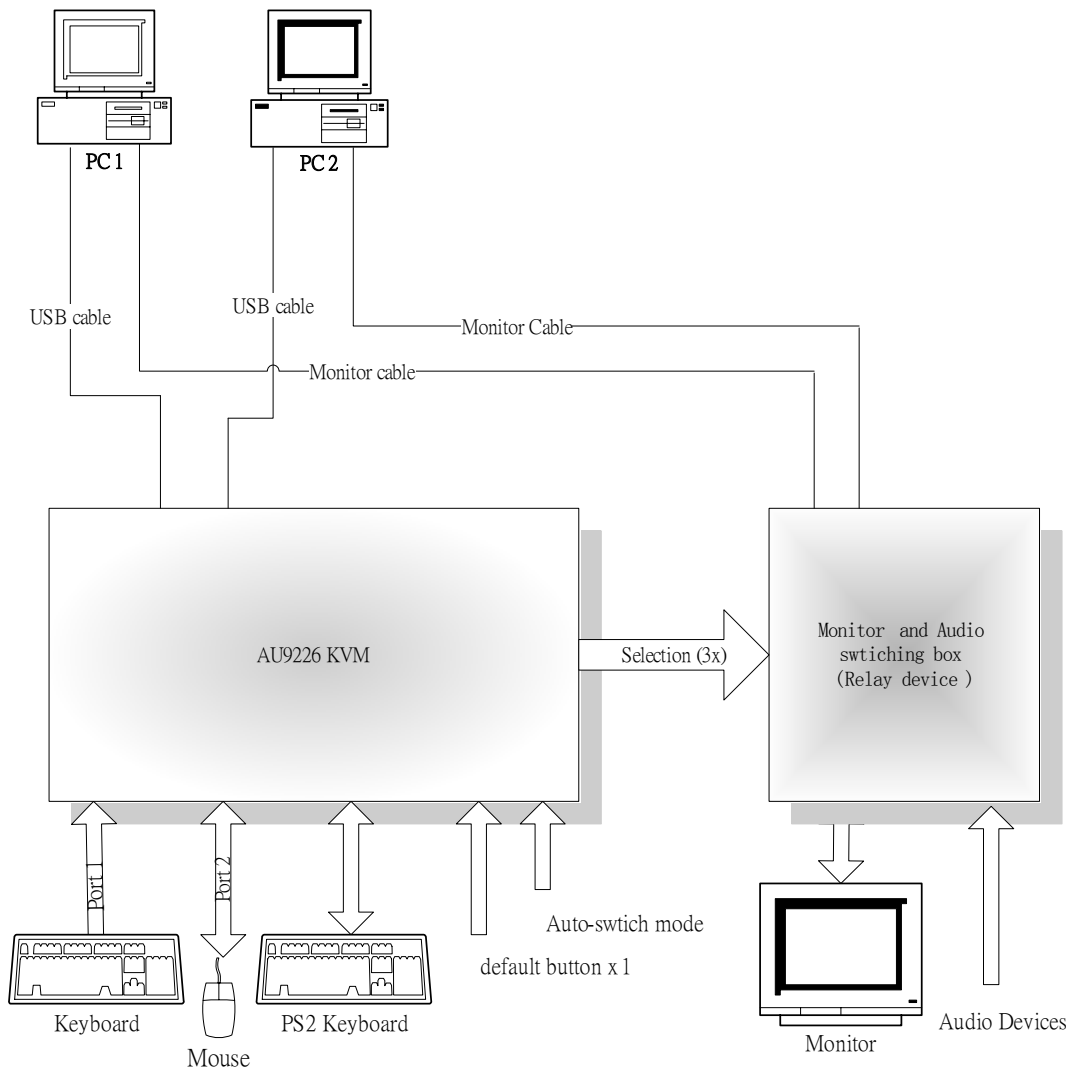
## 1.2 Features

- Fully USB v1.1 specification compliant
- Support USB bus power and device self power
- Support up to 2 PCs via 2 USB upstream ports
- Support USB downstream port 1 for USB keyboard
- Support USB downstream port 2 for USB mouse
- Support PS2 keyboard
- Support both “panel button” and “hot key” to switch among PCs
- Support “auto-scan” mode for users to monitor PCs easily
- Support audio beep when system switches
- Support upstream port selected LED indicators for each PC respectively
- Support phantom keyboard to let user does not need to configure BIOS to turn on “No halt on error”
- Support 2 different sets of key sequence to switch among PCs (Please see the key definition in the following)
- Recalls “Caps Lock”, “Num Lock” and “Scroll Lock” keys’ status for each PC automatically
- Output the monitor selection signals to the external video-switching circuits
- Support 8-bit MCU interface to provide control from external MCU
- Support serial MCU interface to provide control from external MCU
- No need to install device driver under Windows, Macintosh, Linux, FreeBSD and Sun system
- Support EEPROM to memorize hotkey mode

## 2. Application Block Diagram

Typical application is in a computing environment where it demands a centralized control and space & energy saving is critical.

Figure 2.1 Block Diagram

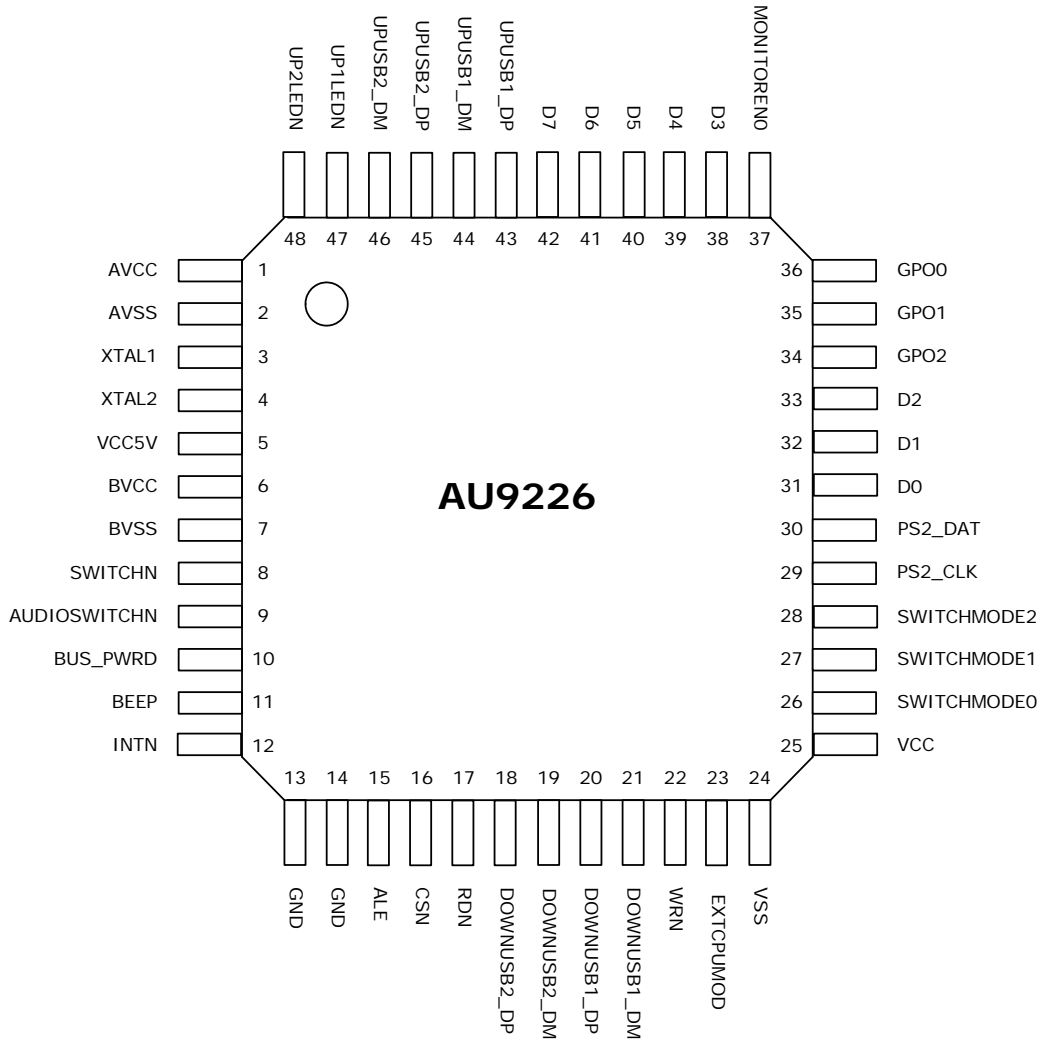




### 3. Pin Assignment

The AU9226 is delivered in 48pin LQFP form factor. Documented below is a figure shows signal names of each pin and a table in the following page describes each pin in more details.

Figure 3.1 Pin Assignment Diagram



**Table 3.1 Pin Descriptions**

Pin #	Pin Name	I/O	Description
1	AVCC	PWR	3.3V analog input power
2	AVSS	PWR	Analog ground
3	XTAL1	I	12 MHz crystal input
4	XTAL2	O	12 MHz crystal output
5	VCC5V	PWR	5.0V input power from USB port
6	BVCC	PWR	3.3V output power from internal regulator
7	BVSS	PWR	Ground pin of internal regulator
8	SWITCHN	I	Button with internal pull-up for switching to next system (0: Enable; 1: Normal)
9	AUDIOSWITCHN	I	Audio ON/OFF switch with internal pull-up (1: OFF; 0: ON)
10	BUS_PWRD	I	Bus power selection (1: Device Self Power; 0: Bus Power)
11	BEEP	O	Audio output (4 KHz frequency) when system switches
12	INTN	O	Interrupt signal output to external MCU when SWITCHMODE2='1' and EXTCPUMOD='1' (falling-edge triggered)
13	GND		Ground
14	GND		Ground
15	ALE	I	When not using external parallel MCU, it should be pulled down. Address latch enable when SWITCHMODE2='1' and EXTCPUMOD='1' (1: AD bus is address; 0: AD bus is data)
16	CSN	I	Chip enable for the MCU interface when SWITCHMODE2='1' and EXTCPUMOD='1' (0: Enable)
17	RDN	I	Read strobe for the MCU interface when SWITCHMODE2='1' and EXTCPUMOD='1' (0: Enable)
18	DOWNUSB2_DP	I/O	DownPort2 DP
19	DOWNUSB2_DM	I/O	DownPort2 DM
20	DOWNUSB1_DP	I/O	DownPort1 DP
21	DOWNUSB1_DM	I/O	DownPort1 DM
22	WRN	I	Read strobe for the MCU interface when SWITCHMODE2='1' and EXTCPUMOD = '1' (0: Enable; 1: Normal)
23	EXTCPUMOD	I	External MCU mode selection (1: Parallel; 0: Serial). Serial is the default mode.
24	VSS	PWR	Digital ground

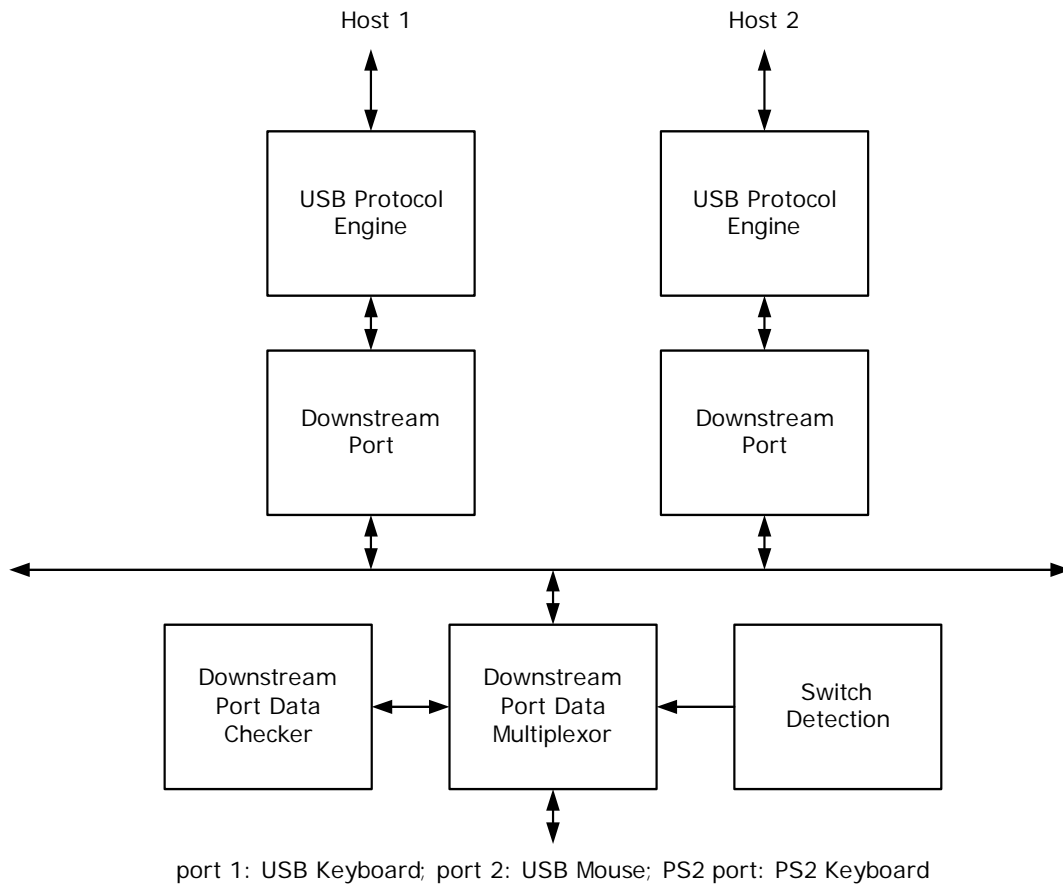
Pin #	Pin Name	I/O	Description
25	VCC	PWR	3.3V digital input power
26	SWITCHMODE0	I	Switch mode selection (SW0, SW1) for internal hotkey mode when SWITCHMODE2='0' and D2='0', it's defined as: (0, 0): [Scroll][Scroll] mode (default) (0, 1): [Caps Lock][Caps Lock] mode (1, 0): [Num Lock][Num Lock] mode (1, 1): [Ctrl][Shift] mode (left-hand side)
27	SWITCHMODE1	I	
28	SWITCHMODE2	I	
29	PS2_CLK	I/O	PS2 clock signal (external pull-up required)
30	PS2_DAT	I/O	PS2 data signal (external pull-up required)
31	D0	I/O	Bit 0 with internal pull-up in address/data bus of MCU interface when SWITCHMODE2='1' and EXTCPUMOD='1'. It also provides alternative functions: I2C bus SDA signal (slave mode) when SWITCHMODE2='1' and EXTCPUMOD='0' I2C bus SDA signal (master mode) when SWITCHMODE2='0' and D2='1'
32	D1	I/O	Bit 1 with internal pull-up in address/data bus of MCU interface when SWITCHMODE2='1' and EXTCPUMOD='1'. It also provides alternative functions: I2C bus SCL signal (slave mode) when SWITCHMODE2='1' and EXTCPUMOD='0' I2C bus SCL signal (master mode) when SWITCHMODE2='0' and D2='1'
33	D2	I/O	Bit 2 with internal pull-down in address/data bus of MCU interface when SWITCHMODE2='1' and EXTCPUMOD='1'. It also provides alternative functions: I2C bus address 0 (slave mode) when SWITCHMODE2='1' and EXTCPUMOD='0' I2C bus master mode enable bit when SWITCHMODE2='0'
34	GPO2	O	General purpose output pin 2
35	GPO1	O	General purpose output pin 1
36	GPO0	O	General purpose output pin 0
37	MONITOREN0	O	Monitor Select : ('0' : Monitor1 Select ; '1' : Monitor2 Select)
38	D3	I/O	Data3 or Address3 when SWITCHMODE2='1' and EXTCPUMOD='1'; I2C slave address1 when SWITCHMODE2='1' and EXTCPUMOD='0';
39	D4	I/O	Data4 or Address4 when SWITCHMODE2='1' and EXTCPUMOD='1'; I2C slave address2 when SWITCHMODE2='1' and EXTCPUMOD='0';

Pin #	Pin Name	I/O	Description
40	D5	I/O	Data5 or Address5 when SWITCHMODE2='1' and EXTCPUMOD='1'
41	D6	I/O	Data6 or Address6 when SWITCHMODE2='1' and EXTCPUMOD='1'
42	D7	I/O	Data7 or Address7 when SWITCHMODE2='1' and EXTCPUMOD='1'
43	UPUSB1_DP	I/O	Upstream Port 1 DP
44	UPUSB1_DM	I/O	Upstream Port 1 DM
45	UPUSB2_DP	I/O	Upstream Port 2 DP
46	UPUSB2_DM	I/O	Upstream Port 2 DM
47	UP1LEDN	O	System 1 LED Indicator
48	UP2LEDN	O	System 2 LED Indicator

# 4. System Architecture and Reference Design

## 4.1 AU9226 Block Diagram

Figure 4.1 AU9226 Block Diagram



## 5. Hot Key Definition

AU9226 supports 2 sets of “Hot Key” to help users to switch PC, rapidly and easily, via hot key sequence defined using keypads on the left hand side of a keyboard.

### 5.1 [Scroll] ++ [Scroll] mode

Hot Key Definition	
[Scroll] ++ [Scroll] + 1 ( <b>note1</b> )	Switch to PC1
[Scroll] ++ [Scroll] + 2	Switch to PC2
[Scroll] ++ [Scroll] + → or ↓	Switch to Next PC
[Scroll] ++ [Scroll] + ← or ↑	Switch to Previous PC
[Scroll] ++ [Scroll] + B	Beeper ON/OFF ( <b>note2</b> )
[Scroll] ++ [Scroll] + S	Auto-scan mode every 10 second. ([ESC] switch to regular key code)
[Scroll] ++ [Scroll] + S + n	Set Auto-scan rate ( <b>note3</b> ). ([ESC] switch to regular key code)
[Scroll]++[Scroll]+F1~F8	Customer define key ( <b>note4</b> ).
[Scroll]++[Scroll]+[Caps]	Switch to [Caps]++[Caps] hot key mode
[Scroll]++[Scroll]+[Num]	Switch to [Num]++[Num] hot key mode
[Scroll]++[Scroll]+[Ctrl]	Switch to [Ctrl]+[Shift] hot key mode

### 5.2 [Caps] ++ [Caps] mode

Hot Key Definition	
[Caps]++[Caps]+1	Switch to PC1
[Caps]++[Caps]+2	Switch to PC2
[Caps]++[Caps]+→ or ↓	Switch to Next PC
[Caps]++[Caps]+← or ↑	Switch to Previous PC
[Caps]++[Caps]+B	Beeper ON/OFF ( <b>note2</b> )
[Caps]++[Caps]+S	Auto-scan mode every 10 second. ([ESC] switch to regular key code)
[Caps]++[Caps]+S + n	Set Auto-scan rate ( <b>note3</b> ). ([ESC] switch to regular key code)
[Caps]++[Caps]+F1~F8	Customer define key ( <b>note4</b> )
[Caps]++[Caps]+[Scroll]	Switch to [Scroll]++[Scroll] hot key mode
[Caps]++[Caps]+[Num]	Switch to [Num]++[Num] hot key mode
[Caps]++[Caps]+[Ctrl]	Switch to [Ctrl]+[Shift] hot key mode

### 5.3 [Num] ++ [Num] Mode

Hot Key Definition	
[Num]++[Num]+1	Switch to PC1
[Num]++[Num]+2	Switch to PC2
[Num]++[Num]+→ or ↓	Switch to Next PC
[Num]++[Num]+← or ↑	Switch to Previous PC
[Num]++[Num]+B	Beeper ON/OFF ( <b>note2</b> )
[Num]++[Num]+S	Auto-scan mode every 10 second. ([ESC] switch to regular key code)
[Num]++[Num]+S + n	Set Auto-scan rate ( <b>note3</b> ). ([ESC] switch to regular key code)
[Num]++[Num]+F1~F8	Customer define key ( <b>note4</b> )
[Num]++[Num]+[Scroll]	Switch to [Scroll]++[Scroll] hot key mode
[Num]++[Num]+[Caps]	Switch to [Caps]++[Caps] hot key mode
[Num]++[Num]+[Ctrl]	Switch to [Ctrl]+[Shift] hot key mode

### 5.4 [Ctrl] + [Shift] mode (left-hand side)

Hot Key Definition	
[Ctrl]++[Ctrl]	Switch to next live PC
[Ctrl + Shift + 1]	Switch to PC1
[Ctrl + Shift + 2]	Switch to PC2
[Ctrl + Shift + → or ↓]	Switch to Next PC
[Ctrl + Shift + ← or ↑]	Switch to Previous PC
[Ctrl + Shift + B]	Beeper ON/OFF ( <b>note2</b> )
[Ctrl + Shift + S]	Auto-scan mode every 10 second. ([ESC] switch to regular key code)
[Ctrl + Shift + S + n]	Set Auto-scan rate ( <b>note3</b> ). ([ESC] switch to regular key code)
[Ctrl + Shift + F1~F8]	Customer define key ( <b>note4</b> )
[Ctrl + Shift + Scroll]	Switch to [Scroll]++[Scroll] hot key mode
[Ctrl + Shift + Caps]	Switch to [Caps]++[Caps] hot key mode
[Ctrl + Shift + Num]	Switch to [Num]++[Num] hot key mode

**Note 1:** “++” means press key rapidly, “[Scroll]++[Scroll]+1” means press “Scroll” twice rapidly and then press “1”. If system receives “Scroll ++ Scroll” command only, system will reset the buffer when it does not receive any input comes after within a defined interval of time (say, 5 seconds).

**Note 2:** Audible feedback when beeper is ON and keep the device in silent mode when beeper is OFF.

**Note 3:** n = number from 1 to 9, each represents 5 seconds' interval latency to 60 seconds correspondingly.

Auto-scan rate set	
【N】 number	Second
1	5
2	10
3	15
4	20
5	25
6	30
7	35
8	40
9	60

**Note 4:**

	GPO 2	GPO 1	GPO 0
Function key +F1	0	0	0
Function key +F2	0	0	1
Function key +F3	0	1	0
Function key +F4	0	1	1
Function key +F5	1	0	0
Function key +F6	1	0	1
Function key +F7	1	1	0
Function key +F8	1	1	1



## 6. Electrical Characteristics

### 6.1 Recommended Operating Conditions

Table 6.1 Recommended Operating Conditions

SYMBOL	PARAMETER	MIN	TYP	MAX	UNITS
V <sub>CC</sub>	Power Supply	4.75	5	5.25	V
V <sub>IN</sub>	Input Voltage	0		V <sub>CC</sub>	V
T <sub>ORP</sub>	Operating Temperature	0		85	OC
T <sub>STG</sub>	Storage Temperature	-40		125	OC

### 6.2 General DC Characteristics

Table 6.2 General DC Characteristics

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
I <sub>IL</sub>	Input low current	no pull-up or pull-down	-1		1	μA
I <sub>IH</sub>	Input high current	no pull-up or pull-down	-1		1	μA
O <sub>Z</sub>	Tri-state leakage current		-10		10	μA
C <sub>IN</sub>	Input capacitance			5		ρF
C <sub>OUT</sub>	Output capacitance			5		ρF
C <sub>BID</sub>	Bi-directional buffer capacitance			5		ρF

### 6.3 DC Electrical Characteristics of 3.3V I/O Cells

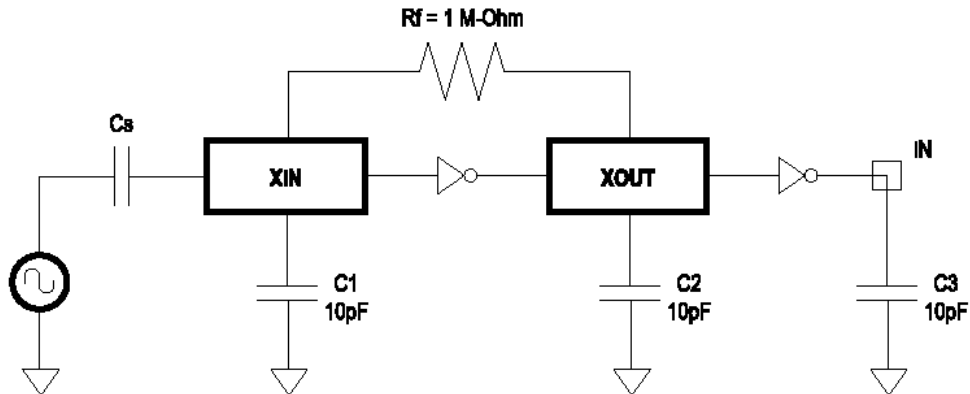
Table 6.3 DC Electrical Characteristics of 3.3V I/O Cells

SYMBOL	PARAMETER	CONDITIONS	Limits			UNIT
			MIN	TYP	MAX	
V <sub>IL</sub>	Input Low Voltage	CMOS			0.9	V
V <sub>IH</sub>	Input High Voltage	CMOS	2.3			V
V <sub>OL</sub>	Output low voltage	IOL=4mA, 16mA			0.4	V
V <sub>OH</sub>	Output high voltage	IOH=4mA, 16mA	2.4			V
R <sub>I</sub>	Input Pull-up/down resistance	Vil=0V or Vih=VCC		10k/200k		KΩ

## 6.4 Crystal Oscillator Circuit Setup for Characterization

The following setup was used to measure the open loop voltage gain for crystal oscillator circuits. The feedback resistor serves to bias the circuit at its quiescent operating point and the AC coupling capacitor, Cs, is much larger than C1 and C2.

**Figure 6.1 Crystal Oscillator Circuit Setup for Characterization**



## 6.5 ESD Test Results

**Test Description:** ESD Testing was performed on a Zapmaster system using the Human-Body –Model (HBM) and Machine-Model (MM), according to MIL\_STD 883 and EIAJ IC\_121 respectively.

- Human-Body-Model stress devices by sudden application of a high voltage supplied by a 100 PF capacitor through 1.5 Kohm resistance.
- Machine-Model stresses devices by sudden application of a high voltage supplied by a 200 PF capacitor through very low (0 ohm) resistance

### Test circuit & condition

- Zap Interval : 1 second
- Number of Zaps : 3 positive and 3 negative at room temperature
- Criteria : I-V Curve Tracing

**Table 6.4 ESD Data**

Model	Model	S/S	TARGET	Results
HBM	Vdd, Vss, I/C	15	2000V	TBD
MM	Vdd, Vss, I/C	15	200V	TBD

## 6.6 Latch-Up Test Results

**Test Description:** Latch-Up testing was performed at room ambient using an IMCS-4600 system which applies a stepped voltage to one pin per device with all other pins open except Vdd and Vss which were biased to 5 Volts and ground respectively.

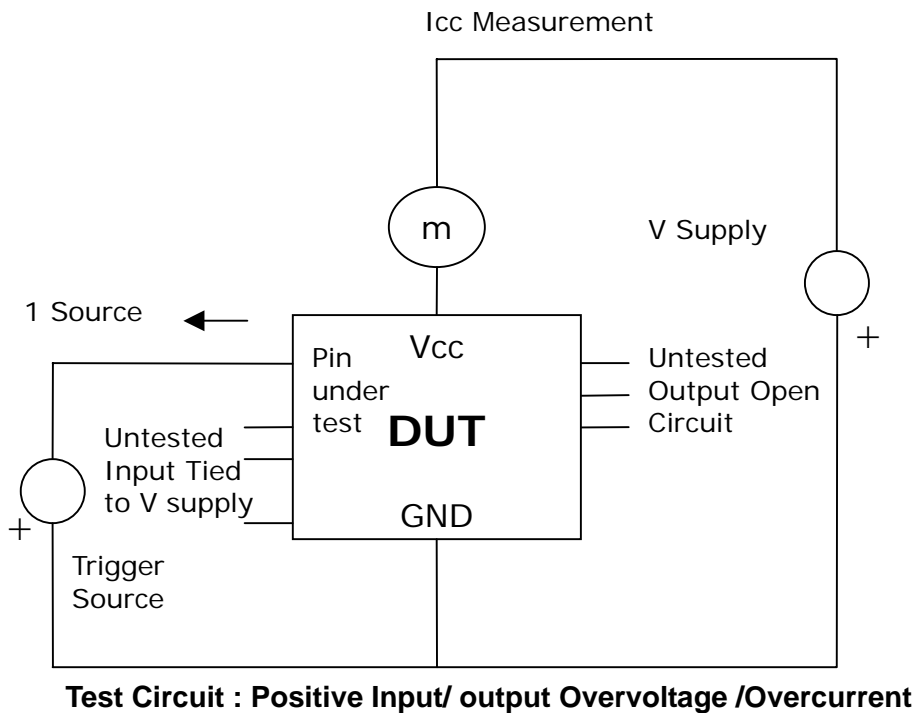
Testing was started at 5.0 V (Positive) or 0 V (Negative), and the DUT was biased for 0.5 seconds.

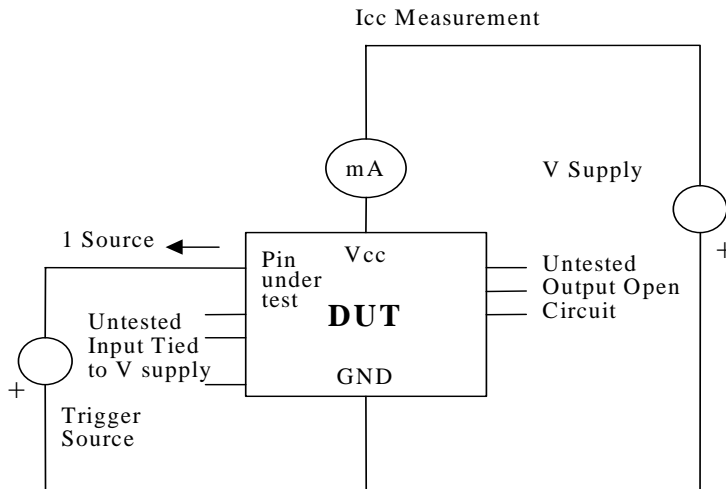
If neither the PUT current supply nor the device current supply reached the predefined limit (DUT=0 mA, I<sub>cc</sub>=100 mA), then the voltage was increased by 0.1 Volts and the pin was tested again.

This procedure was recommended by the JEDEC JC-40.2 CMOS Logic standardization committee.

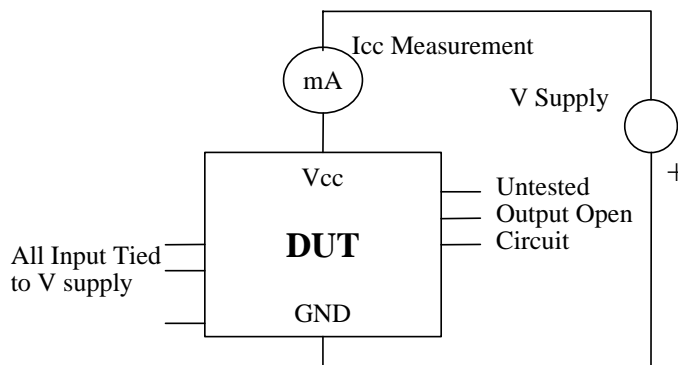
### Notes:

1. DUT: Device Under Test.
2. PUT: Pin Under Test.





**Test Circuit: Negative Input/Output Overvoltage/Overcurrent**



**Supply voltage Test**

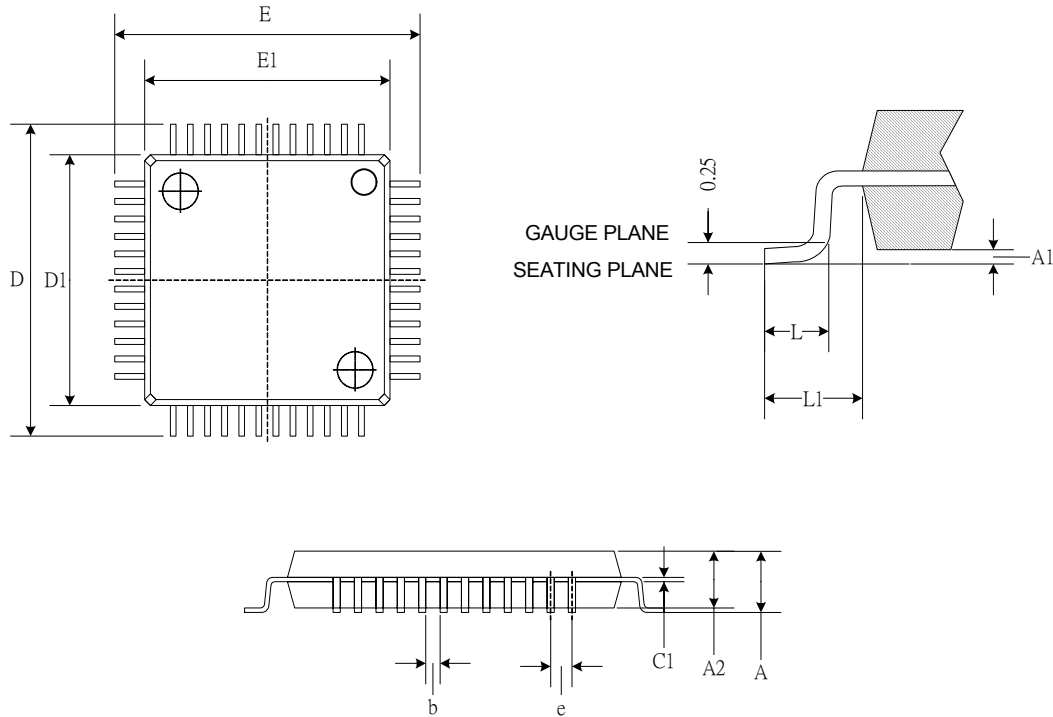
**Figure 6.2 Latch-Up Test Circuits**

Model	Model	Voltage (v)/ Current (mA)	S/S	Results
Voltage	+	11.0	5	TBD
	-	11.0		
Current	+	200	5	TBD
	-	200		
Vdd-Vxx		9.0	5	TBD

**Table 6.5 Latch-Up Data**

# 7. Mechanical Information

Figure 7.1 Mechanical Information Diagram



SYMBOLS	MIN.	MAX.
A	--	1.6
A1	0.05	0.15
A2	1.35	1.45
c1	0.09	0.16
D	9.00 BSC	
D1	7.00 BSC	
E	9.00 BSC	
E1	7.00 BSC	
e	0.5 BSC	
b	0.17	0.27
L	0.45	0.75
L1	1 REF	

1. JEDEC OUTLINE: MS-026 BBC
2. DIMENSIONS D1 AND E1 DO NOT INCLUDE MOLD PROTRUSION. ALLOWABLE PROTRUSION IS 0.25mm PER SIDE. D1 AND E1 ARE MAXIMUM PLASTIC BODY SIZE DIMENSIONS INCLUDING MOLD MISMATCH.
3. DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL NOT CAUSE THE LEAD WIDTH TO EXCEED THE MAXIMUM b DIMENSION BY MORE THAN 0.08mm

## 7. Abbreviations

In this chapter some of the terms and abbreviations used throughout the technical reference manual are listed as follows.

<b>SIE</b>	Serial Interface Engine
<b>UTMI</b>	USB Transceiver Macrocell Interface

## About Alcor Micro, Corp.

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