TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSV)

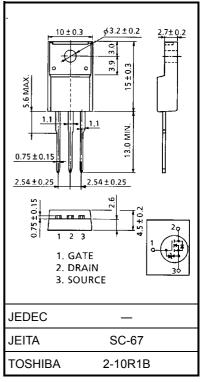
2SK2382

Switching Regulator, DC–DC Converter and Motor Drive Applications

- Low drain-source ON resistance $: R_{DS}(ON) = 0.13 \Omega$ (typ.)
- High forward transfer admittance $|Y_{fs}| = 17 \text{ S (typ.)}$
- Low leakage current $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 200 \ V)$
- Enhancement-mode : $V_{th} = 1.5 \sim 3.5 \text{ V} (V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA})$

Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	200	V
Drain-gate voltage (R_{GS} = 20 k Ω)		V _{DGR}	200	V
Gate-source voltage		V _{GSS}	±20	V
Drain current	DC (Note 1)	۱ _D	15	А
	Pulse (Note 1)	I _{DP}	45	А
Drain power dissipatio	n (Tc = 25°C)	PD	45	W
Single pulse avalanche energy (Note 2)		E _{AS}	166	mJ
Avalanche current		I _{AR}	15	А
Repetitive avalanche e	energy (Note 3)	E _{AR}	4.5	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature r	ange	T _{stg}	-55~150	°C



Weight: 1.9 g (typ.)

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	2.78	°C / W
Thermal resistance, channel to ambient	R _{th (ch−a)}	62.5	°C / W

Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2: V_{DD} = 50 V, T_{ch} = 25°C (initial), L = 1.2 mH, R_G = 25 Ω , I_{AR} = 15 A

Note 3: Repetitive rating; Pulse width limited by maximum channel temperature.

This transistor is an electrostatic sensitive device. Please handle with caution.

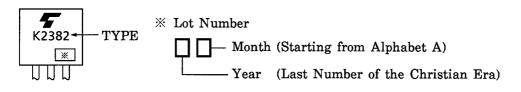
Electrical Characteristics (Ta = 25°C)

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	—	±10	μA
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 200 V, V _{GS} = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	200	_	_	V
Gate threshold v	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	1.5	_	3.5	V
Drain-source O	N resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 10 A	—	0.13	0.18	Ω
Forward transfer	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 10 A	10	17	_	S
Input capacitance	ce	C _{iss}		_	2000	_	
Reverse transfe	r capacitance	C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	200	_	pF
Output capacita	utput capacitance C _{oss}		_	600	_		
	Rise time	tr	$I_D = 10A$	_	35	_	
Switching time	Turn-on time	t _{on}		_	50	_	20
Switching time Fall time Turn-off time	t _f		_	10	_	ns	
	Turn-off time	t _{off}	$V_{DD} = 100V$ Duty $\leq 1\%$, $t_w = 10\mu s$	_	66	_	
Total gate charge (Gate-source plus gate-drain)		Qg		_	40	_	
Gate-source charge		Q _{gs}	V _{DD} ≈ 100 V, V _{GS} = 10 V, I _D = 15 A	_	25	—	nC
Gate-drain ("mil	ller") charge	Q _{gd}			15	—	

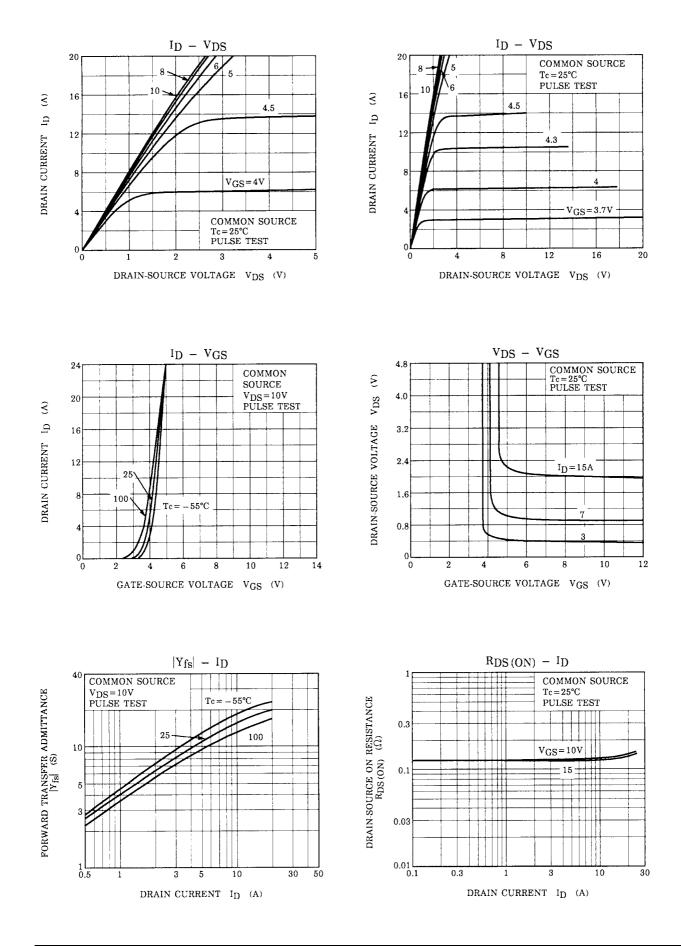
Source–Drain Ratings and Characteristics (Ta = 25°C)

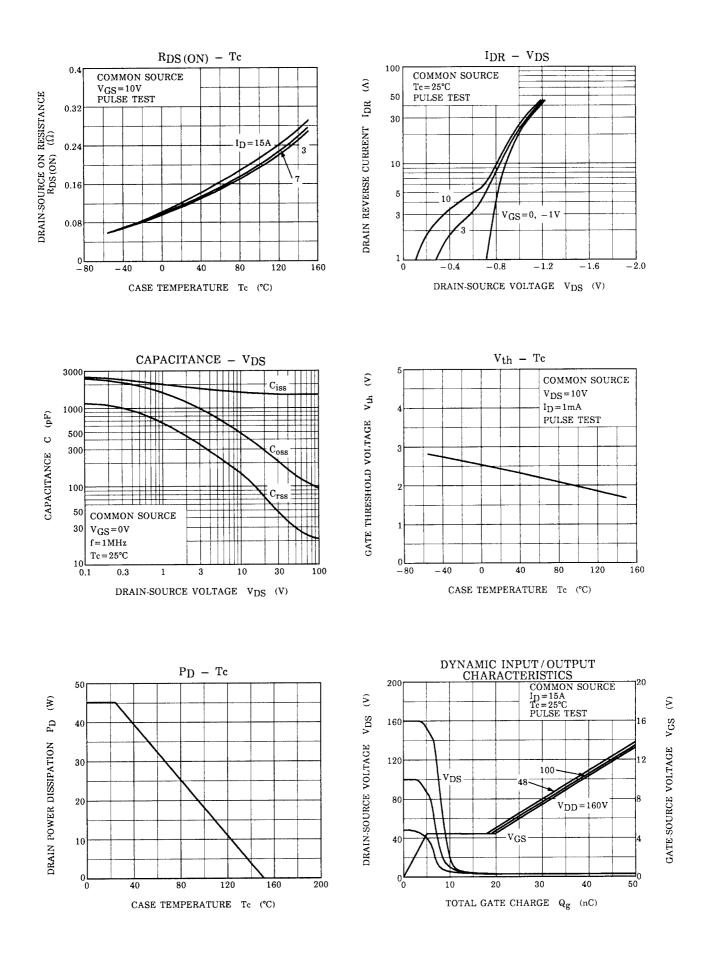
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	15	А
Pulse drain reverse current (Note 1)	I _{DRP}	—			45	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 15 A, V _{GS} = 0 V	_	_	-2.0	V
Reverse recovery time	t _{rr}	I _{DR} = 15 A, V _{GS} = 0 V		180	—	ns
Reverse recovered charge	Q _{rr}	dI _{DR} / dt = 100 Å / µs		1.13	_	μC

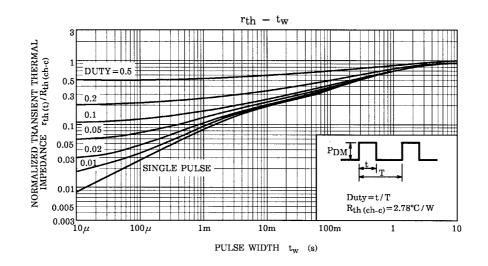
Marking

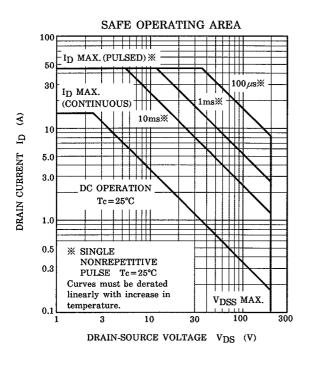


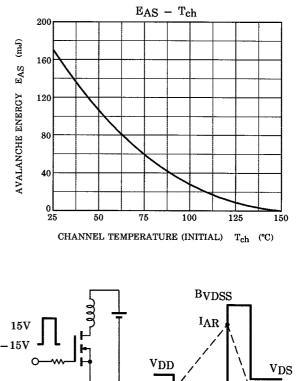
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TEST CIRCUIT

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WAVE FORM

$R_G = 25 \ \Omega$	$EAS = \frac{1}{2} \cdot L \cdot I^2 \cdot$	(BVDSS)
$V_{DD} = 50 \text{ V}, L = 1.2 \text{ mH}$	$EAS = \frac{1}{2} \cdot L \cdot l^2$	$\left(\overline{BVDSS - VDD} \right)$

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