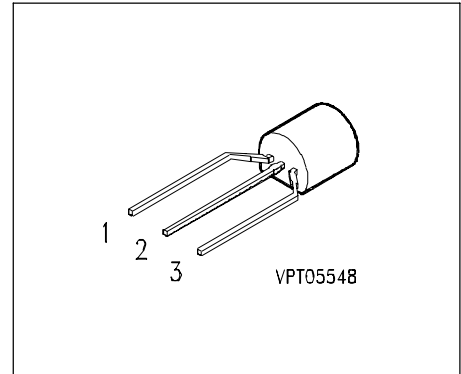


## BS 170

### SIPMOS® Small-Signal Transistor

- N channel
- Enhancement mode
- Logic Level
- $V_{GS(th)} = 0.8...2.0V$



Pin 1	Pin 2	Pin 3
S	G	D

Type	$V_{DS}$	$I_D$	$R_{DS(on)}$	Package	Marking
BS 170	60 V	0.3 A	5 $\Omega$	TO-92	BS 170

Type	Ordering Code	Tape and Reel Information
BS 170	Q67000-S076	E6288

### Maximum Ratings

Parameter	Symbol	Values	Unit
Drain source voltage	$V_{DS}$	60	V
Drain-gate voltage	$V_{DGR}$	60	V
$R_{GS} = 20 \text{ k}\Omega$			
Gate source voltage	$V_{GS}$	$\pm 20$	
ESD Sensitivity (HBM) as per MIL-STD 883		Class 1	
Continuous drain current	$I_D$	0.3	A
$T_A = 25 \text{ }^\circ\text{C}$			
DC drain current, pulsed	$I_{Dpuls}$	1.2	A
$T_A = 25 \text{ }^\circ\text{C}$			
Power dissipation	$P_{tot}$	0.63	W
$T_A = 25 \text{ }^\circ\text{C}$			

**Maximum Ratings**

Parameter	Symbol	Values	Unit
Chip or operating temperature	$T_j$	-55 ... + 150	°C
Storage temperature	$T_{stg}$	-55 ... + 150	
Thermal resistance, chip to ambient air <sup>1)</sup>	$R_{thJA}$	≤ 200	K/W
DIN humidity category, DIN 40 040		E	
IEC climatic category, DIN IEC 68-1		55 / 150 / 56	

**Electrical Characteristics, at  $T_j = 25^\circ\text{C}$ , unless otherwise specified**

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

**Static Characteristics**

Drain- source breakdown voltage $V_{GS} = 0\text{ V}, I_D = 0.25\text{ mA}, T_j = 25^\circ\text{C}$	$V_{(BR)DSS}$	60	-	-	V
Gate threshold voltage $V_{GS} = V_{DS}, I_D = 1\text{ mA}$	$V_{GS(th)}$	0.8	1.4	2	
Zero gate voltage drain current $V_{DS} = 60\text{ V}, V_{GS} = 0\text{ V}, T_j = 25^\circ\text{C}$ $V_{DS} = 60\text{ V}, V_{GS} = 0\text{ V}, T_j = 125^\circ\text{C}$	$I_{DSS}$	-	0.05	0.5	μA
Gate-source leakage current $V_{GS} = 20\text{ V}, V_{DS} = 0\text{ V}$	$I_{GSS}$	-	1	10	
Drain-Source on-state resistance $V_{GS} = 10\text{ V}, I_D = 0.2\text{ A}$	$R_{DS(on)}$	-	2.5	5	Ω

**Electrical Characteristics, at  $T_j = 25^\circ\text{C}$ , unless otherwise specified**

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

**Dynamic Characteristics**

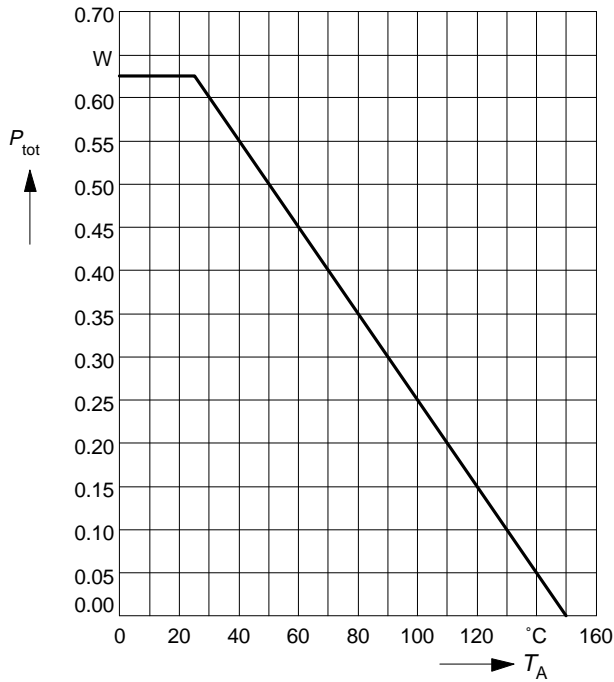
Transconductance $V_{DS} \geq 2 * I_D * R_{DS(on)max}, I_D = 0.2 \text{ A}$	$g_{fs}$	0.12	0.18	-	S
Input capacitance $V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$	$C_{iss}$	-	40	60	pF
Output capacitance $V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$	$C_{oss}$	-	15	25	
Reverse transfer capacitance $V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$	$C_{rss}$	-	5	10	
Turn-on delay time $V_{DD} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 0.29 \text{ A}$ $R_G = 50 \Omega$	$t_{d(on)}$	-	5	8	ns
Rise time $V_{DD} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 0.29 \text{ A}$ $R_G = 50 \Omega$	$t_r$	-	8	12	
Turn-off delay time $V_{DD} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 0.29 \text{ A}$ $R_G = 50 \Omega$	$t_{d(off)}$	-	12	16	
Fall time $V_{DD} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 0.29 \text{ A}$ $R_G = 50 \Omega$	$t_f$	-	17	22	

**Electrical Characteristics, at  $T_j = 25^\circ\text{C}$ , unless otherwise specified**

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
<b>Reverse Diode</b>					
Inverse diode continuous forward current $T_A = 25^\circ\text{C}$	$I_S$	-	-	0.3	A
Inverse diode direct current, pulsed $T_A = 25^\circ\text{C}$	$I_{SM}$	-	-	1.2	
Inverse diode forward voltage $V_{GS} = 0\text{ V}, I_F = 0.5\text{ A}$	$V_{SD}$	-	0.9	1.2	V

**Power dissipation**

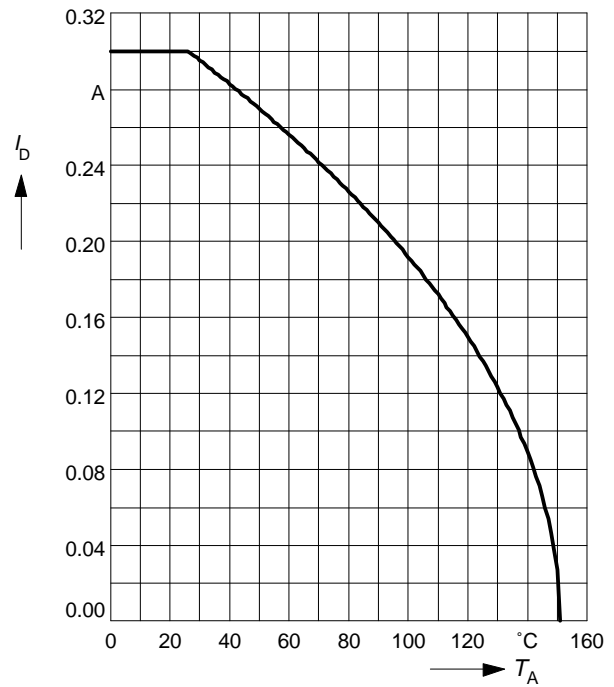
$P_{tot} = f(T_A)$



**Drain current**

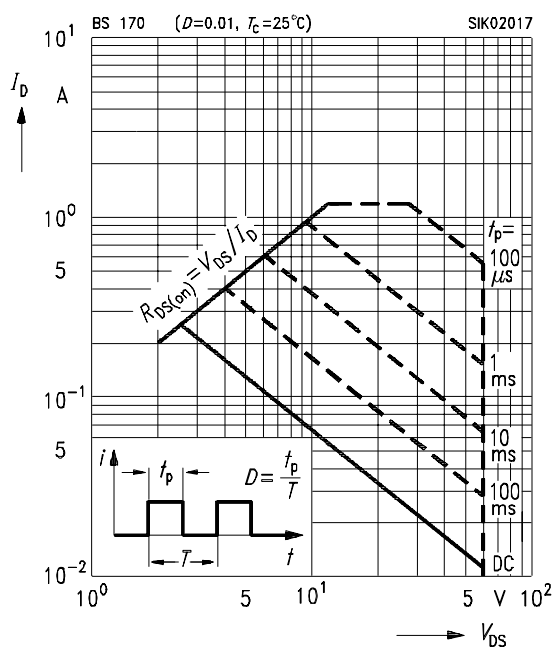
$I_D = f(T_A)$

parameter:  $V_{GS} \geq 10$  V



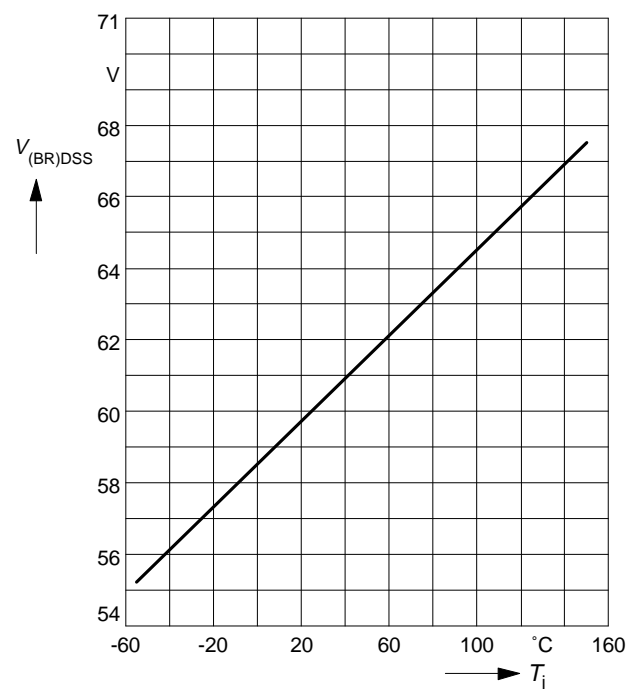
**Safe operating area  $I_D = f(V_{DS})$**

parameter :  $D = 0.01$ ,  $T_C = 25^\circ\text{C}$



**Drain-source breakdown voltage**

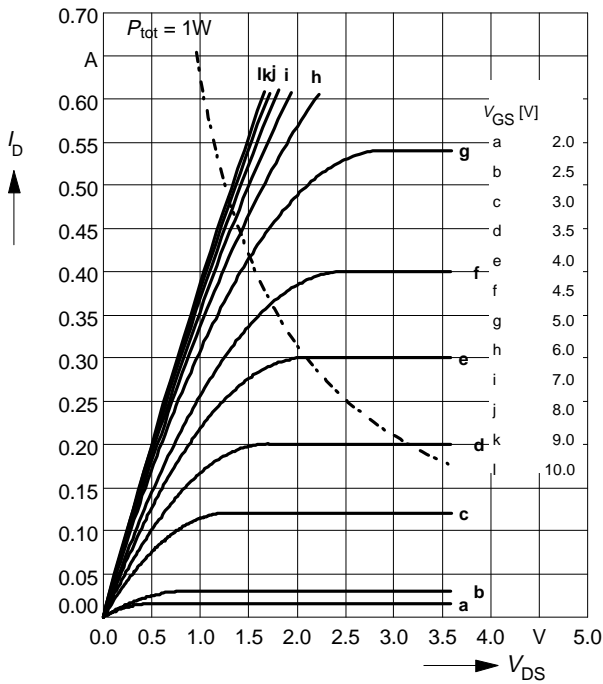
$V_{(BR)DSS} = f(T_j)$



**Typ. output characteristics**

$I_D = f(V_{DS})$

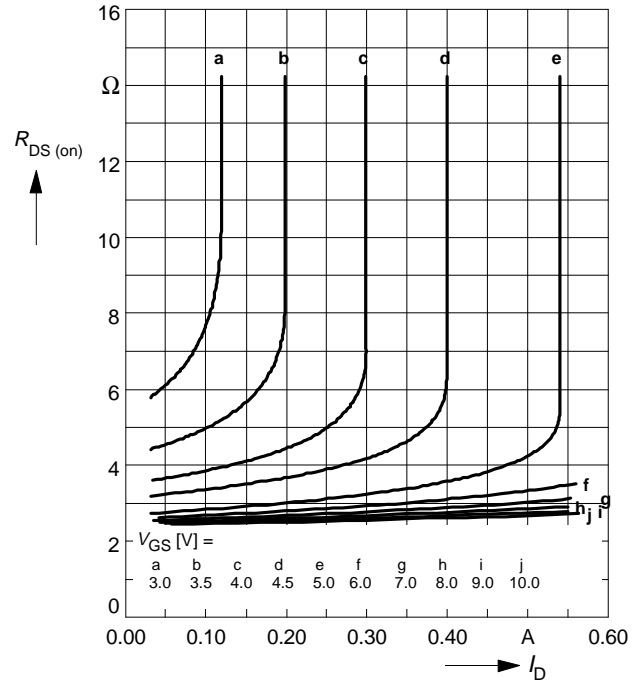
parameter:  $t_p = 80 \mu s$



**Typ. drain-source on-resistance**

$R_{DS(on)} = f(I_D)$

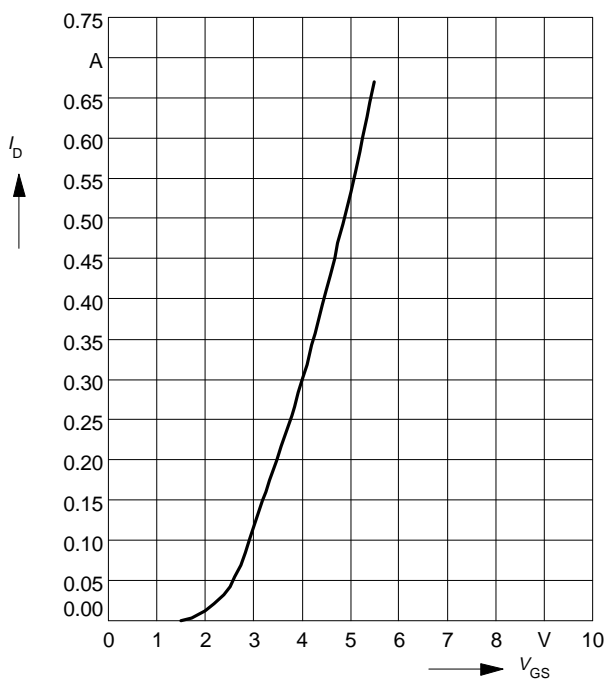
parameter:  $t_p = 80 \mu s, T_j = 25 \text{ }^\circ C$



**Typ. transfer characteristics  $I_D = f(V_{GS})$**

parameter:  $t_p = 80 \mu s$

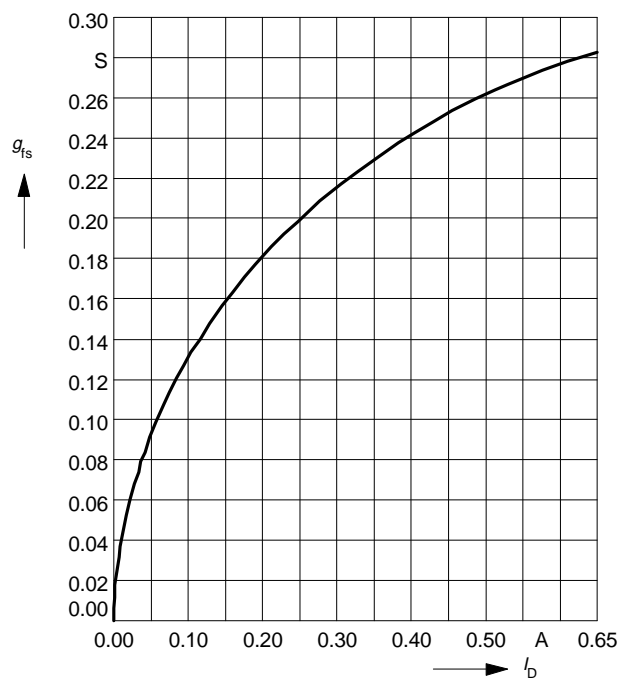
$V_{DS} \geq 2 \times I_D \times R_{DS(on)max}$



**Typ. forward transconductance  $g_{fs} = f(I_D)$**

parameter:  $t_p = 80 \mu s,$

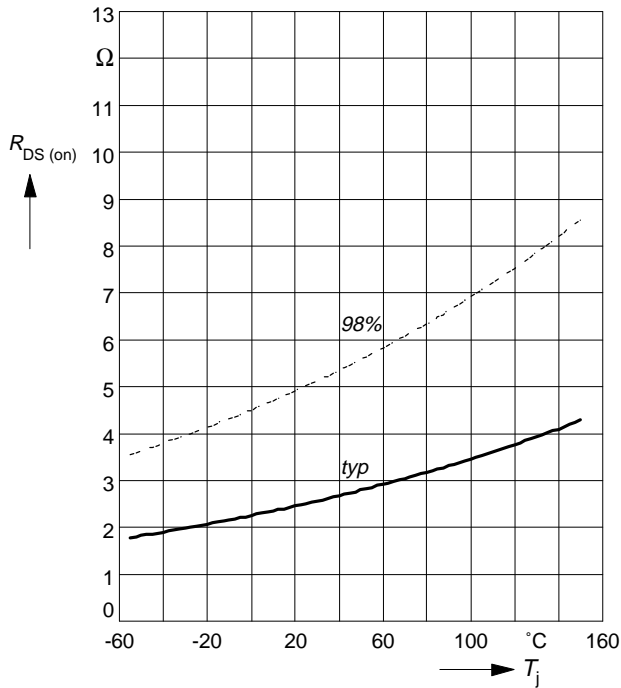
$V_{DS} \geq 2 \times I_D \times R_{DS(on)max}$



**Drain-source on-resistance**

$$R_{DS(on)} = f(T_j)$$

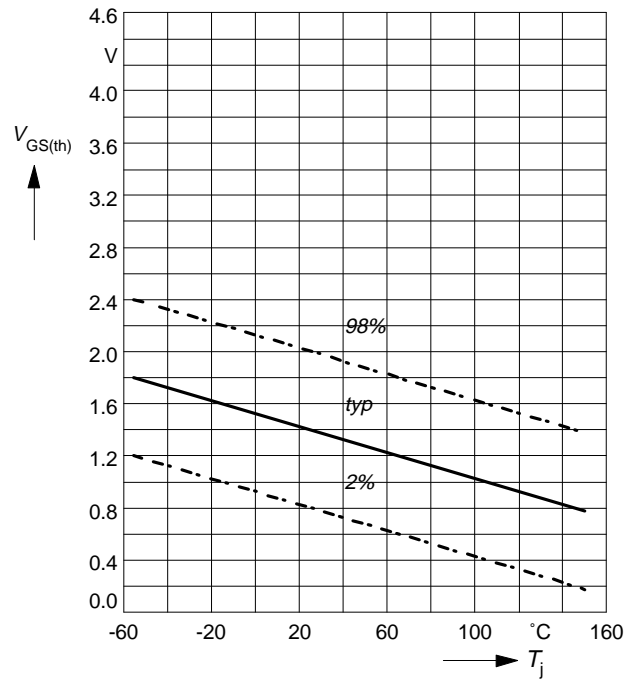
parameter:  $I_D = 0.2 \text{ A}$ ,  $V_{GS} = 10 \text{ V}$



**Gate threshold voltage**

$$V_{GS(th)} = f(T_j)$$

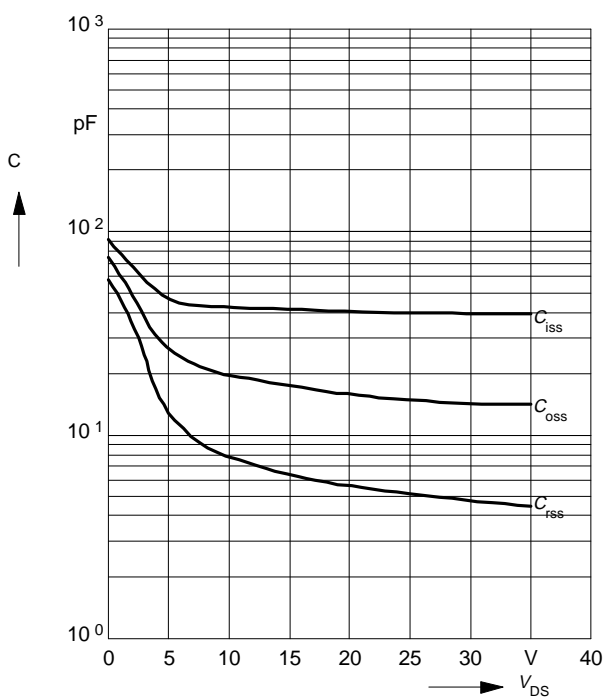
parameter:  $V_{GS} = V_{DS}$ ,  $I_D = 1 \text{ mA}$



**Typ. capacitances**

$$C = f(V_{DS})$$

parameter:  $V_{GS} = 0 \text{ V}$ ,  $f = 1 \text{ MHz}$



**Forward characteristics of reverse diode**

$$I_F = f(V_{SD})$$

parameter:  $T_j, t_p = 80 \mu\text{s}$

