

# HZM6.8ZMWA

Silicon Planar Zener Diode for Surge Absorb

# HITACHI

ADE-208-822 (Z)

Rev 0

Oct. 1999

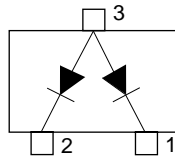
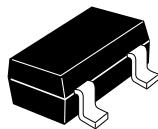
## Features

- HZM6.8ZMWA has two devices in a monolithic, and can absorb surge.
- Low capacitance ( $C=25\text{pF}$  max) and can protect ESD of signal line.
- MPAK Package is suitable for high density surface mounting and high speed assembly.

## Ordering Information

Type No.	Laser Mark	Package Code
HZM6.8ZMWA	68N	MPAK

## Outline



(Top View)

1 Cathode

2 Cathode

3 Anode

## Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Value	Unit
Power dissipation	Pd <sup>*1</sup>	200	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note 1. Four device total, See Fig.2.

## Electrical Characteristics (Ta = 25°C) <sup>\*1</sup>

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Zener voltage	V <sub>Z</sub>	6.47	—	7.00	V	I <sub>Z</sub> = 5 mA, 40ms pulse
Reverse current	I <sub>R</sub>	—	—	2	μA	V <sub>R</sub> = 3.5V
Capacitance	C	—	—	25	pF	V <sub>R</sub> = 0V, f = 1 MHz
Dynamic resistance	r <sub>d</sub>	—	—	30	Ω	I <sub>Z</sub> = 5 mA
ESD-Capability <sup>*2A@*3</sup>	—	20	—	—	kV	C = 150pF, R = 330Ω, Both forward and reverse direction 10 pulse

- Notes
1. Per one device.
  2. Failure criterion ; IR>2 μA at VR = 3.5V.
  3. Between cathode and anode.

Main Characteristic

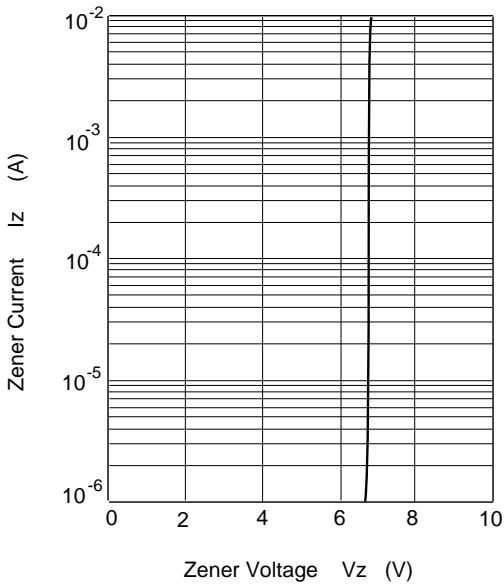


Fig.1 Zener current Vs. Zener voltage

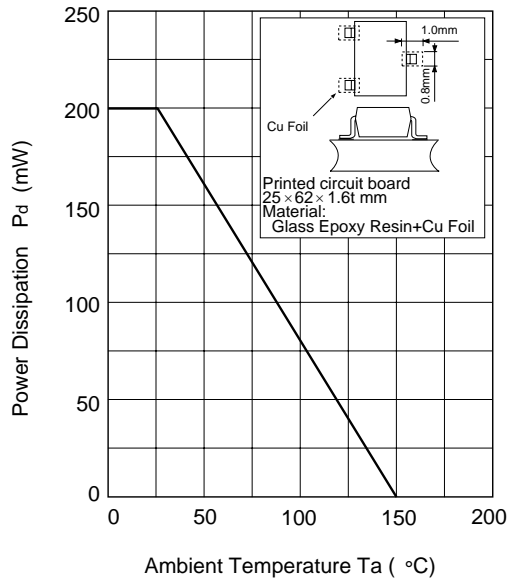


Fig.2 Power Dissipation Vs. Ambient Temperature

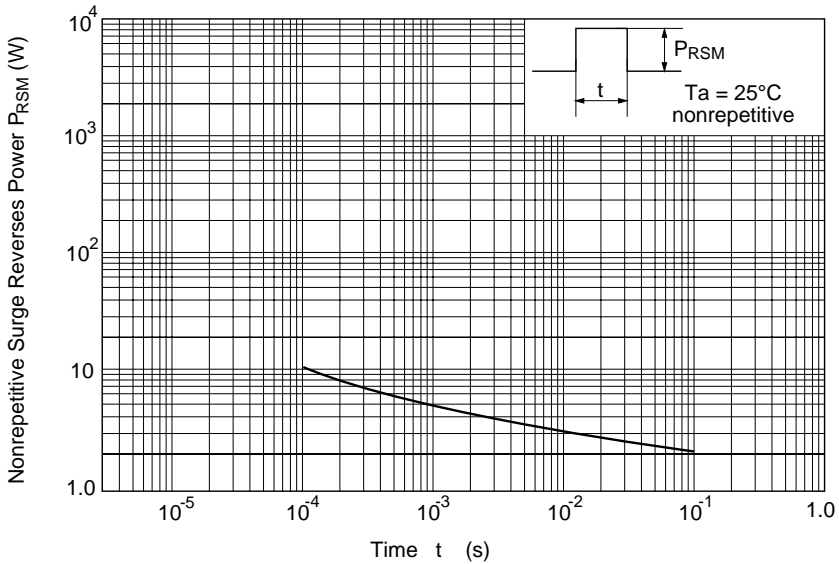


Fig.3 Surge Reverse Power Ratings

**Main Characteristic**

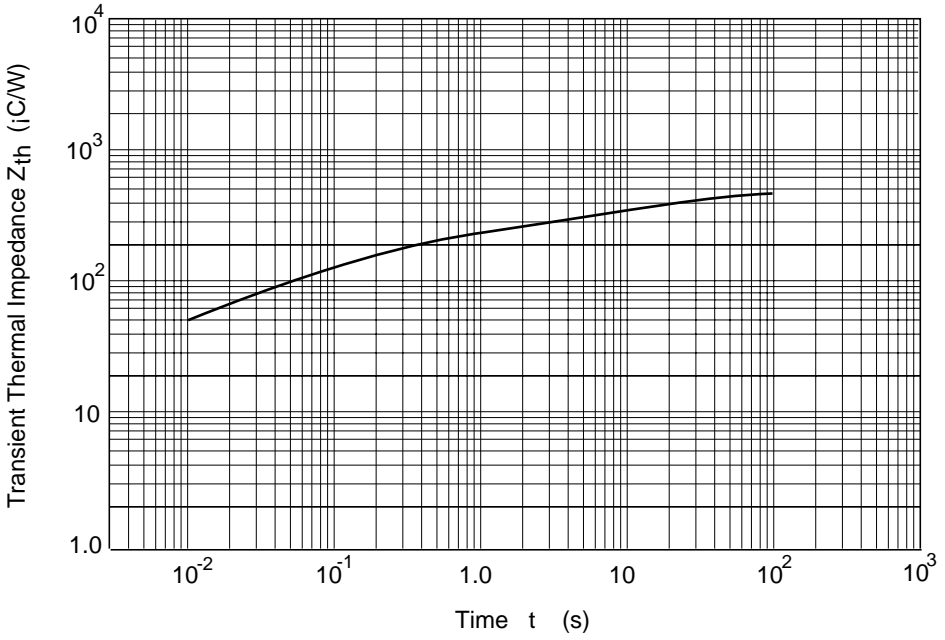
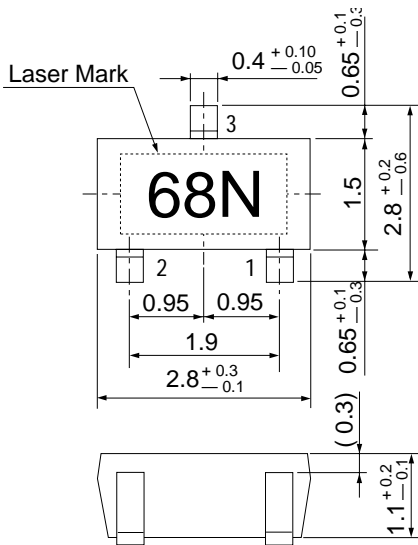


Fig.4 Transient Thermal Impedance

## Package Dimensions

Unit : mm



- 1 Cathode
- 2 Cathode
- 3 Anode

Hitachi Code	MPAK
JEDEC Code	
EIAJ Code	
Mass (g)	0.011

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