To our customers,

Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS

MOS FIELD EFFECT TRANSISTOR
N2500N

SWITCHING
N-CHANNEL MOSFET

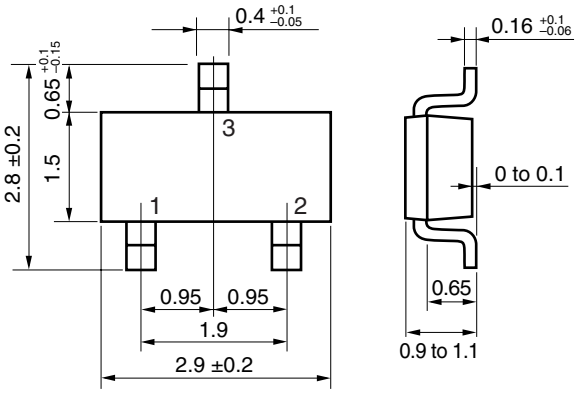
DESCRIPTION

The N2500N is N-channel MOS Field Effect Transistor designed for DC-DC converter and 2.5 V drive switching applications.

FEATURES

- 2.5 V drive available
- Low on-state resistance
  $R_{DS(on)1}$ = 5.8 Ω MAX. (V_{GS} = 4.5 V, I_D = 0.25 A)
  $R_{DS(on)2}$ = 6.6 Ω MAX. (V_{GS} = 2.5 V, I_D = 0.25 A)
- Low input capacitance
  C_{iss} = 145 pF TYP.
- Small and surface mount package (SC-96)
- Built-in gate protection diode

PACKAGE DRAWING (Unit: mm)



1. Gate
2. Source
3. Drain

ORDERING INFORMATION

PART NUMBER	PACKAGE
N2500N-T1B-AT Note	SC-96 (Mini Mold Thin Type)
N2500N-T2B-AT Note	

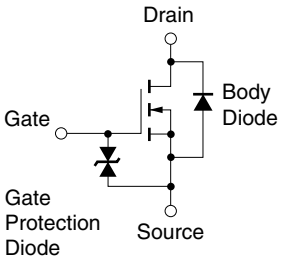
Note Pb-free (This product does not contain Pb in the external electrode and other parts.)

Marking: YA

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C)

Drain to Source Voltage (V_{GS} = 0 V)	V_{DSS}	250	V
Gate to Source Voltage (V_{DS} = 0 V)	V_{GSS}	±12	V
Drain Current (DC) (T_A = 25°C)	$I_{D(DC)}$	±0.5	A
Drain Current (pulse) Note1	$I_{D(pulse)}$	±2.0	A
Total Power Dissipation (T_A = 25°C)	P_{T1}	0.2	W
Total Power Dissipation (T_A = 25°C) Note2	P_{T2}	1.25	W
Channel Temperature	T_{ch}	150	°C
Storage Temperature	T_{stg}	−55 to +150	°C

EQUIVALENT CIRCUIT



Notes 1. PW ≤ 10 μs, Duty Cycle ≤ 1%

2. Mounted on FR-4 board of 50 mm x 50 mm x 1.6 mmt, t ≤ 5 sec

Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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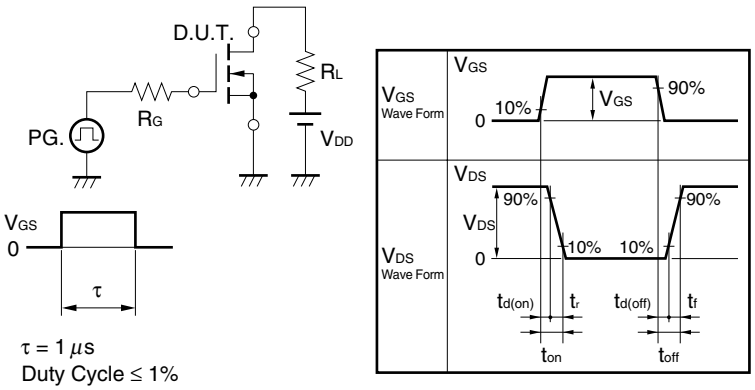
Date Published October 2009 NS
Printed in Japan

ELECTRICAL CHARACTERISTICS (T_A = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 250 V, V_{GS} = 0 V			10	μA
Gate Leakage Current	I_{GSS}	V_{GS} = ±12 V, V_{DS} = 0 V			±10	μA
Gate to Source Cut-off Voltage	$V_{GS(off)}$	V_{DS} = 10 V, I_D = 1.0 mA	0.5	1.0	1.5	V
Forward Transfer Admittance **Note**	$\lvert y_{fs} \rvert$	V_{DS} = 10 V, I_D = 0.25 A	0.8			S
Drain to Source On-state Resistance **Note**	$R_{DS(on)1}$	V_{GS} = 4.5 V, I_D = 0.25 A		4.2	5.8	Ω
	$R_{DS(on)2}$	V_{GS} = 2.5 V, I_D = 0.25 A		4.3	6.6	Ω
Input Capacitance	C_{iss}	V_{DS} = 10 V,		145		pF
Output Capacitance	C_{oss}	V_{GS} = 0 V,		17		pF
Reverse Transfer Capacitance	C_{rss}	f = 1.0 MHz		5		pF
Turn-on Delay Time	$t_{d(on)}$	V_{DD} = 125 V, I_D = 0.25 A,		7		ns
Rise Time	t_r	V_{GS} = 4.5 V,		12		ns
Turn-off Delay Time	$t_{d(off)}$	R_G = 10 Ω		22		ns
Fall Time	t_f			35		ns
Total Gate Charge	Q_G	V_{DD} = 200 V,		4.7		nC
Gate to Source Charge	Q_{GS}	V_{GS} = 4.5 V,		0.4		nC
Gate to Drain Charge	Q_{GD}	I_D = 0.5 A		2.5		nC
Body Diode Forward Voltage **Note**	$V_{F(S-D)}$	I_F = 0.5 A, V_{GS} = 0 V		0.82	1.5	V
Reverse Recovery Time	t_{rr}	I_F = 0.5 A, V_{GS} = 0 V,		45		ns
Reverse Recovery Charge	Q_{rr}	di/dt = 100 A/μs		36		nC

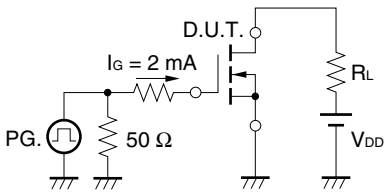
Note Pulsed

TEST CIRCUIT 1 SWITCHING TIME



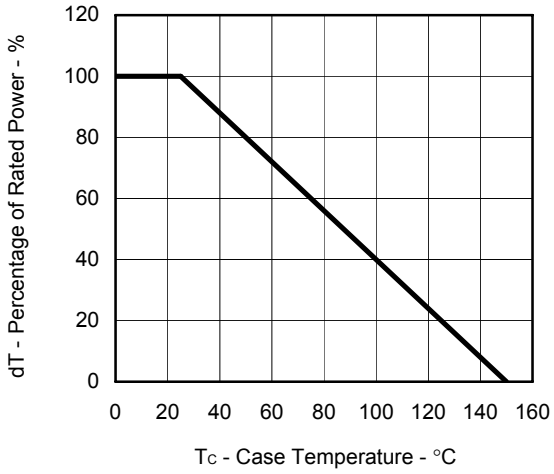
τ = 1 μs
Duty Cycle ≤ 1%

TEST CIRCUIT 2 GATE CHARGE

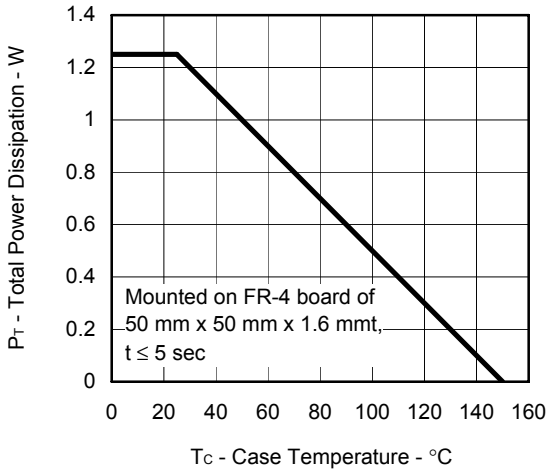


TYPICAL CHARACTERISTICS (T_A = 25°C)

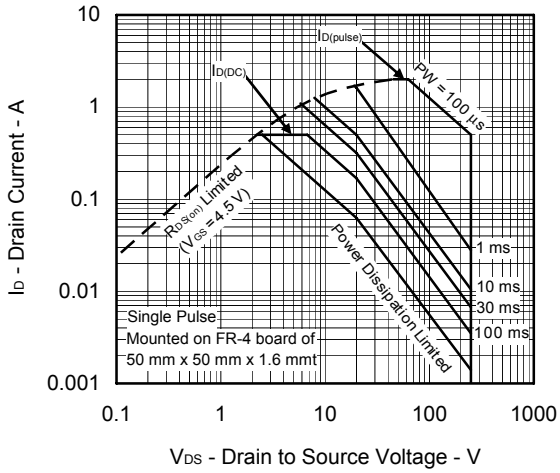
DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA



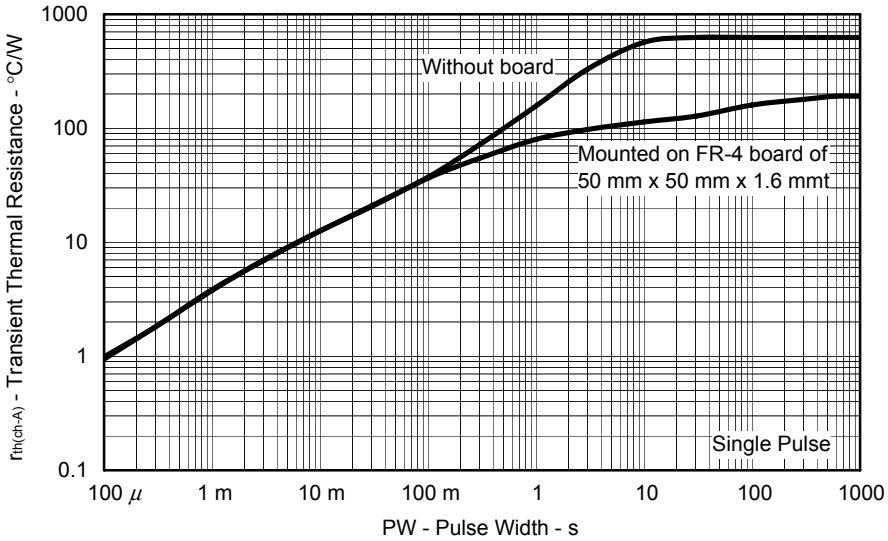
TOTAL POWER DISSIPATION vs. CASE TEMPERATURE



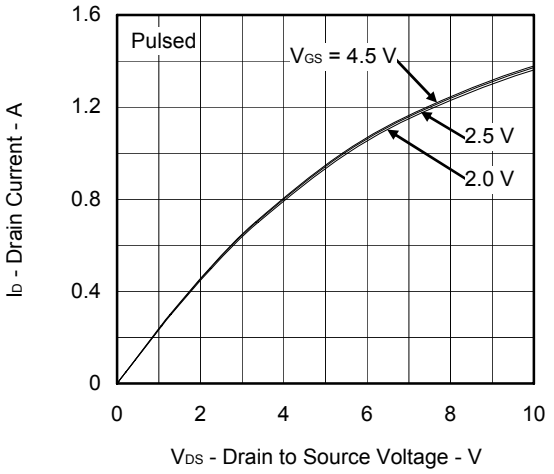
FORWARD BIAS SAFE OPERATING AREA



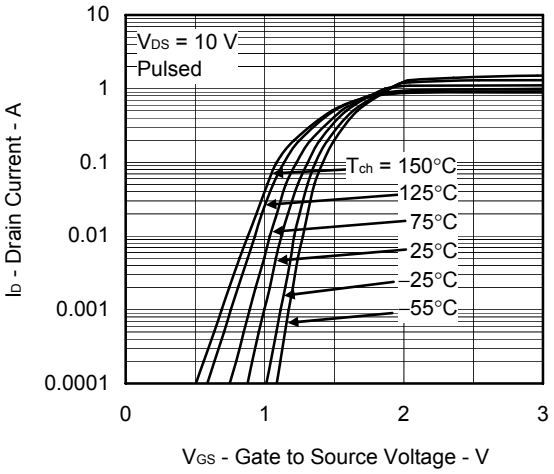
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



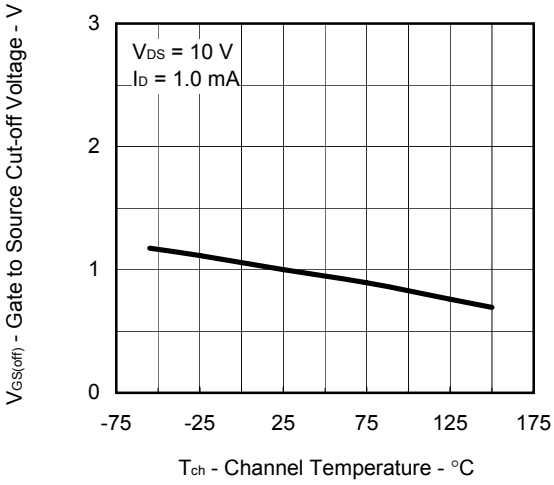
DRAIN CURRENT vs.
DRAIN TO SOURCE VOLTAGE



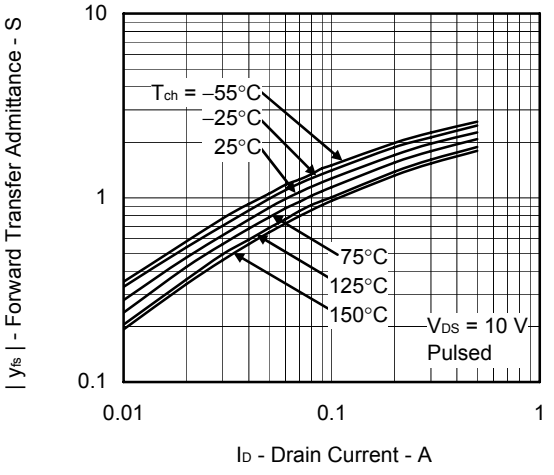
FORWARD TRANSFER CHARACTERISTICS



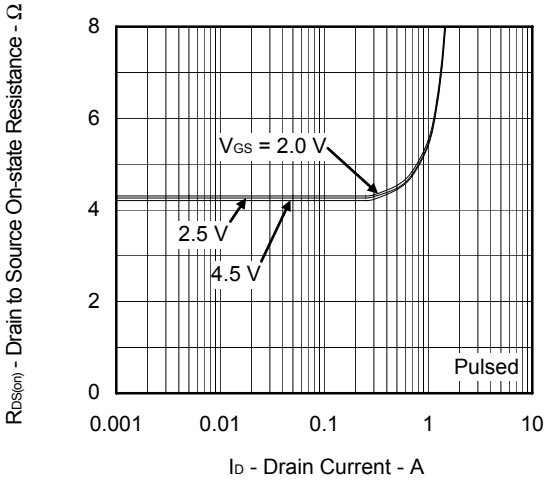
GATE TO SOURCE CUT-OFF VOLTAGE vs.
CHANNEL TEMPERATURE



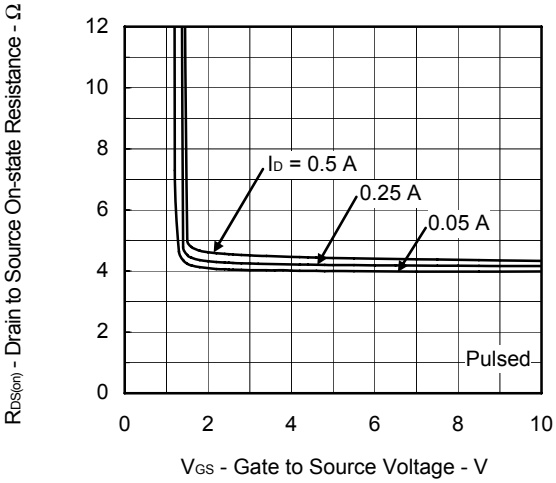
FORWARD TRANSFER ADMITTANCE vs.
DRAIN CURRENT



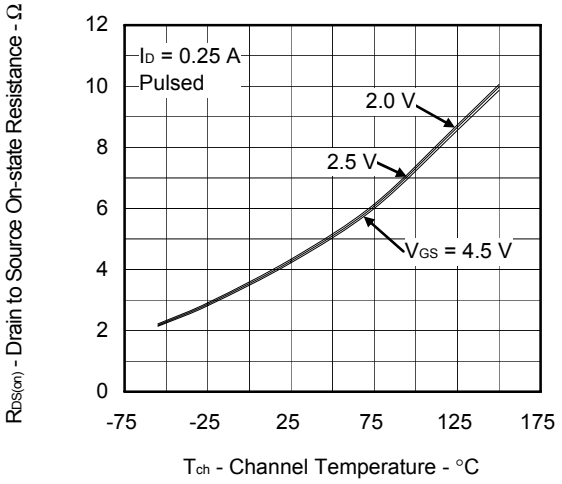
DRAIN TO SOURCE ON-STATE RESISTANCE vs.
DRAIN CURRENT



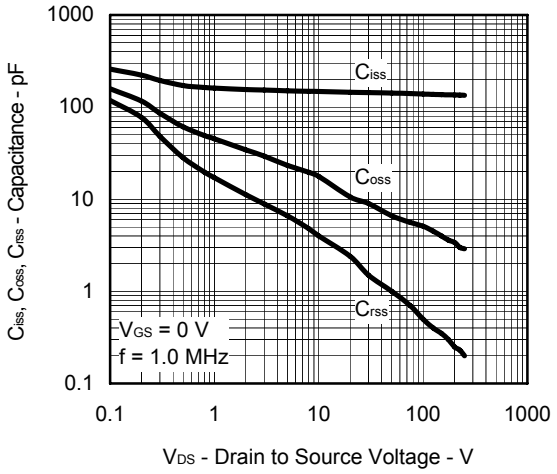
DRAIN TO SOURCE ON-STATE RESISTANCE vs.
GATE TO SOURCE VOLTAGE



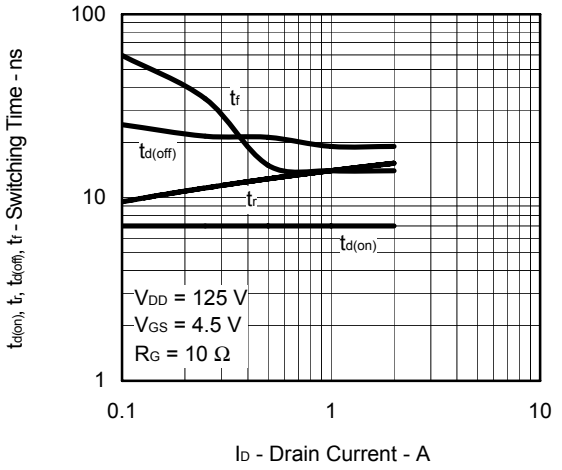
DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



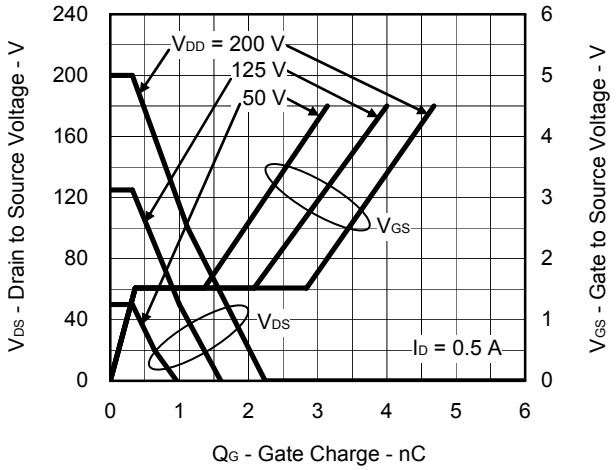
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



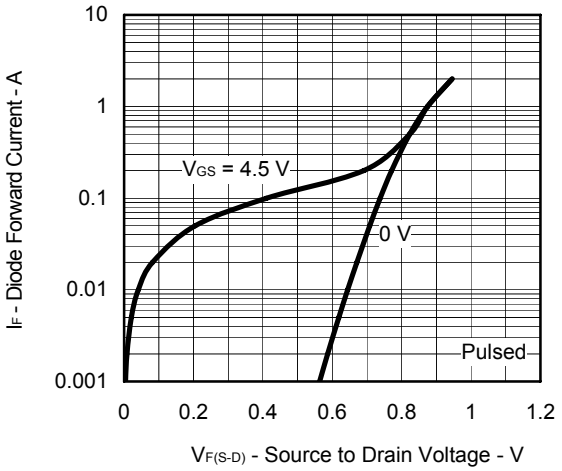
SWITCHING CHARACTERISTICS



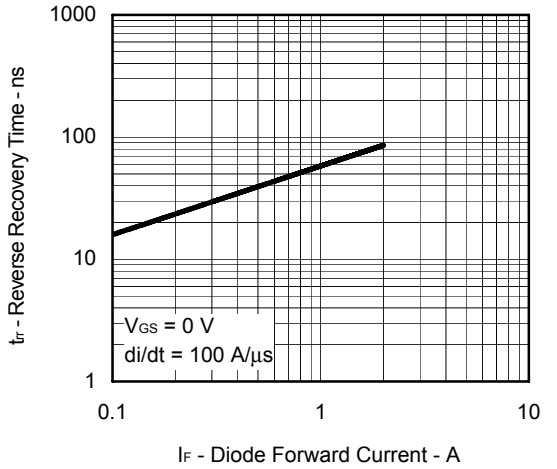
DYNAMIC INPUT/OUTPUT CHARACTERISTICS



SOURCE TO DRAIN DIODE FORWARD VOLTAGE



REVERSE RECOVERY TIME vs. DIODE FORWARD CURRENT



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