

SONY**CXA1527Q**

High-Speed Transimpedance Amplifier

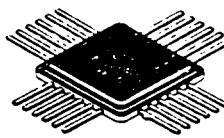
Description

CXA1527Q is a low noise amplifier, particularly suitable for signal recovery in fiber-optic receivers.

Features

High-speed bipolar process provides for ultra high-speed operation	
• High transimpedance	3kΩ (Typ.)
• Wide band width	550MHz (Typ.)
• Low noise (at 10MHz)	2.8pA/√Hz (Typ.)
• Low power consumption	27mW (Typ.)
• Single power supply (Negative supply)	V _{CC} =V _{CCA} =GND, V _{EE} =-5.2V
(Positive supply)	V _{CC} =V _{CCA} =+5.0V, V _{EE} =GND

24pin QFP (Metal)



Absolute Maximum Ratings (V_{CC}=V_{CCA})

• Supply voltage	V _{CC} -V _{EE}	-0.3 to +7.0	V
• Minimum input voltage	V _{IN}	V _{EE} -2.0	V
• Input voltage	I _{IN}	-250 to +250	μA
• Output current (continuous)	I _O	0 to 50	mA
• Output current (surge)	I _O	0 to 100	mA
• Operating case temperature	T _c	-55 to +125	°C
• Storage temperature	T _{STG}	-65 to +150	°C

Stresses greater than these conditions may cause permanent damage to the devices or affect their reliability.

Recommended Operating Conditions (V_{CC}=V_{CCA})

• Supply voltage (Negative Supply)	V _{EE}	V _{CC} =GND	-5.46 to -4.94	V
(Positive Supply)	V _{CC}	V _{EE} =GND	4.5 to 5.5	V
• Operating case temperature	T _c		0 to 85	°C

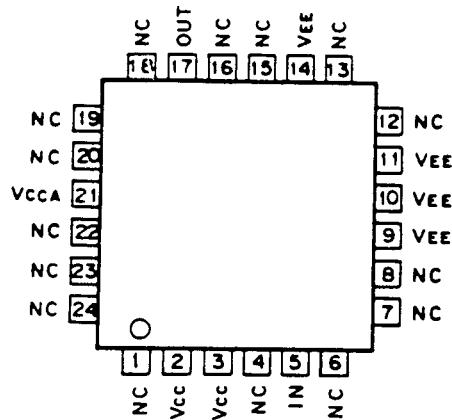
The devices should be operated under these conditions, beyond which the parametric values are not specified.

Application

- Fiber-optic receiver
- Instrument

Structure

Bipolar silicon monolithic IC

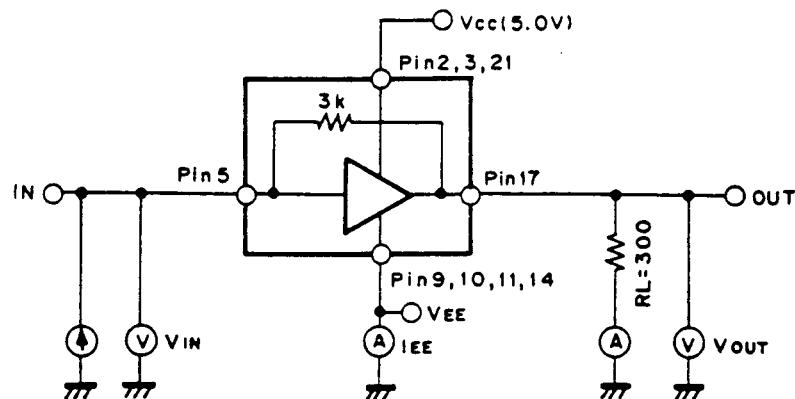
Pin Assignment**Pin Description**

Pin No.	Symbol	Equivalent circuit	Description
5	IN		Data input
17	OUT		Data output
2, 3	Vcc		Positive power supply
21	VCCA		Positive power supply for output
9, 10 11, 14	VEE		Negative power supply
1, 4, 6, 7, 8, 12, 13, 14, 16, 18, 19, 20, 22, 23, 24	NC	(2), (3) Vcc O--- R1 --- Q3 (5) IN O--- R2 --- Q1 VEE O--- (9), (10) (11), (14)	No connection

DC Characteristics

(Tc=0 to +85 °C , VEE=GND, Vcc=VCCA=5V ± 0.5V, RL=300Ω)

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Supply current	I _{EE}	Input pin left open *1	-4.0	-2.7	-1.6	mA
Transimpedance	Z _T	I _{IN} =-140 μA to +140 μA	2.2	3.0	3.8	kΩ
Maximum input current	I _{MAX}		140			μA
Input bias voltage	V _{IN}			V _{EE} +0.77		V
Output bias voltage	V _{OUT}	Input pin left open		V _{EE} +0.98		V
Input capacitance	C _{IN}			1.3		pF

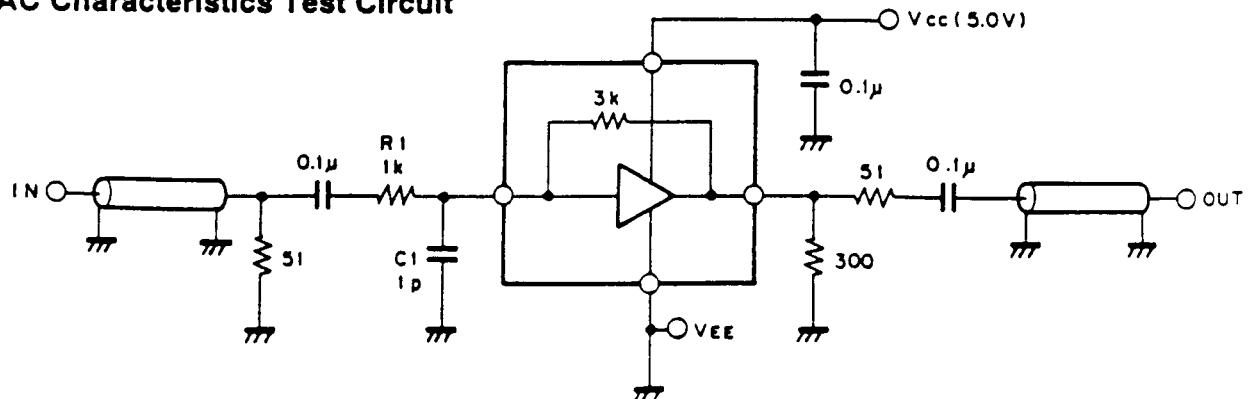
* 1: P_D=|I_{EE}| × 5.0V+V_{OUT}/R_L × (5.0V-V_{OUT}), see DC characteristics test circuit.**DC Characteristics Test Circuit****AC Characteristics**

(Tc=0 to 85 °C , VEE=GND, Vcc=VCCA=5.0V ± 0.5V)

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Transimpedance	Z _T	f _{IN} =10MHz	2.2	3.0	3.8	kΩ
Bandwidth (-3dB)	f _{3dB}	* 1, 2	450	550		MHz
Input current noise spectral density	I _N	f _{IN} =10MHz		2.8		pA/ $\sqrt{\text{Hz}}$

* 1: Assumes photodiode capacitance. C_{PD}=1pF. Referenced from 10MHz.

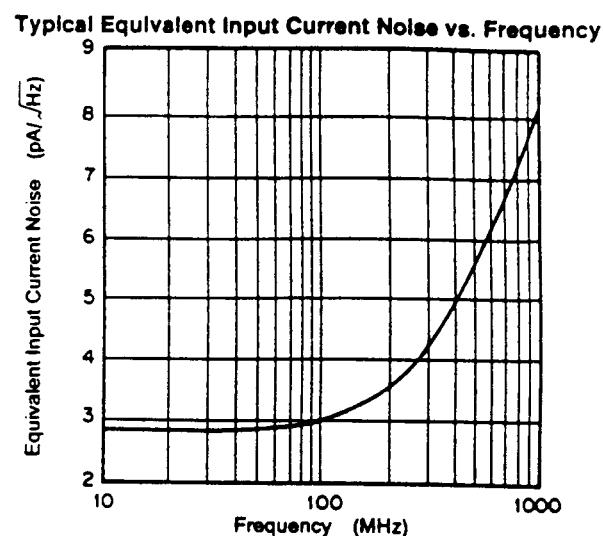
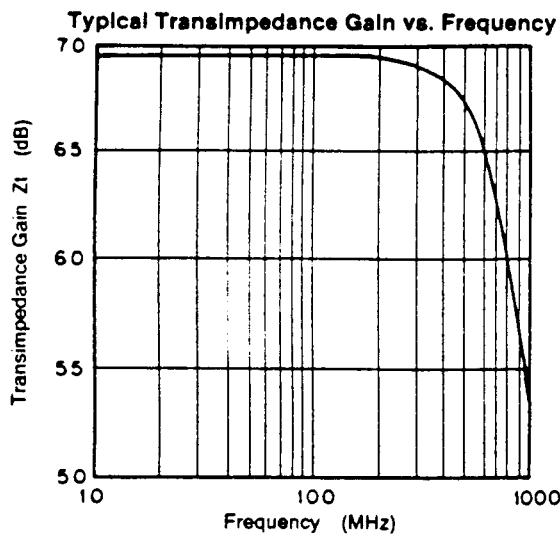
* 2: Circuit board layout dependent at higher frequencies. For best performance use RF filter in power supply lines.

AC Characteristics Test Circuit

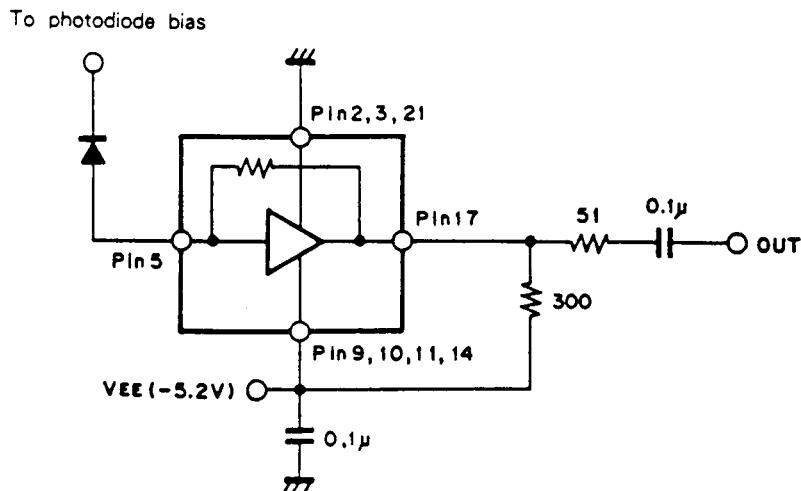
C1 : Assumes photodiode capacitance

R1 : For V - I conversion

Typical Performance ($T_a=25^{\circ}\text{C}$, $V_{EE}=\text{GND}$, $V_{CC}=V_{CCA}=5.0\text{V}$)



Application Circuit



Application circuits shown are typical examples illustrating the operation of the devices. Sony cannot assume responsibility for any problems arising out of the use of these circuits or for any infringement of third party patent and other right due to same.

Cautions for Handling

1. As the electrostatic breakdown level is weak, take care to handle.
2. The internal resistor ($R_5=10\text{k}\Omega$) of the output pin (No. 17) does not have the capability of drive. The terminal resistor must be connected, and the resistance value should be selected between 100Ω and 300Ω .

Package Outline Unit : mm

24pin QFP (Metal) 0.3g

