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❑ MN1874083

Type			**MN1874083**
ROM (x8-bit)			40K
RAM (x8-bit)			640
Minimum Instruction Execution Time			**2/3 dividing 0.5µs (at 4.5 to 5.5V, 12MHz)**
Interrupts			• RESET • External 0 • External 1 • Timer 0 • Timer 1 • Timer 2 • I²C • Remote Control • Line 21 • COSD
Timer Counter			**Timer Counter 0 : 8-bit x 1** Clock Source .....1/1, 1/4, 1/16, 1/64 of System Clock Interrupt Source .....Overflow of Timer Counter 0 **Timer Counter 1 : 8-bit x 1** Clock Source .....1/2, 1/16, 1/64, 1/256, 1/512 of System Clock Interrupt Source .....Overflow of Timer Counter 1 **Time Base Counter** Clock Source .....1/4096 of System Clock Interrupt Source .....1/1, 1/2, 1/4, 1/8 of Timer Counter 2 **Watchdog**
Serial Interface			**I²C x 1** (Two bus line system)
I/O Pins	**I/O**	**21**	• Common use : 5
	Input	**1**	• Common use : 1
	Output	**7**	• Nch Open-drain : 7
A/D Inputs			5-bit x 7ch (without S/H)
PWM			14-bit x 1ch (Repetition Cycle 16µs, at 12MHz), 8-bit x 8ch (Repetition Cycle 32µs, at 12MHz), 7-bit x 1ch (Repetition Cycle 16µs, at 12MHz) (All PWM are 5V ; not connectable to 12V systems)
Special Ports			Remote Control Reception
CRTC			Single OSD built-in (Caption OSD : 12 x 26, 256 letters)
Notes			Remote Control Data Detection Circuit built-in
Package			SDIP064-P-0750

Electrical Characteristics

A/D Converter Characteristics

Parameter	Symbol	Condition	Limit			Unit
			min	typ	max	
A/D Conversion Time	TAD	fosc=12MHz	9			µs
Analog Input Voltage	VAD		VSS		VDD	V

(Ta= –20 to +70°C, VDD=5.0V, VSS=0V)

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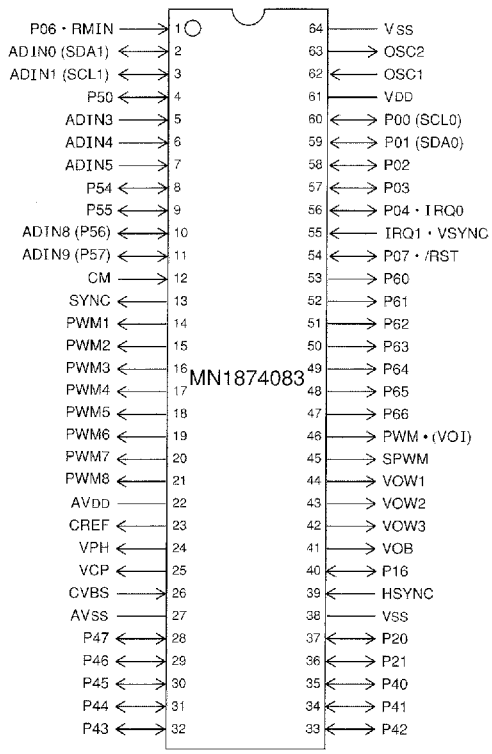
Support Tool

In-Circuit Emulator	PX-ICE1870 / 80 + PX-PRB1879682 (under development)
EPROM built-in Type	Use **MN18P79682** in SDIP064-P-0750 package.

Pin Assignment



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