

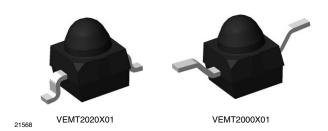
# Vishay Semiconductors

AUTOMOTIVE

COMPLIANT HALOGEN

FREE

# **Silicon NPN Phototransistor**



### **DESCRIPTION**

VEMT2000X01 series are silicon NPN epitaxial planar phototransistors with daylight blocking filter in a miniature, black dome lens package for surface mounting. Filter bandwidth is matched with 830 nm to 950 nm IR emitters.

#### **FEATURES**

- Package type: surface mount
- Package form: GW, RGW
- Dimensions (L x W x H in mm): 2.3 x 2.3 x 2.8
- AEC-Q101 qualified
- High radiant sensitivity
- Daylight blocking filter matched with 830 nm to 950 nm IR emitters



- Angle of half sensitivity:  $\phi = \pm 15^{\circ}$
- Package matched with IR emitter series VSMB2000X01
- Floor life: 4 weeks, MSL 2a, acc. J-STD-020
- Lead (Pb)-free reflow soldering
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

#### **APPLICATIONS**

- Detector in automotive applications
- · Photo interrupters
- Miniature switches
- Counters
- Encoders
- Position sensors

PRODUCT SUMMARY				
COMPONENT	I <sub>ca</sub> (mA)	φ (deg)	λ <sub>0.5</sub> (nm)	
VEMT2000X01	6	± 15	790 to 970	
VEMT2020X01	6	± 15	790 to 970	

#### Note

· Test condition see table "Basic Characteristics"

ORDERING INFORMATION					
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM		
VEMT2000X01	Tape and reel	MOQ: 6000 pcs, 6000 pcs/reel	Reverse gullwing		
VEMT2020X01	Tape and reel	MOQ: 6000 pcs, 6000 pcs/reel	Gullwing		

#### Note

• MOQ: minimum order quantity

ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Collector emitter voltage		$V_{CEO}$	20	V
Emitter collector voltage		V <sub>ECO</sub>	7	V
Collector current		I <sub>C</sub>	50	mA
Power power dissipation	T <sub>amb</sub> ≤ 75 °C	P <sub>V</sub>	100	mW
Junction temperature		Tj	100	°C
Operating temperature range		T <sub>amb</sub>	- 40 to + 100	°C

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# VEMT2000X01, VEMT2020X01

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ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Storage temperature range		T <sub>stg</sub>	- 40 to + 100	°C
Soldering temperature	Acc. reflow profile fig. 8	T <sub>sd</sub>	260	°C
Thermal resistance junction/ambient	Acc. J-STD-051	R <sub>thJA</sub>	250	K/W

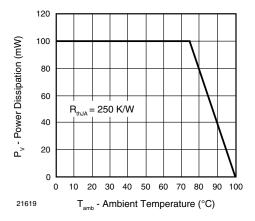


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Collector emitter breakdown voltage	$I_{\rm C} = 0.1  {\rm mA}$	$V_{CEO}$	20			V
Collector dark current	V <sub>CE</sub> = 5 V, E = 0	I <sub>CEO</sub>		1	100	nA
Collector emitter capacitance	$V_{CE} = 0 V$ , $f = 1 MHz$ , $E = 0$	C <sub>CEO</sub>		25		рF
Collector light current	$E_e = 1 \text{ mW/cm}^2,  \lambda = 950 \text{ nm}, \\ V_{CE} = 5 \text{ V}$	I <sub>ca</sub>	3	6	9	mA
Angle of half sensitivity		φ		± 15		deg
Wavelength of peak sensitivity		$\lambda_{p}$		860		nm
Range of spectral bandwidth		λ <sub>0.5</sub>		790 to 970		nm
Collector emitter saturation voltage	$I_{C} = 0.05 \text{ mA}$	V <sub>CEsat</sub>			0.4	V
Temperature coefficient of Ica	$E_e$ = 1 mW/cm <sup>2</sup> , $\lambda$ = 950 nm, $V_{CE}$ = 5 V	Tk <sub>lca</sub>		1.1		%/K

# Vishay Semiconductors

### **BASIC CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)

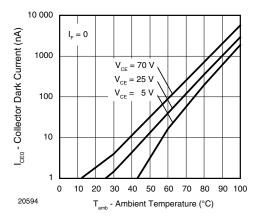


Fig. 2 - Collector Dark Current vs. Ambient Temperature

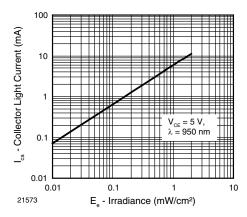


Fig. 3 - Collector Light Current vs. Irradiance

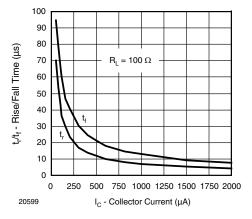


Fig. 4 - Rise/Fall Time vs. Collector Current

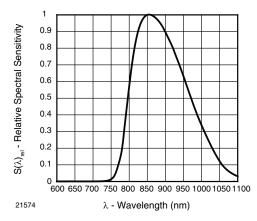


Fig. 5 - Relative Spectral Sensitivity vs. Wavelength

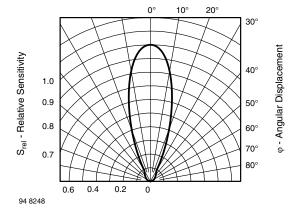


Fig. 6 - Relative Radiant Sensitivity vs. Angular Displacement

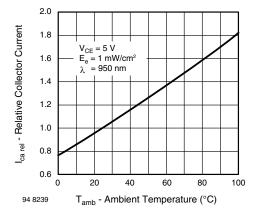


Fig. 7 - Relative Collector Current vs. Ambient Temperature



### **REFLOW SOLDER PROFILE**

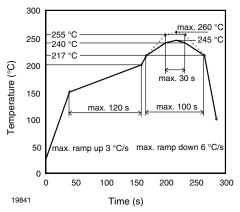


Fig. 8 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

#### **DRYPACK**

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

#### **FLOOR LIFE**

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 4 weeks

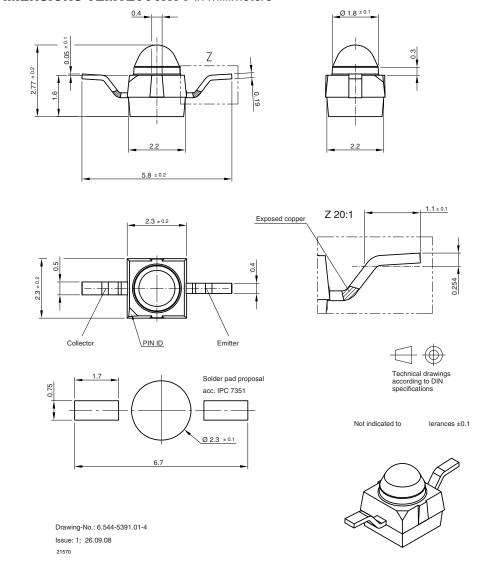
Conditions:  $T_{amb}$  < 30 °C, RH < 60 %

Moisture sensitivity level 2a, acc. to J-STD-020.

#### **DRYING**

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40  $^{\circ}$ C (+ 5  $^{\circ}$ C), RH < 5  $^{\circ}$ C.

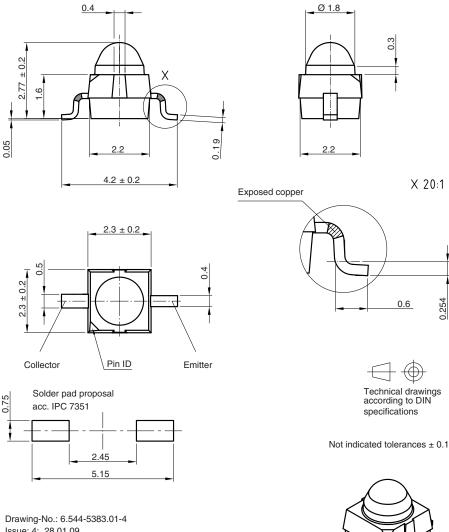
#### PACKAGE DIMENSIONS VEMT2000X01 in millimeters





# Vishay Semiconductors

### **PACKAGE DIMENSIONS VEMT2020X01** in millimeters

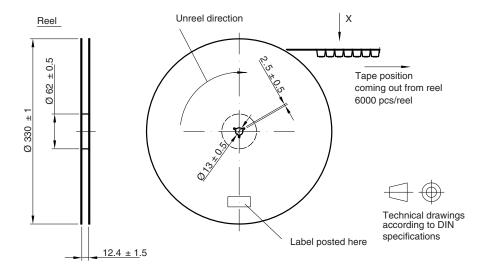


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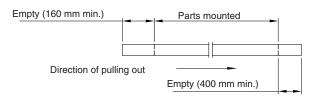
# Silicon NPN Phototransistor



### TAPE AND REEL DIMENSIONS VEMT2000X01 in millimeters

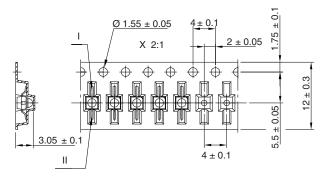


Leader and trailer tape:



#### Terminal position in tape

Devicce	Lead I	Lead II
VEMT2000		
VEMT2500	Collector	Emitter
VEMD2000		
VEMD2500	0-411-	A I -
VSMB2000	Cathode	Anode
VSMG2000		
VSMY2850RG	Anode	Cathode



Drawing-No.: 9.800-5100.01-4

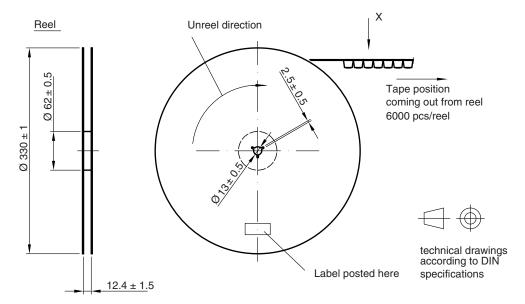
Issue: 2; 18.03.10

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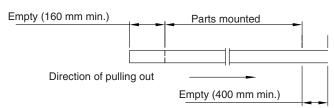


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### TAPE AND REEL DIMENSIONS VEMT2020X01 in millimeters

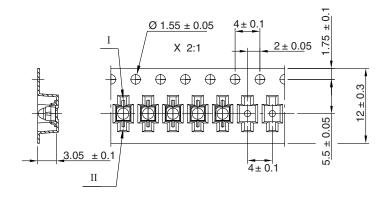


Leader and trailer tape:



### Terminal position in tape

Devicce	Lead I	Lead II
VEMT2020		
VEMT2520	Collector	Emitter
VSMB2020		
VSMG2020	Cathode	Anode
VEMD2020	Calriode	Ariode
VEMD2520		
VSMY2850G	Anode	Cathode



Drawing-No.: 9.800-5091.01-4

Issue: 3; 18.03.09

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