
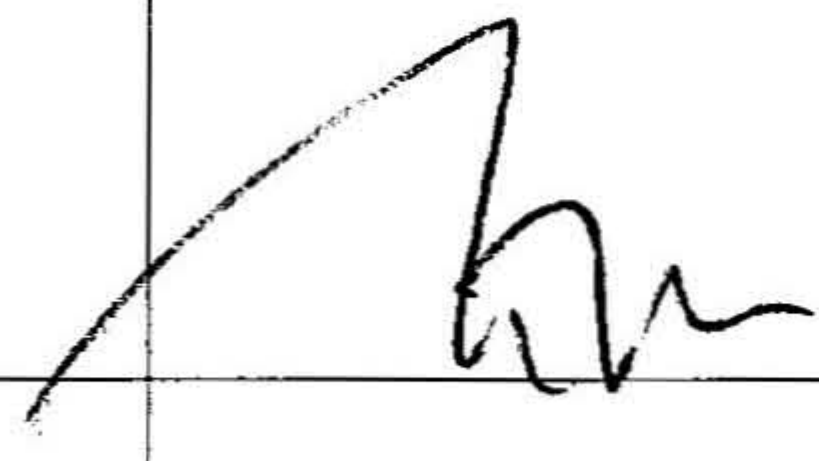





VL-PS-COG-VL248160-02 REV.A
(COG-VL248160)
NOV/2008
PAGE 1 OF 12

DOCUMENT NUMBER AND REVISION
VL-PS-COG-VL248160-02 REV. A
(COG-VL248160)

DOCUMENT TITLE:
PRELIMINARY SPECIFICATION
OF
LCD MODULE TYPE
MODEL NUMBER: COG-VL248160-02

DEPARTMENT	NAME	SIGNATURE	DATE
PREPARED BY	PHILIP CHENG		2008-11-19
CHECKED BY	TIM WONG		2008/11/19
APPROVED BY	CYRUS CHEUNG		2008/11/19

DISTRIBUTION LIST: MARKETING



DOCUMENT REVISION HISTORY 1:

DOCUMENT REVISION FROM TO	DATE	DESCRIPTION	CHANGED BY	CHECKED BY
A	2008.11.19	First Release. (Based on LCD counter drawing: COG-DEMO1003 (Rev.0))	PHILIP CHENG	TIM WONG

CONTENTS

	<u>Page No.</u>
1. GENERAL DESCRIPTION	4
2. MECHANICAL SPECIFICATIONS	4
3. INTERFACE SIGNALS	7
4. ABSOLUTE MAXIMUM RATINGS	8
4.1 ELECTRICAL MAXIMUM RATINGS - FOR IC ONLY	8
4.2 ENVIRONMENTAL CONDITION	8
5. ELECTRICAL SPECIFICATIONS	9
5.1 TYPICAL ELECTRICAL CHARACTERISTICS	9
5.2 TIMING SPECIFICATIONS	10
5.3 POWER-UP SEQUENCE	11
5.4 POWER-DOWN SEQUENCE	11
6. LCD COSMETIC CONDITIONS	12
7. REMARK	12

VARITRONIX LIMITED

Preliminary Specification of LCD Module Type Model No.: COG-VL248160-02

1. General Description

- 248x160 dots, FSTN, black & white, positive, transfective, LCD graphic module.
- Viewing angle: 12 o'clock.
- Driving scheme: 1/160 duty, 1/12 bias.
- 'ULTRA CHIP' UC1698u (COG) LCD controller-driver.
- 8-bit parallel bus (8080).
- Logic voltage: +3V.
- White LED02 backlight.

2. Mechanical Specifications

The mechanical detail is shown in Fig. 1 and summarized in Table 1 below.

Table 1

Parameter	Specifications	Unit
Outline dimensions	63.0(W) x 66.7(H) x 8.3(D)	mm
Viewing area	57.0(W) x 41.0(H)	mm
Active area	53.31(W) x 35.19(H)	mm
Display format	248(Horizontal) x 160(Vertical)	dots
Dot size	0.205(W) x 0.21(H)	mm
Dot spacing	0.01(W) x 0.01(H)	mm
Dot pitch	0.215(W) x 0.22(H)	mm
Weight	Approx. TBD	grams

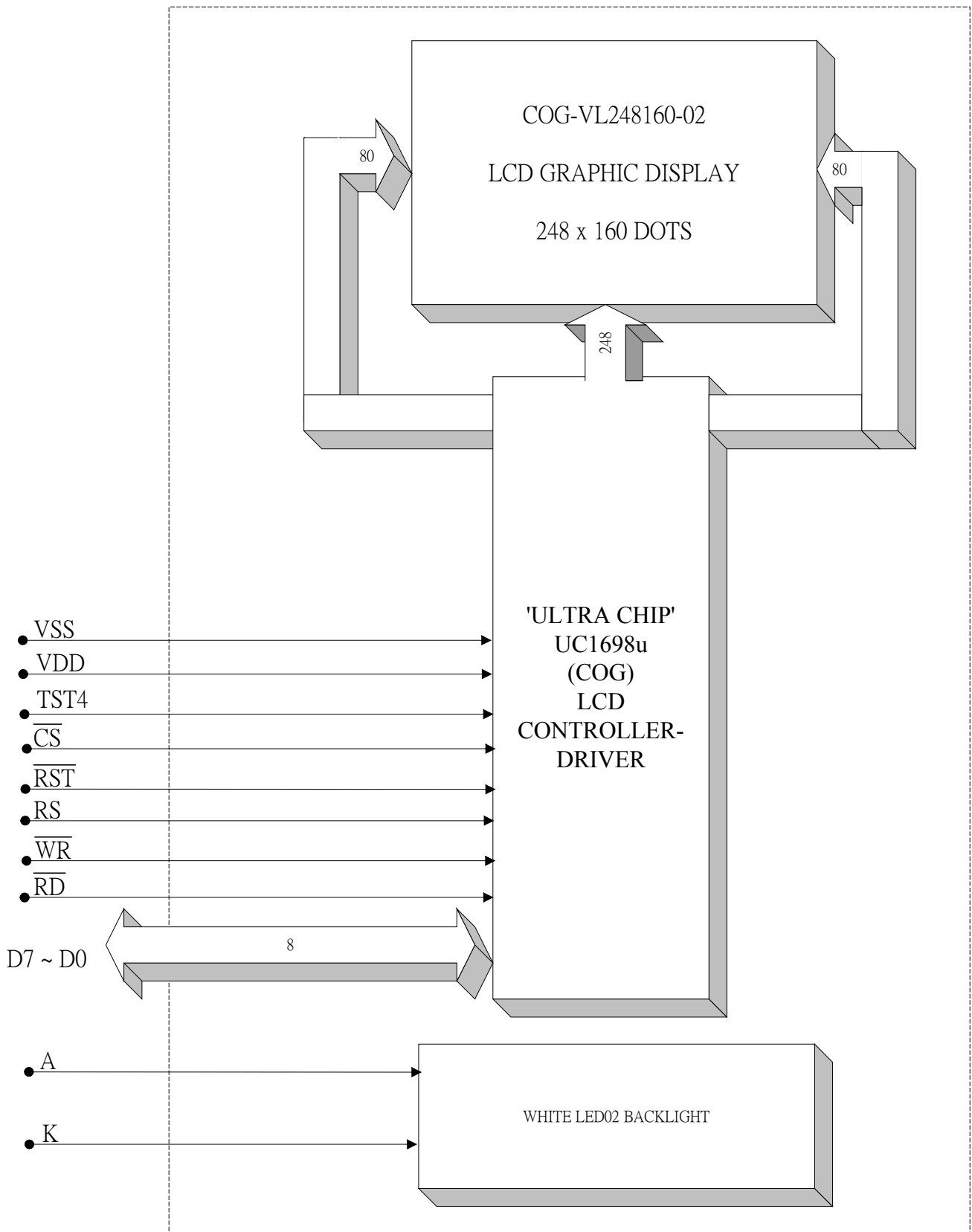


Figure 2: Block diagram

3. Interface signals

Table 2

Pin No.	Symbol	Description
1	NC	No connection.
2	NC	
3	VSS	Ground.
4	VDD	Power supply.
5	TST4	Test control. This pin has on-chip pull-up resistor. Leave it open during normal operation. TST4 is also used as one of the high voltage power supply for MTP programming operation. For COG designs, please wire out TST4 with trace resistance between 30~50 Ω.
6	NC	No connection.
7	\overline{CS}	\overline{CS} (CS0). Chip Select. Chip is selected when \overline{CS} = "L". When the chip is not selected, D[7:0] will be high impedance.
8	\overline{RST}	\overline{RST} (RST). When \overline{RST} ="L", all control registers are re-initialized by their default states. Since UC1698u has built-in Power-ON reset and software reset commands, \overline{RST} pin is not required for proper chip operation. An RC Filter has been included on-chip. There is no need for external RC noise filter. When \overline{RST} is not used, connect the pin to VDD.
9	RS	RS(CD). Select Control data or Display data for read/write operation. "L": Control data "H": Display data
10	\overline{WR}	\overline{RD} , \overline{WR} (WR[1:0]) controls the read/write operation of the host interface.
11	\overline{RD}	\overline{WR} (WR0):write. \overline{RD} (WR1):read.
12	D7	Bi-directional bus for parallel host interface.
13	D6	
14	D5	
15	D4	
16	D3	
17	D2	
18	D1	
19	D0	
20	VSS	Ground.
-	A	Anode of LED backlight.
-	K	Cathode of LED backlight.

4. Absolute Maximum Ratings

4.1 Electrical Maximum Ratings - for IC Only

Table 3

Parameter	Symbol	Min.	Max.	Unit
Supply voltage	VDD - VSS	-0.3	+4.0	V
LCD driving voltage(-25°C to +75°C)	VLCD	-0.3	+19.8	V
Digital input voltage	Vin	-0.4	VDD+0.5	V

Note:

- 1.) The modules may be destroyed if they are used beyond the absolute maximum ratings.
- 2.) VDD is based on VSS = 0V.

4.2 Environmental Condition

Table 4

Item	Operating temperature (Topr)		Storage temperature (Tstg) (Note 1)		Remark
	Min.	Max.	Min.	Max.	
Ambient temperature	-10°C	+70°C	-40°C	+80°C	Dry
Humidity (Note 1)	90% max. RH for Ta ≤ 40°C < 50% RH for 40°C < Ta ≤ Maximum operating temperature				No condensation
Vibration (IEC 68-2-6) cells must be mounted on a suitable connector	Frequency: 10 ~ 55 Hz Amplitude: 0.75 mm Duration: 20 cycles in each direction.				3 directions
Shock (IEC 68-2-27) Half-sine pulse shape	Pulse duration: 11 ms Peak acceleration: 981 m/s ² = 100g Number of shocks: 3 shocks in 3 mutually perpendicular axes.				3 directions

Note 1: Product cannot sustain at extreme storage conditions for long time.

5. Electrical Specifications

5.1 Typical Electrical Characteristics

At $T_a = 25\text{ }^\circ\text{C}$, $V_{DD}=3\text{V}\pm 5\%$, $V_{SS}=0\text{V}$.

Table 5

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Supply voltage (Logic)	VDD-VSS		2.85	3	3.15	V
LCD driving voltage (built-in)	VLCD	$T_a = -10\text{ }^\circ\text{C}$, VDD=3V, Note 1	-	TBD	-	V
		$T_a = +25\text{ }^\circ\text{C}$, VDD=3V, Note 1	-	TBD	-	V
		$T_a = +70\text{ }^\circ\text{C}$, VDD=3V, Note 1	-	TBD	-	V
Input logic HIGH	V_{IH}		0.8 VDD	-	-	V
Input logic LOW	V_{IL}		-	-	0.2VDD	V
Supply Current (Logic & LCD)	IDD	Character mode	-	TBD	-	mA
		Checker board mode	-	TBD	-	mA
Supply voltage of white LED02 backlight	VLED	Forward current =TBDmA	-	5	-	V
Luminance(on the backlight surface)		Number of LED chips =TBD	-	TBD	-	cd/m ²

Note 1 : There is tolerance in optimum LCD driving voltage during production and it will be within the specified range.

5.2 Timing Specifications

5.2.1 Reset Characteristics

At $T_a = -10^{\circ}\text{C}$ to $+70^{\circ}\text{C}$, $V_{DD}=3\text{V}\pm 5\%$, $V_{SS} = 0\text{V}$

Refer to Fig. 3, the reset characteristics.

Table 6

Symbol	Signal	Description	Condition	Min.	Max.	Units
t_{RW}	RST	Reset low pulse width		3	–	μS
t_{RD}	RST, WR	Reset to WR pulse delay		10	–	mS

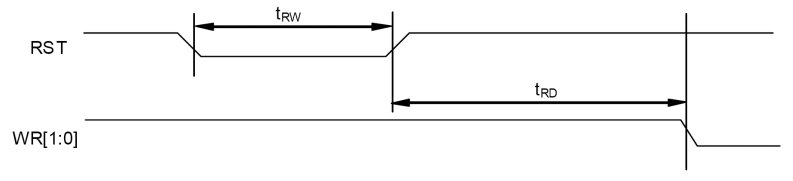


Figure 3: Reset characteristics.

5.2.2 Parallel Bus Timing Characteristics (for 8080 MCU)

At $T_a = -10^{\circ}\text{C}$ to $+70^{\circ}\text{C}$, $V_{DD}=3\text{V}\pm 5\%$, $V_{SS} = 0\text{V}$

Refer to Fig. 4, Parallel Bus Timing Characteristics (for 8080 MCU)

Table 7

Symbol	Signal	Description	Condition	Min.	Max.	Units
t_{AS80}	CD	Address setup time		0	–	nS
t_{AH80}	CD	Address hold time		0	–	nS
t_{CY80}		System cycle time			–	nS
		16-bit bus (read)		170		
		(write)		130		
		8-bit bus (read)		100		
		(write)		80		
t_{PWR80}	WR1	Pulse width 16-bit (read)		85	–	nS
		8-bit		50		
t_{PWW80}	WR0	Pulse width 16-bit (write)		65	–	nS
		8-bit		40		
t_{HPW80}	WR0, WR1	High pulse width			–	nS
		16-bit bus (read)		85		
		(write)		65		
		8-bit bus (read)		50		
		(write)		40		
t_{DS80}	D0-D15	Data setup time		30	–	nS
t_{DH80}	D0-D15	Data hold time		0	–	nS
t_{ACC80}		Read access time	$C_L = 100\text{pF}$	–	60	nS
t_{OD80}		Output disable time		15	30	nS
t_{CSSA80}	CS1/CS0	Chip select setup time		5		nS
t_{CSH80}	CS1/CS0	Chip select hold time		5		nS

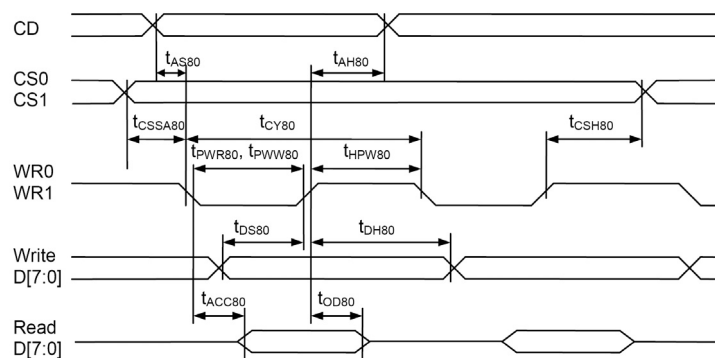


Figure 4: Parallel Bus Timing Characteristics (for 8080 MCU)

5.3 Power-Up Sequence

UC1698u power-up sequence is simplified by builtin “Power Ready” flags and the automatic invocation of *System-Reset* command after *Power-ON-Reset*.

System programmers are only required to wait 150 ms before the CPU starting to issue commands to UC1698u. No additional time sequences are required between enabling the charge pump, turning on the display drivers, writing to RAM or any other commands. However, while turning on VDD, VDD2/3 should be started not later than VDD.

Delay allowance between VDD and VDD2/3 is illustrated as Figure 7.

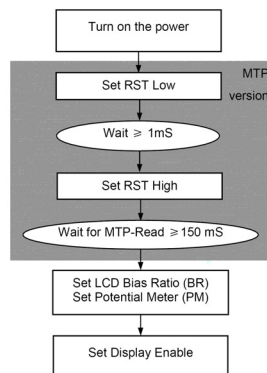


Figure 5: Reference Power-Up Sequence

5.4 Power-Down Sequence

To prevent the charge stored in capacitors CBX+, CBX-, and CL from damaging the LCD, when VDD is switched off, use Reset mode to enable the built-in draining circuit and discharge these capacitors.

The draining resistor is 10KΩ for both VLCD and VB+. It is recommended to wait 3 x RC for VLCD and 1.5 x RC for VB+. For example, if CL is 0.1uF, then the draining time required for VLCD is ~3mS.

When internal VLCD is not used, UC1698u will *NOT* drain VLCD during RESET. System designers need to make sure external VLCD source is properly drained off before turning off VDD.

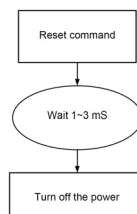


Figure 6: Reference Power-Down Sequence

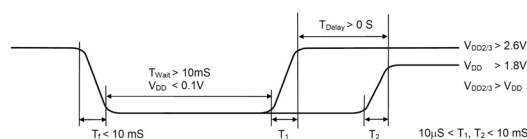


Figure 7: Delay allowance between VDD and VDD2/3

6. LCD Cosmetic Conditions

Refer to the document: TBD.

Note: LCD size of the product is TBD.

7. Remark

HANDLING LCD AND LCD MODULES

1. Liquid Crystal Display (LCD)

LCD is made up of glass, organic sealant, organic fluid and polymer based polarizers. The following precautions should be taken when handling:

- (1) Keep the temperature within range for use and storage. Excessive temperature and humidity could cause polarization degradation, polarizer peel-off or bubble generation. When storage for a long period over 40° C is required, the relative humidity should be kept below 60%.
- (2) Do not contact the exposed polarizers with anything harder than an HB pencil lead. To clean dust off the display surface, wipe gently with cotton, chamois or other soft material soaked in petroleum benzin. Never scrub hard.
- (3) Varitronix does not responsible for any polarizer defect after the protective film has been removed from the display
- (4) Wipe off saliva or water drops immediately. Contact with water over a long period of time may cause polarizer deformation or color fading, while an active LCD with water condensation on its surface will cause corrosion ofITO electrodes.
- (5) PETROLEUM BENZIN is recommended to remove adhesives used to attach front/rear polarizers and reflectors, while chemicals like acetone, toluene, ethanol and isopropyl alcohol will cause damage to the polarizer. Avoid oil and fats. Avoid lacquer and epoxies which might contain solvents and hardeners to cause electrode erosion. Some solvents will also soften the epoxy covering the DIL pins and thereby weakening the adhesion of the epoxy on glass. This will cause the exposed electrodes to erode electrochemically when operating in high humidity and condensing environment.
- (6) Glass can be easily chipped or cracked from rough handling, especially at corners and edges.
- (7) Do not drive LCD with DC voltage.
- (8) When soldering DIL pins, avoid excessive heat and keep soldering temperature between 260°C to 300°C for no more than 5 seconds. Never use wave or reflow soldering.

2. Liquid Crystal Display Modules (MDL)

2.1 Mechanical Considerations

MDL's are assembled and adjusted with a high degree of precision. Avoid excessive shocks and do not make any alterations or modifications. The following should be noted.

- (1) Do not tamper in any way with the tabs on the metal frame.
- (2) Do not modify the PCB by drilling extra holes, changing its outline, moving its components or modifying its pattern.
- (3) Do not touch the elastomer connector (conductive rubber), especially when inserting an EL panel.

- (4) When mounting a MDL make sure that the PCB is not under any stress such as bending or twisting. Elastomer contacts are very delicate and missing pixels could result from slight dislocation of any of the elements.
- (5) Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels.
- (6) If FPCA need to be bent, please refer the suggested bending area on the specification. The stiffener and component area on FPC/FPC/COF must not be bent during or after assembly (Note: for those models with FPC/FPC/COF +stiffener).
- (7) Sharp bending should be avoided on FPC to prevent track cracking.

2.2 Static Electricity

MDL contains CMOS LSI's and the same precaution for such devices should apply, namely:

- (1) The operator should be grounded whenever he comes into contact with the module. Never touch any of the conductive parts such as the LSI pads, the copper leads on the PCB and the interface terminals with any part of the human body.
- (2) The modules should be kept in antistatic bags or other containers resistant to static for storage.
- (3) Only properly grounded soldering irons should be used.
- (4) If an electric screwdriver is used it should be well grounded and shielded from commutator sparks.
- (5) The normal static prevention measures should be observed for work clothes and working benches; for the latter conductive (rubber) mat is recommended.
- (6) Since dry air is inductive to statics, a relative humidity of 50 - 60% is recommended.

2.3 Soldering

- (1) Solder only to the I/O terminals.
- (2) Use only soldering irons with proper grounding and no leakage.
- (3) Soldering temperature is 280°C ± 10°C .
- (4) Soldering time: 3 to 4 seconds.
- (5) Use eutectic solder with resin flux fill.
- (6) If flux is used, the LCD surface should be covered to avoid flux spatters. Flux residue should be removed afterwards.
- (7) Use proper de-soldering methods (e.g. suction type desoldering irons) to remove lead wires from the I/O terminals when necessary. Do not repeat the soldering/ desoldering process more than three times as the pads and plated through holes may be damaged.

2.4 Label

Identification labels will be stuck on the module without

obstructing the viewing area of display.

3. Operation

- (1) The viewing angle can be adjusted by varying the LCD driving voltage Vo.
- (2) Driving voltage should be kept within specified range, excess voltage shortens display life.
- (3) Response time increases with decrease in temperature.
- (4) Display may turn black or dark Blue at temperatures above its operational range; this is however not destructive and the display will return to normal once the temperature falls back to range.
- (5) Mechanical disturbance during operation (such as pressing on the viewing area) may cause the segments to appear "fractured". They will recover once the display is turned off.
- (6) Condensation at terminals will cause malfunction and possible electrochemical reaction. Relative humidity of the environment should therefore be kept below 60%.
- (7) Display performance may vary out of viewing area. If there is any special requirement on performance out of viewing area, please consult Varitronix.

4. Storage and Reliability

- (1) LCD's should be kept in sealed polyethylene bags while MDL's should use antistatic ones. If properly sealed, there is no need for desiccant.
- (2) Store in dark places and do not expose to sunlight or fluorescent light. Keep the temperature between 0°C and 35°C and the relative humidity low. Please consult VARITRONIX for other storage requirements.
- (3) Water condensation will affect reliability performance of the display and is not allowed.
- (4) Semi-conductor device on the display is sensitive to light and should be protected properly.
- (5) Power up/down sequence.
 - a) Power Up: in general, LCD supply voltage, Vo must be supplied after logic voltage, VDD becomes steady. Please refer to related IC data sheet for details.
 - b) Power Down: in general, LCD supply voltage, Vo must be removed before logic voltage, VDD turns off. Please refer to related IC data sheet for details.

5. Safety

If any fluid leaks out of a damaged glass cell, wash off any human part that comes into contact with soap and water. Never swallow the fluid. The toxicity is extremely low but caution should be exercised at all times.

LIMITED WARRANTY

VARITRONIX LCDs and modules are not consumer products, but may be incorporated by VARITRONIX's customers into consumer products or components thereof. VARITRONIX does not warrant that its LCDs and components are fit for any such particular purpose.

- 1. The liability of VARITRONIX is limited to repair or replacement on the terms set forth below. VARITRONIX will not be responsible for any subsequent or consequential events or injury or damage to any personnel or user including third party personnel and/or user.

Unless otherwise agreed in writing between VARITRONIX and the customer, VARITRONIX will only replace or repair any of its LCD which is found defective electrically or visually when inspected in

accordance with VARITRONIX LCD Acceptance Standards (copies available on request), for a period of one year from the date of shipment. Confirmation of such date shall be based on freight documents.

- 2. No warranty can be granted if any of the precautions stated in HANDLING LCD and LCD Modules above have been disregarded. Broken glass, scratches on polarizers, mechanical damages as well as defects that are caused by accelerated environmental tests are excluded from warranty.
- 3. In returning the LCD and Modules, they must be properly packaged and there should be detailed description of the failures or defects.

IMPORTANT NOTICE

The information presented in this document has been carefully checked and is believed to be accurate, however, no responsibility is assumed for inaccuracies. VARITRONIX reserves the right to make changes to any specifications without further notice for performance, reliability, production technique and other considerations, VARITRONIX does not assume any liability arising out of the application or use of products herein. Please see Limited Warranty in the previous section.

“Varitronix Limited reserves the right to change this specification.”

Tel:(852) 2197-6000.

Fax:(852) 2343-9555.

URL:<http://www.varitronix.com>

- END -