



UT30P04

Preliminary

Power MOSFET

P-CHANNEL LOGIC LEVEL ENHANCEMENT MODE FIDLD EFFECT TRANSISTOR

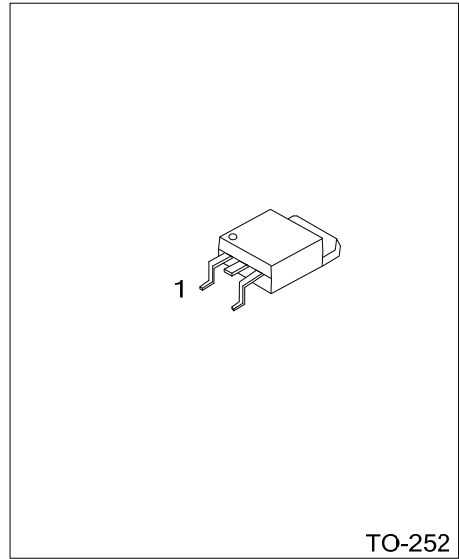
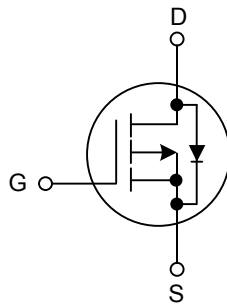
DESCRIPTION

The UTC **UT30P04** is a P-channel enhancement mode Power MOSFET, providing customers fast switching, ruggedized device design, low on-resistance and cost-effectiveness with UTC's advanced technology.

FEATURES

- * Low on-Resistance
- * Fast Switching Speed
- * Halogen Free

SYMBOL



ORDERING INFORMATION

Ordering Number	Package	Pin Assignment			Packing
		1	2	3	
UT30P04G-TN3-R	TO-252	G	D	S	Tape Reel

Note: G:Gate, D:Drain, S:Source

UT30P04G-TN3-R (1)Packing Type (2)Package Type (3)Halogen Free	(1) R: Tape Reel (2) TN3: TO-252 (3) G: Halogen Free
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■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C Unless Otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	V _{DS}	-40	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current	I _D	T _C =25°C	-21
		T _C =70°C	-17
Pulsed Drain Current (Note 2)	I _{DM}	-70	A
Avalanche Current	I _{AS}	-27	
Avalanche Energy (Note 3)	E _{AS}	36	mJ
Power Dissipation	P _D	T _C =25°C	30
		T _C =70°C	20
Operating Junction Temperature	T _J	-55~150	°C
Storage Temperature	T _{STG}	-55~150	°C

Note: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Pulse width limited by maximum junction temperature.

3. V_{DD} = -20V. Starting T_J = 25°C.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	θ _{JA}	40	°C/W
Junction to Case	θ _{JC}	4.1	°C/W

■ ELECTRICAL CHARACTERISTICS (T_J = 25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D = -250μA, V _{GS} = 0V	-40			V
Drain-Source Leakage Current	I _{DSS}	V _{DS} = -32V, V _{GS} = 0V			1	μA
		V _{DS} = -30V, V _{GS} = 0V, T _J = 125°C			10	
Gate- Source Leakage Current	I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V			±250	nA
On-State Drain Current (Note 1)	I _{D(ON)}	V _{DS} = -5V, V _{GS} = -10V	-70			A
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} = V _{GS} , I _D = -250μA	-2.1	-2.5	-3	V
Static Drain-Source On-State Resistance (Note 1)	R _{DS(ON)}	V _{GS} = -5V, I _D = -8A		65	73	mΩ
		V _{GS} = -7V, I _D = -8A		35	50	
		V _{GS} = -10V, I _D = -10A		30	40	
Forward Transconductance (Note 1)	g _{FS}	V _{DS} = -10V, I _D = -10A		20		S
Gate Resistance	R _g	V _{GS} = 0V, V _{DS} = 0V, f = 1.0MHz		4.95		Ω
DYNAMIC PARAMETERS						
Input Capacitance	C _{ISS}	V _{GS} = 0V, V _{DS} = -20V, f = 1.0MHz		1090		pF
Output Capacitance	C _{OSS}			175		
Reverse Transfer Capacitance	C _{RSS}			91		
SWITCHING PARAMETERS (Note 2)						
Total Gate Charge	Q _G (V _{GS} = -10V)	V _{DS} = 0.5V _{(BR)DSS} , I _D = -18A		17		nC
	Q _G (V _{GS} = -4.5V)			8.5		
Gate to Source Charge	Q _{GS}			5.5		
Gate to Drain Charge	Q _{GD}			3		
Turn-ON Delay Time	t _{D(ON)}	V _{GS} = -10V, V _{DS} = -20V, I _D ≈ -10A, R _{GS} = 6Ω, R _L = 2Ω		6		ns
Rise Time	t _R			16		ns
Turn-OFF Delay Time	t _{D(OFF)}			26		ns
Fall-Time	t _F			10		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Continuous Current	I _S				-21	A
Drain-Source Diode Forward Voltage ¹	V _{SD}	I _F = -10A, V _{GS} = 0V			-1.2	V
Reverse Recovery Time	t _{RR}	I _F = -10A, dI _F /dt = 100A/μs		15.5		ns
Reverse Recovery Charge	Q _{RR}			7.9		nC

Note: 1. Pulsed test: Pulse width ≤ 300μsec, duty cycle ≤ 2%.

2. Independent of operating temperature.

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