19-2619; Rev 1; 2/04

MAXIM

65V, Low-Quiescent-Current, High-Voltage Linear Regulators with µP Reset and Watchdog Timer

General Description

The MAX5023/MAX5024 high-voltage linear regulators operate from a +6.5V to +65V input voltage and deliver up to 150mA of output current. These devices consume only 60µA of quiescent current with no load and withstand a -60V reverse-battery voltage at the input. The MAX5023/MAX5024 include an active-low internal microprocessor (µP) reset circuit that asserts when the regulator output drops below the preset output voltage threshold by 7.5% or 12.5%, depending on the device selected. Both devices are available with a fixed +3.3V or +5V output. These devices are short-circuit protected and include thermal shutdown.

In addition to an enable input to turn on or off the regulator, the MAX5023/MAX5024 include a $\overline{HOLD}$ input that allows for the implementation of a self-holding circuit without requiring external components. Setting $\overline{HOLD}$ low after enabling the regulator, forces the regulator to remain on even if EN is subsequently set low. Releasing $\overline{HOLD}$ shuts down the regulator.

The MAX5023 includes a watchdog input that monitors a pulse train from the µP and generates reset pulses if the watchdog input remains high or low for a duration longer than the 1.6s watchdog timeout period. The MAX5024 includes a SET input which, when connected to ground, selects a preset output voltage of +3.3V (MAX5024S/MAX5024T) or +5V (MAX5024L/MAX5024M). Set the adjustable output voltage by connecting SET to the regulator's output through a resistive-divider network.

The MAX5023/MAX5024 operate over the automotive temperature range (-40°C to +125°C) and are available in a thermally enhanced, surface-mount 8-pin SO-EP package.

Applications

Automotive
Industrial
Home Security
Fire/Smoke Alarms
Telecom/Networking

Features

- **Wide Operating Input Voltage Range +6.5V to +65V**
- **Thermally Enhanced 8-Pin SO Package with Exposed Pad Dissipates 1.5W**
- **Guaranteed 150mA Output Current**
- **60µA No-Load Supply Current**
- **-60V Reverse-Battery Protection**
- **Preset +3.3V or +5.0V Output Voltage**
- **Thermal and Short-Circuit Protection**
- **Operate Over -40°C to +125°C Temperature Range**
- **Integrated µP Reset Circuit**
- **Watchdog Timer with 1.6s Timeout Period (MAX5023)**
- **Regulator Enable and Hold Inputs Implement Self-Holding Circuit**
- **SET Input for Adjustable Output Voltage (MAX5024)**

Ordering Information

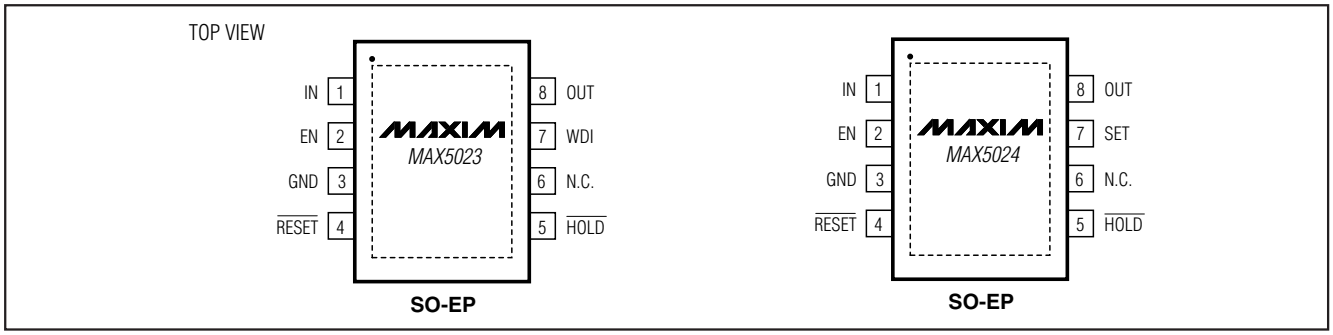
PART	TEMP RANGE	PIN-PACKAGE
MAX5023_ ASA	-40°C to +125°C	8 SO-EP*
MAX5024_ ASA	-40°C to +125°C	8 SO-EP*

Note: *These parts offer a choice of reset thresholds, reset threshold tolerances, and regulator output voltages. From the Selector Guide, insert the desired suffix letter into the blank to complete the part number.*

**EP = Exposed Paddle.*

Selector Guide and Typical Operating Circuit appear at end of data sheet.

Pin Configurations



65V, Low-Quiescent-Current, High-Voltage Linear Regulators with µP Reset and Watchdog Timer

ABSOLUTE MAXIMUM RATINGS

IN to GND .....-60V to +70V
EN to GND .....-0.3V to (V_{IN} + 0.3V)
$\overline{HOLD}$ to GND.....-0.3V to (V_{OUT} + 0.3V)
SET, WDI, OUT to GND.....-0.3V to +13.2V
$\overline{RESET}$ to GND (Open Drain).....-0.3V to +13.2V
Short-Circuit Duration.....Continuous
Maximum Current to Any Pin (Except IN, OUT).....±20mA
Continuous Power Dissipation (T_A = +70°C)
8-Pin SO-EP (derate 19.2mW/°C above +70°C).....1538mW

Thermal Resistance:
(θ_{JA}) .....52°C/W
(θ_{JC}) .....2°C/W
Operating Temperature Range .....-40°C to +125°C
Junction Temperature.....+150°C
Storage Temperature Range .....-65°C to +150°C
Lead Temperature (soldering, 10s) .....+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(V_{IN} = +14V, I_{OUT} = 1mA, C_{IN} = 10µF, C_{OUT} = 15µF, V_{EN} = +2.4V, $\overline{HOLD}$ = open, 10kΩ from $\overline{RESET}$ to OUT, T_A = -40°C to +125°C, unless otherwise noted. Typical specifications are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Input Voltage Range	V_{IN}	V_{IN} must be at least 1.5V greater than V_{OUT}		6.5		65	V
Reverse Input Current	$I_{REVERSE}$	V_{IN} = -60V			0.1	10	µA
Supply Current	I_Q	Measured at GND	I_{OUT} = 0mA		58	140	µA
			I_{OUT} = 150mA		2000		
Shutdown Supply Current	I_{SHDN}	V_{EN} ≤ +0.4V			6	16	µA
REGULATOR							
Guaranteed Output Current	I_{OUT}	V_{OUT} = +5V		150			mA
Output Voltage	V_{OUT}	SET = GND, I_{OUT} = 1mA to 150mA, 5V version		4.8	5	5.2	V
		SET = GND, I_{OUT} = 1mA to 150mA, 3.3V version		3.168	3.3	3.432	
		I_{OUT} = 5mA, adjustable version (MAX5024)		2.5		11	
Dropout Voltage	ΔV_{DO}	I_{LOAD} = 150mA, V_{OUT} = +5V (Note 2)			0.9	1.5	V
Startup Response Time		Rising edge of V_{IN} to V_{OUT}, R_L = 500Ω, SET = GND			400		µs
Line Regulation	$\Delta V_{OUT}/\Delta V_{IN}$	+8V ≤ V_{IN} ≤ +65V	+5V version	-1		+1	mV/V
			+3.3V version	-0.5		0.5	
Enable Voltage	V_{EN}	EN = high, regulator on		2.4			V
		EN = low, regulator off				0.4	
Enable Input Current	I_{EN}	V_{EN} = +2.4V			0.5		µA
		V_{EN} = +14V			4		
$\overline{HOLD}$ Voltage	V_{IL}	Regulator on, EN transition from high to low				0.4	V
$\overline{HOLD}$ Release Voltage	V_{IH}	EN = low, regulator shuts off		V_{OUT} - 0.4V			V
$\overline{HOLD}$ Pullup Current	$I_{\overline{HOLD}}$	Internally connected to OUT			4		µA
SET Reference Voltage	V_{SET}	I_{OUT} = 10mA		1.223	1.248	1.273	V
SET Input Leakage Current	I_{SET}				0.5	100	nA

ELECTRICAL CHARACTERISTICS (continued)

(V_{IN} = +14V, I_{OUT} = 1mA, C_{IN} = 10µF, C_{OUT} = 15µF, V_{EN} = +2.4V, $\overline{HOLD}$ = open, 10kΩ from $\overline{RESET}$ to OUT, T_A = -40°C to +125°C, unless otherwise noted. Typical specifications are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Load Regulation	$\Delta V_{OUT}/\Delta I_{OUT}$	I_{OUT} = 1mA to 150mA		1		mV/mA
Power-Supply Rejection Ratio	PSRR	I_{OUT} = 10mA, f = 100Hz, 500mV_{P-P}, V_{OUT} = +5V		54		dB
Short-Circuit Current	I_{SC}	V_{IN} = +8V	175	300		mA
Thermal-Shutdown Temperature	$T_{J(SHDN)}$			150		°C
Thermal-Shutdown Hysteresis	$\Delta T_{J(SHDN)}$			20		°C
RESET CIRCUIT						
Reset Threshold	V_{TH}	MAX502_L, SET = GND	4.50	4.625	4.75	V
		MAX502_M, SET = GND	4.25	4.375	4.50	
		MAX502_T, SET = GND	2.970	3.052	3.135	
		MAX502_S, SET = GND	2.805	2.887	2.970	
		MAX5024L/T, SET = Divider (Figure 1) (Note 3)		0.925 x V_{OUT}		
		MAX5024M/S, SET = Divider (Figure 1) (Note 3)		0.875 x V_{OUT}		
Reset Timeout Period	t_{RP}		140	200	260	ms
V_{OUT} to Reset Delay	t_{RD}	V_{OUT} falling		5		µs
Open-Drain $\overline{RESET}$ Output Voltage (Note 4)	V_{OL}	V_{OUT} ≥ +1.0V, I_{SINK} = 50µA, $\overline{RESET}$ asserted			0.3	V
		V_{OUT} ≥ +2.85V, I_{SINK} = 1.2mA, $\overline{RESET}$ asserted			0.3	
		V_{OUT} ≥ +4.25V, I_{SINK} = 3.2mA, $\overline{RESET}$ asserted			0.4	
Open-Drain $\overline{RESET}$ Output-Leakage Current	I_{LKG}	$\overline{RESET}$ not asserted, $V_{\overline{RESET}}$ = +11V			1.0	µA
WATCHDOG FUNCTION						
Watchdog Timeout Period	t_{WD}		1.12	1.6	2.08	s
WDI Pulse Width	t_{WDI}	(Note 5)	50			ns
WDI Input Voltage	V_{IL}				0.4	V
	V_{IH}		2.4			
WDI Input Current	I_{WDI}	WDI = GND	-1		+1	µA

Note 1: Limits at -40°C are guaranteed by characterization and not production tested.

Note 2: Dropout voltage is defined as V_{IN} - V_{OUT} when V_{OUT} is 100mV below the value of V_{OUT} for V_{IN} = V_{OUT} + 3V.

Note 3: V_{OUT} = V_{SET} (1 + R1 / R2) = 1.248V (1 + R1 / R2).

Note 4: $\overline{RESET}$ is guaranteed to be in the correct logic state for V_{OUT} > +1V.

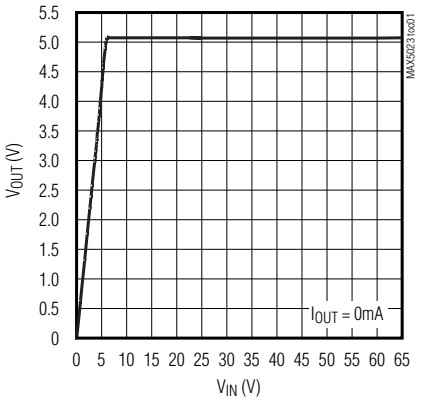
Note 5: Guaranteed by design, not production tested.

65V, Low-Quiescent-Current, High-Voltage Linear Regulators with µP Reset and Watchdog Timer

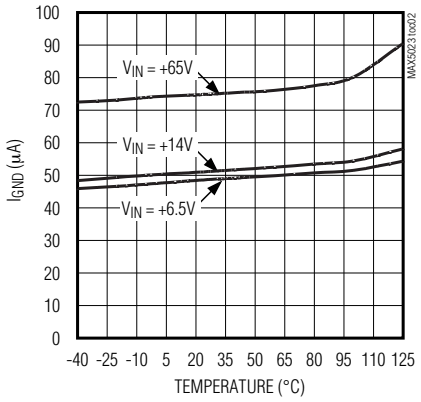
Typical Operating Characteristics

(V_{IN} = +14V, C_{IN} = 10µF, C_{OUT} = 15µF, V_{EN} = +2.4V, V_{OUT} = +5V, SET = GND, T_A = +25°C, unless otherwise specified.)

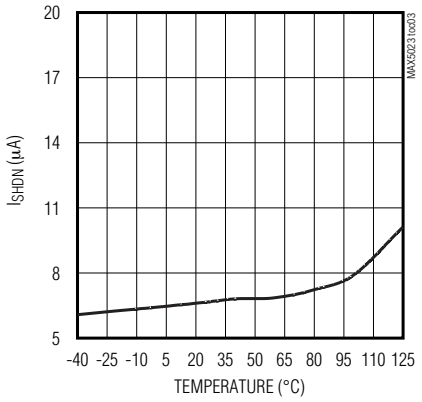
OUTPUT VOLTAGE vs. INPUT VOLTAGE



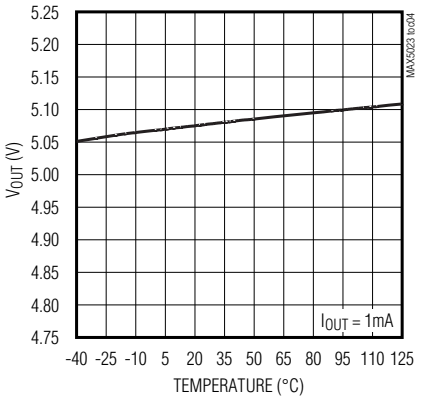
NO-LOAD GROUND CURRENT vs. TEMPERATURE



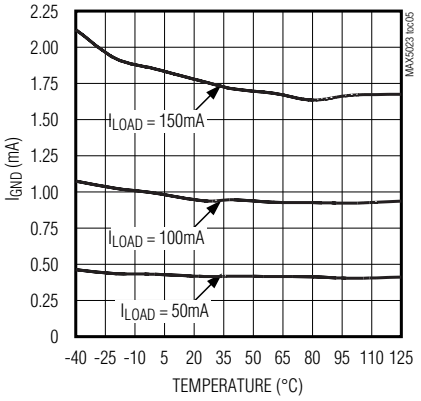
SHUTDOWN SUPPLY CURRENT vs. TEMPERATURE



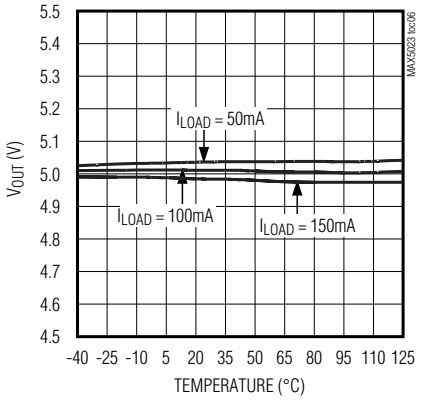
OUTPUT VOLTAGE vs. TEMPERATURE



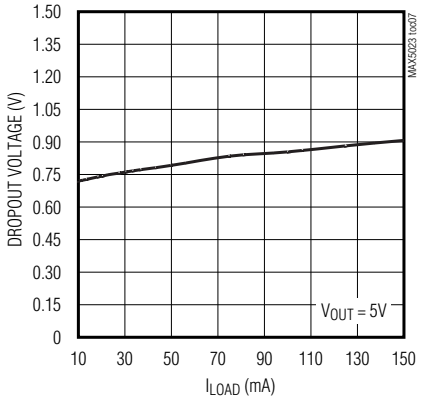
GROUND CURRENT vs. TEMPERATURE



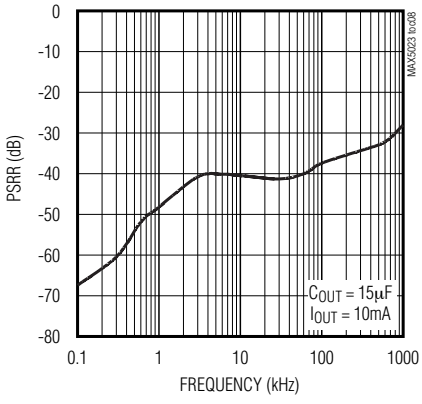
OUTPUT VOLTAGE vs. LOAD CURRENT AND TEMPERATURE



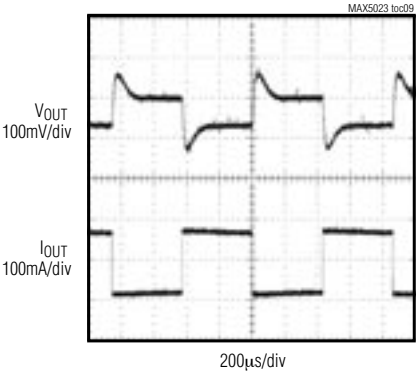
DROPOUT VOLTAGE vs. LOAD CURRENT



POWER-SUPPLY REJECTION RATIO vs. FREQUENCY



LOAD-TRANSIENT RESPONSE

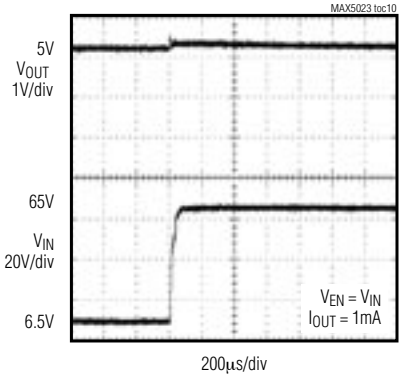


65V, Low-Quiescent-Current, High-Voltage Linear Regulators with µP Reset and Watchdog Timer

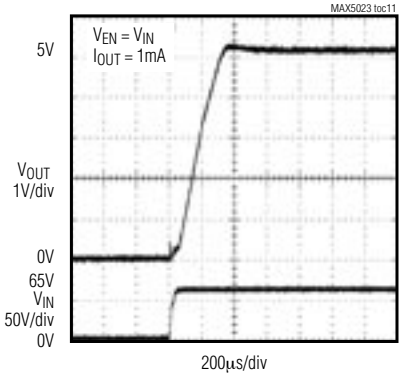
Typical Operating Characteristics (continued)

(V_{IN} = +14V, C_{IN} = 10µF, C_{OUT} = 15µF, V_{EN} = +2.4V, V_{OUT} = +5V, SET = GND, T_A = +25°C, unless otherwise specified.)

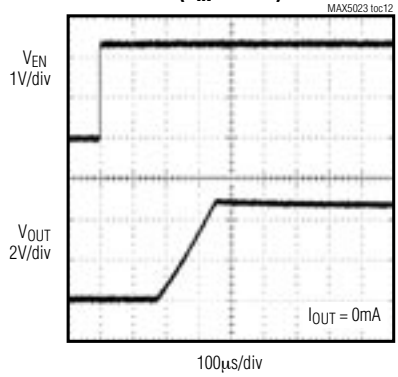
INPUT VOLTAGE STEP RESPONSE



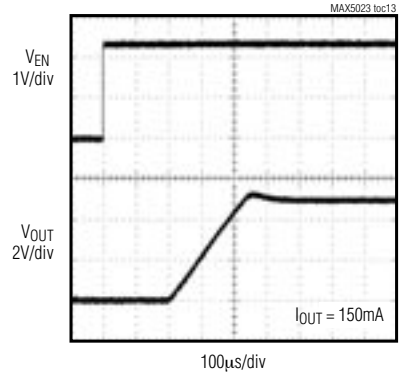
STARTUP RESPONSE



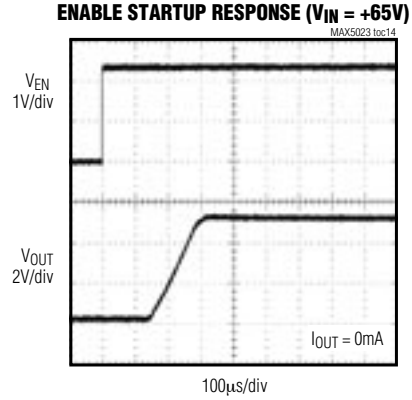
ENABLE STARTUP RESPONSE (V_{IN} = +14V)



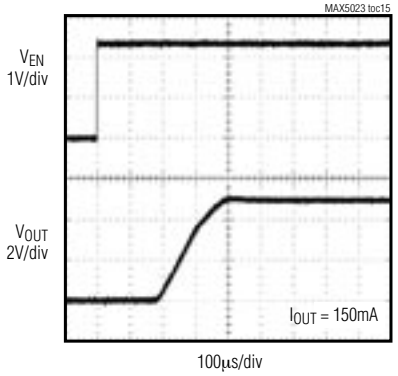
ENABLE STARTUP RESPONSE (V_{IN} = +14V)



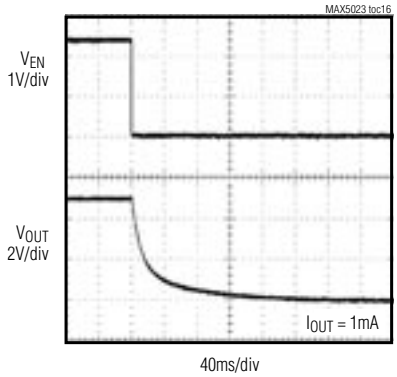
ENABLE STARTUP RESPONSE (V_{IN} = +65V)



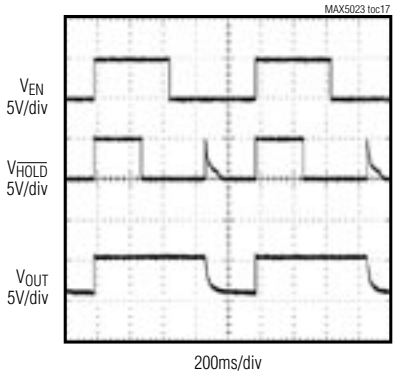
ENABLE STARTUP RESPONSE (V_{IN} = +65V)



SHUTDOWN RESPONSE



ENABLE AND $\overline{HOLD}$ TIMING

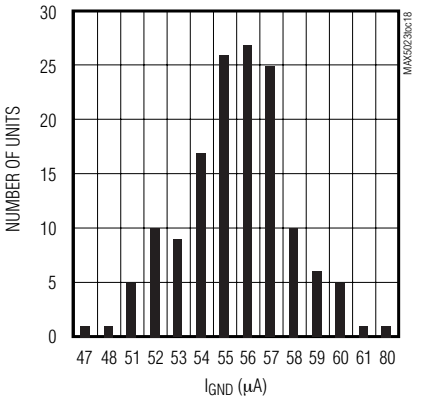


65V, Low-Quiescent-Current, High-Voltage Linear Regulators with µP Reset and Watchdog Timer

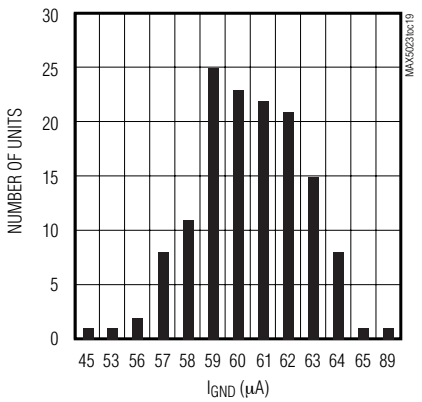
Typical Operating Characteristics (continued)

(V_{IN} = +14V, C_{IN} = 10µF, C_{OUT} = 15µF, V_{EN} = +2.4V, V_{OUT} = +5V, SET = GND, T_A = +25°C, unless otherwise specified.)

GROUND-CURRENT DISTRIBUTION (T_A = +40°C)



GROUND-CURRENT DISTRIBUTION (T_A = +125°C)



Pin Description

PIN		NAME	FUNCTION
MAX5023	MAX5024		
1	1	IN	Regulator Input. Supply voltage ranges from +6.5V to +65V. Bypass with a 10µF capacitor to GND.
2	2	EN	Enable Input. Force EN high to turn on the regulator. Pull EN low and force $\overline{HOLD}$ high (or open circuit) to place the device in shutdown mode. Internally connected to ground through a 5MΩ resistor.
3	3	GND	Ground. GND also functions as a heatsink. Solder to large pads or the circuit-board ground plane to maximize thermal dissipation.
4	4	$\overline{RESET}$	Active-Low Open-Drain Reset Output. $\overline{RESET}$ remains low while V_{OUT} is below the reset threshold or when WDI is not pulsed within 1.6s. $\overline{RESET}$ remains low for the duration of the reset timeout period after the reset conditions are terminated.
5	5	$\overline{HOLD}$	Regulator Hold Input. When $\overline{HOLD}$ is forced low, the regulator stores the on state of the output allowing the regulator to function even if EN is pulled low. To shutdown the regulator, release $\overline{HOLD}$ after EN is pulled low. If $\overline{HOLD}$ is unused, either float $\overline{HOLD}$ or connect to OUT. Internally connected to OUT through a 4µA pullup current source (see Table 1, Truth Table).
6	6	N.C.	No Connection. Not internally connected.
7	—	WDI	Watchdog Timer Input (MAX5023 only). The watchdog timer asserts a reset if WDI does not transition within the 1.6s watchdog timeout period. WDI **cannot** be disabled.
8	8	OUT	Regulator Output. Fixed (+3.3V or +5V) or adjustable (+2.5V to +11V). Bypass with a 15µF capacitor (min).
—	7	SET	Feedback Input for Setting the Output Voltage. Connect to GND to set the output voltage to the preset fixed value (+3.3V or +5V). Connect to an external resistor-divider network for adjustable output operation (MAX5024 only).
EP	EP	PAD	Exposed Pad. Connect pad to GND.

65V, Low-Quiescent-Current, High-Voltage Linear Regulators with µP Reset and Watchdog Timer

Detailed Description

The MAX5023/MAX5024 high-voltage linear regulators include an integrated µP reset circuit and watchdog timer/adjustable output voltage. The devices guarantee 150mA load drive and are available with preset output voltages of +3.3V or +5V. The MAX5023 features a watchdog timer (WDI) with a 1.6s timeout period. The MAX5024 offers an adjustable output voltage using an external resistive-divider network between SET and OUT. The internal reset circuit monitors the regulator output voltage and asserts a reset output when the regulator output falls below the µP supply tolerance. Other features include reverse-voltage protection to -60V, enable (EN) and hold ($\overline{HOLD}$) regulator control inputs, 16µA (max) shutdown current, short-circuit protection, and thermal shutdown.

Regulator

The regulator accepts an input voltage range from +6.5V to +65V. The MAX5023/MAX5024 offer fixed output voltages of +3.3V and +5V. The MAX5024 also features an adjustable output voltage that is implemented with an external resistive-divider network connected between OUT, SET, and GND (Figure 1). The MAX5024 automatically determines the feedback path depending on the voltage at SET. Featured characteristics include reverse-voltage protection to -60V and enable and hold regulator control inputs. The *Typical Operating Circuit* shows a self-holding configuration for the MAX5023.

Functional Diagram

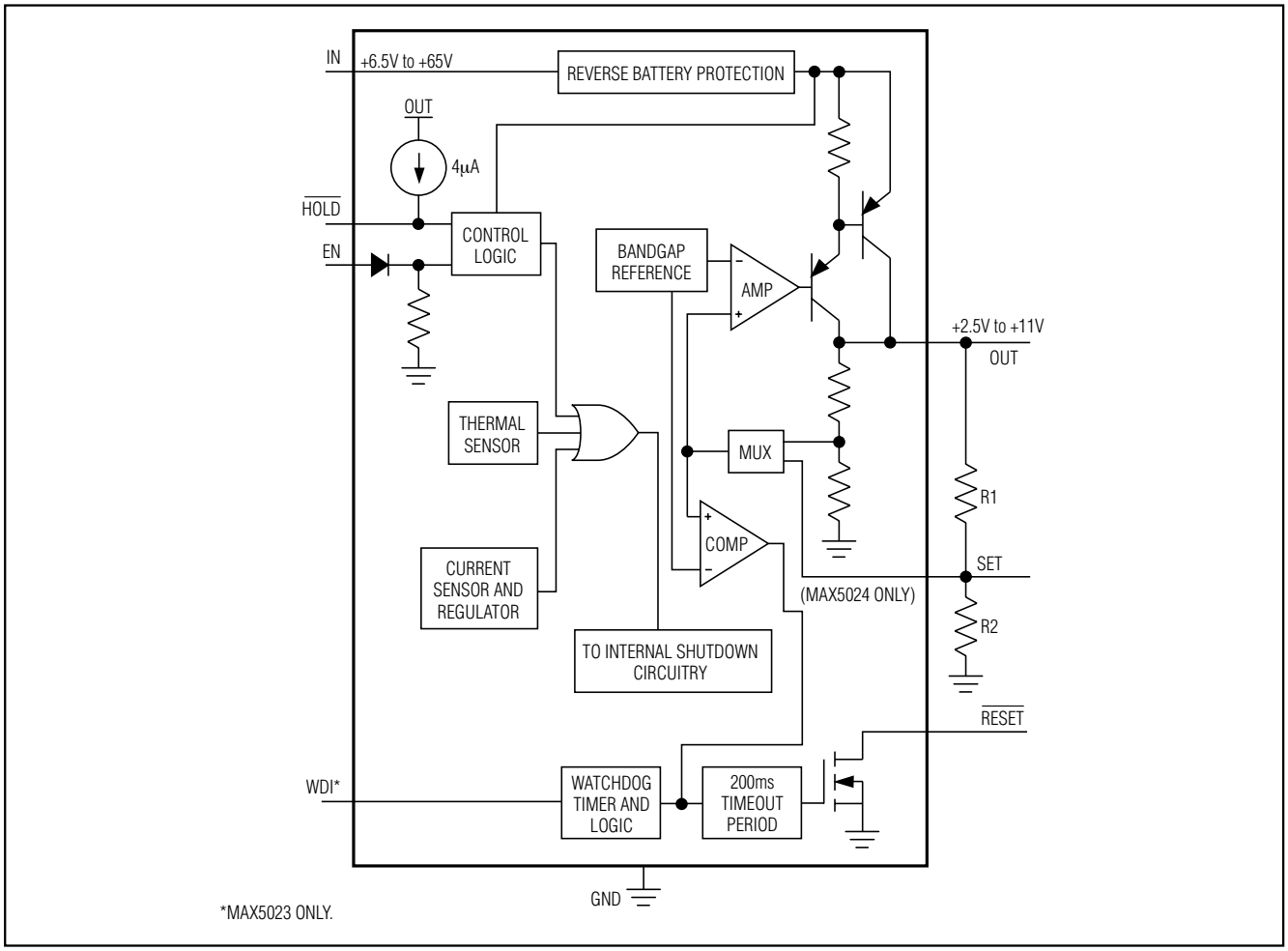


Figure 1. Functional Diagram

65V, Low-Quiescent-Current, High-Voltage Linear Regulators with µP Reset and Watchdog Timer

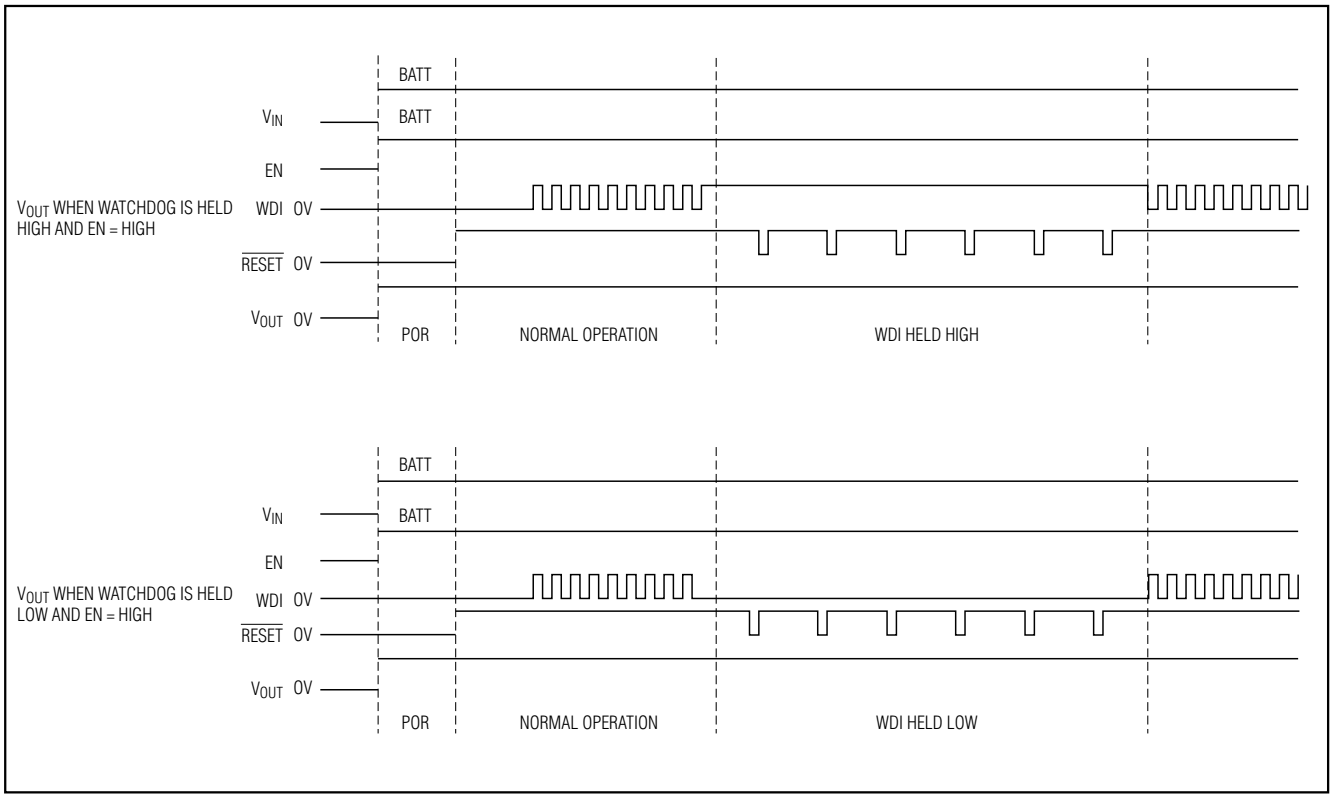


Figure 2. Watchdog Operation Timing Diagram

Reset Output

The reset supervisor circuit is fully integrated in the MAX5023/MAX5024 and uses the same reference voltage as the regulator. $\overline{RESET}$ asserts during power-up/down and brownout conditions. $\overline{RESET}$ goes low if V_{OUT} drops below the preset output voltage threshold, and remains low for 200ms (reset timeout period, t_{RP}) after V_{OUT} rises above the reset voltage threshold. For the MAX5023 only, $\overline{RESET}$ also asserts when WDI does not transition for 1.6s (watchdog timeout period, t_{WD}). Two supply tolerance reset thresholds, -7.5% and -12.5%, are available for each device type.

Watchdog Timer (MAX5023 only)

A watchdog timer asserts $\overline{RESET}$ if the watchdog input (WDI) is not toggled for 1.6s (watchdog timeout period, t_{WD}). $\overline{RESET}$ remains low for 200ms (reset timeout period, t_{RP}). If the watchdog is not updated for lengthy periods of time, the reset output appears as a pulse train, asserted for 200ms, deasserted for 1.6s, until WDI is toggled again. Once $\overline{RESET}$ asserts, it stays low for the entire reset timeout period ignoring any WDI transitions during the reset timeout period.

Figure 2 shows the Watchdog Operation Timing Diagram. To prevent the watchdog from asserting $\overline{RESET}$, toggle WDI with a valid rising or falling edge prior to t_{WD} (min) = 1.12s. The watchdog counter clears when WDI toggles prior to t_{WD} or when $\overline{RESET}$ asserts. The watchdog resumes counting after $\overline{RESET}$ deasserts.

Enable and Hold Inputs

The MAX5023/MAX5024 support two logic inputs, EN (active high) and $\overline{HOLD}$ (active low), making these devices "automotive friendly." For example, the ignition drives EN high, the regulator turns on and remains on even if EN goes low, as long as $\overline{HOLD}$ is forced low and stays low after initial regulator power-up. This feature makes it possible to implement a self-holding circuit without external components. Release $\overline{HOLD}$ (an internal current source connects HOLD to OUT) to turn the regulator off.

Force EN low and $\overline{HOLD}$ high to place the MAX5023/MAX5024 into shutdown mode. Shutdown mode draws less than 16µA of supply current. Table 1 shows the state of the regulator output with respect to the voltage level at

EN and $\overline{HOLD}$. Figure 3 shows the timing diagram for the enable and hold functions. Connecting $\overline{HOLD}$ to OUT or floating $\overline{HOLD}$ allows the EN input to act as a standard ON/$\overline{OFF}$ switch for the regulator output.

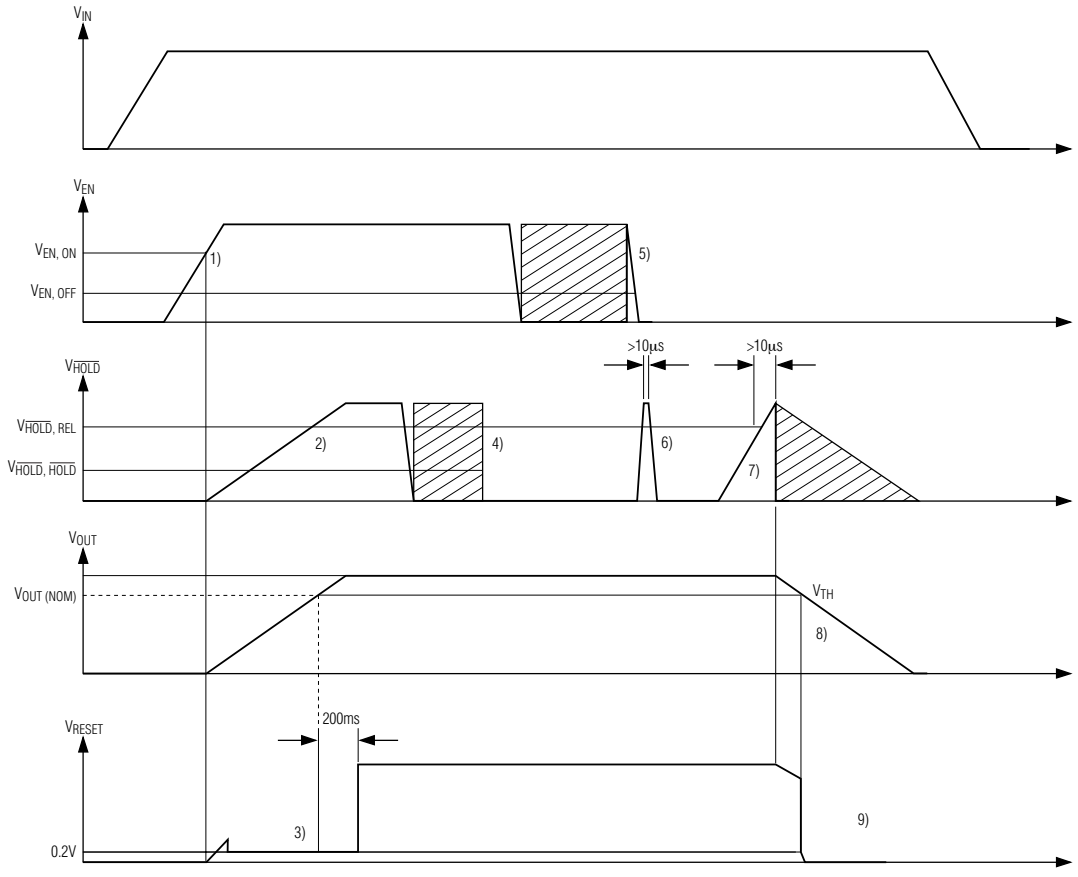
Thermal Protection

When the junction temperature exceeds T_J = +150°C, an internal thermal sensor signals the shutdown logic that turns off the pass transistor and allows the IC to cool. The thermal sensor turns the pass transistor on again after the IC's junction temperature cools by 20°C, resulting in a cycled output during continuous thermal-overload conditions. Thermal protection protects the MAX5023/MAX5024 in the event of fault conditions. For continuous operation, do not exceed the absolute maximum junction temperature rating of T_J = +150°C.

Applications Information

Output Voltage Selection (MAX5024 only)

The MAX5024 features Dual Mode™ operation: it operates in either a preset voltage mode or an adjustable mode. In preset voltage mode, internal trimmed feedback resistors set the MAX5024's internal linear regulator to +3.3V or +5V (see the *Selector Guide*).



1) ENABLE ACTIVE
2) HOLD INACTIVE, PULLED UP BY V_{OUT}
3) POWER-ON RESET
4) HOLD ACTIVE, CLAMPED TO GND BY EXTERNAL µP
5) ENABLE INACTIVE, CLAMPED BY INT, PULLDOWN RESISTOR
6) PULSE WIDTH SMALLER THAN 10µs
7) HOLD INACTIVE, RELEASED BY µP
8) REGULATOR OUTPUT SHUTDOWN
9) OUTPUT-LOW RESET

Figure 3. Enable and Hold Behavior

Dual Mode is a trademark of Maxim Integrated Products, Inc.

65V, Low-Quiescent-Current, High-Voltage Linear Regulators with µP Reset and Watchdog Timer

Table 1. Truth Table for Regulator Output State

ORDER	EN	$\overline{HOLD}$	OUT	COMMENTS
1	Low	X	Off	Initial state. EN has 5MΩ internal resistor to ground. $\overline{HOLD}$ has internal current source to OUT.
2	High	X	On	Regulator output is active when EN is pulled high.
3	High	Low	On	Hold is asserted forcing the regulator output on even if EN goes low.
4	X	Low	On	Self-holding state. Regulator output stays on regardless of the state of EN.
5	Low	High	Off	Regulator output is shutdown by releasing $\overline{HOLD}$ while EN remains low.

X = Don't care.

Select preset voltage mode by connecting SET to ground. In adjustable mode, select an output between +2.5V and +11V using two external resistors connected as a voltage-divider to SET (Figure 4). Set the output voltage using the following equation:

$$V_{OUT} = V_{SET} (1 + R1 / R2)$$

where V_{SET} = 1.248V and R2 ≅ 100kΩ.

Available Output Current Calculation

The MAX5023/MAX5024 high-voltage regulator provides up to 150mA of output current. The input voltage extends to +65V. Package power dissipation limits the amount of output current available for a given input/output voltage and ambient temperature. Figure 5 depicts the maximum power dissipation curve for these devices. The graph assumes that the exposed metal pad of the MAX5023/MAX5024 package is soldered to 1in² of PC board copper.

Use Figure 5 to determine the allowable package dissipation for a given ambient temperature. Alternately, use the following formula to calculate the allowable package dissipation:

$$P_D = \begin{cases} 1.538W \text{ for } T_A \le +70°C \\ 1.538 - 0.01923 (T_A - 70°C) \text{ for } +70°C < T_A \le +125°C \end{cases}$$

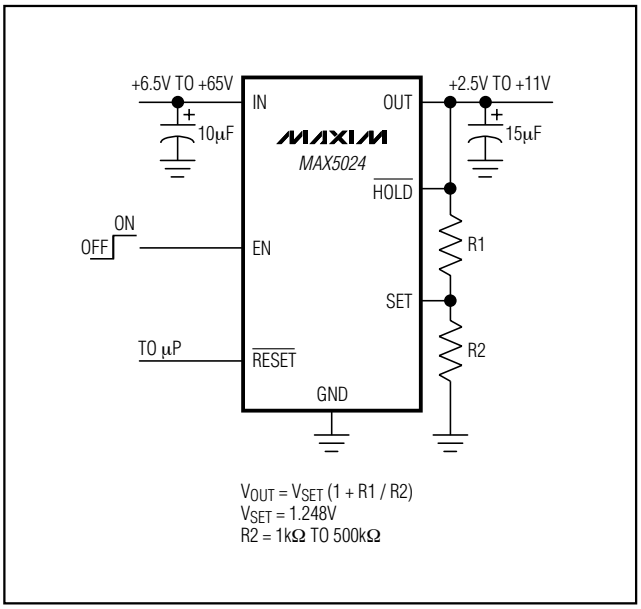


Figure 4. Setting the MAX5024 Adjustable Output Voltage

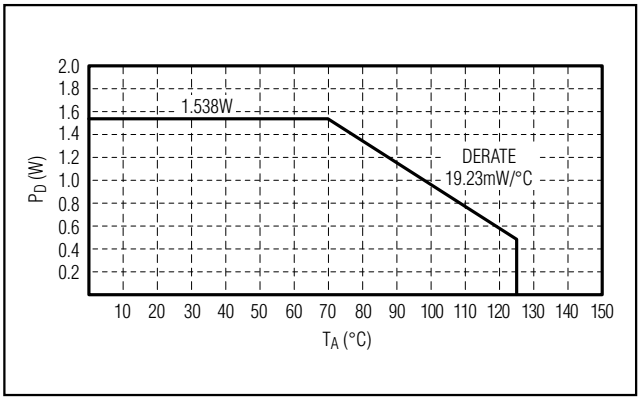


Figure 5. Maximum Power Dissipation vs. Temperature

After determining the allowable package dissipation, calculate the maximum output current using the following formula:

$$I_{OUT(MAX)} \cong \frac{P_D}{V_{IN} - V_{OUT}} \le 150mA$$

The above equations do not include the negligible power dissipation from self-heating due to the IC ground current.

Example 1:

T_A = +95°C

V_{IN} = +14V

V_{OUT} = +5V

Find the maximum allowable output current. First calculate package dissipation at the given temperature as follows:

$$P_D = 1.538W - (0.01923W/°C)(95°C - 70°C)$$
$$= 1.057W$$

Then determine the maximum output current:

$$I_{OUT(MAX)} = \frac{(1.057W)}{(14V)-(5V)} = 117.4mA$$

Example 2:

T_A = +125°C

V_{IN} = +14V

V_{OUT} = +3.3V

Calculate package dissipation at the given temperature as follows:

$$P_D = 1.538W - (0.01923W/°C)(125°C - 70°C)$$
$$= 480.4mW$$

And establish the maximum current:

$$I_{OUT(MAX)} = \frac{(480.4mW)}{(14V)-(3.3V)} = 44.89mA$$

Example 3:

T_A = +50°C

V_{IN} = +14V

V_{OUT} = +5V

Calculate package dissipation at the given temperature as follows:

$$P_D = 1.538W$$

And find the maximum output current:

$$I_{OUT(MAX)} = \frac{(1.538W)}{(14V)-(5V)} = 170.9mA \Rightarrow I_{OUT(MAX)} = 150mA$$

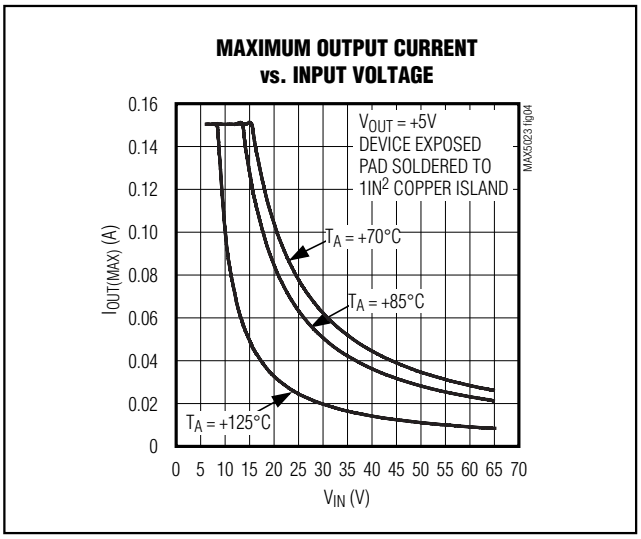


Figure 6. Maximum Output Current vs. Input Voltage

In Example 3, the maximum output current is calculated as 170.9mA, however, the maximum output current cannot exceed 150mA. Use Figure 6 to quickly determine allowable maximum output current for selected ambient temperatures.

Capacitor Selection and Regulator Stability

For stable operation over the full temperature range and with load currents up to 150mA, use a 15µF (min) output capacitor with an ESR < 0.5Ω. To reduce noise and improve load-transient response, stability, and power-supply rejection, use larger output capacitor values such as 22µF.

Some ceramic dielectrics exhibit large capacitance and ESR variation with temperature. For dielectric capacitors such as Z5U and Y5V, use 22µF or more to ensure stability at temperatures below -10°C. With X7R or X5R dielectrics, 15µF should be sufficient at all operating temperatures. For high-ESR tantalum capacitors use 22µF or more to maintain stability. To improve power-supply rejection and transient response use a minimum 10µF capacitor between IN and GND.

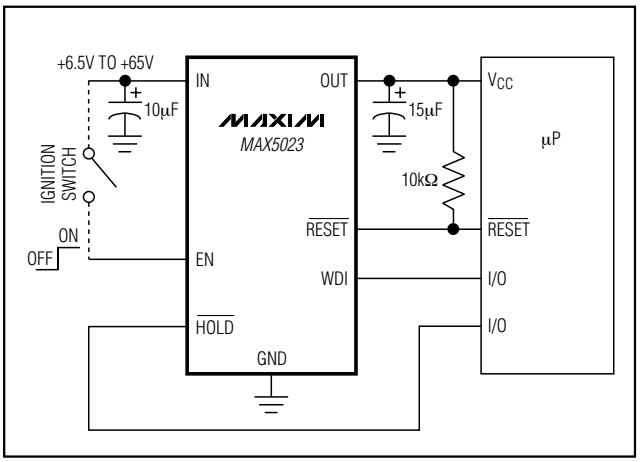
65V, Low-Quiescent-Current, High-Voltage Linear Regulators with µP Reset and Watchdog Timer

Selector Guide

PART	PRESET V_{OUT}		RESET THRESHOLD (V_{TH})				V_{OUT} RESET TOLERANCE		WATCHDOG TIMER	ADJUSTABLE REGULATOR OUTPUT
	5V	3.3V	4.63V	4.38V	3.05V	2.89V	V_{OUT} - 7.5%	V_{OUT} - 12.5%		
MAX5023L	✔	—	✔	—	—	—	✔	—	✔	—
MAX5023M	✔	—	—	✔	—	—	—	✔	✔	—
MAX5023T	—	✔	—	—	✔	—	✔	—	✔	—
MAX5023S	—	✔	—	—	—	✔	—	✔	✔	—
MAX5024L	✔	—	✔	—	—	—	✔	—	—	✔
MAX5024M	✔	—	—	✔	—	—	—	✔	—	✔
MAX5024T	—	✔	—	—	✔	—	✔	—	—	✔
MAX5024S	—	✔	—	—	—	✔	—	✔	—	✔

Bold Items indicate standard versions. Samples are generally available on standard versions only. Contact factory for availability of non-standard versions. Set MAX5024's adjustable output voltage by connecting a resistive-divider from OUT to SET (see the Output Voltage Selection section).

Typical Operating Circuit



Chip Information

TRANSISTOR COUNT: 1382

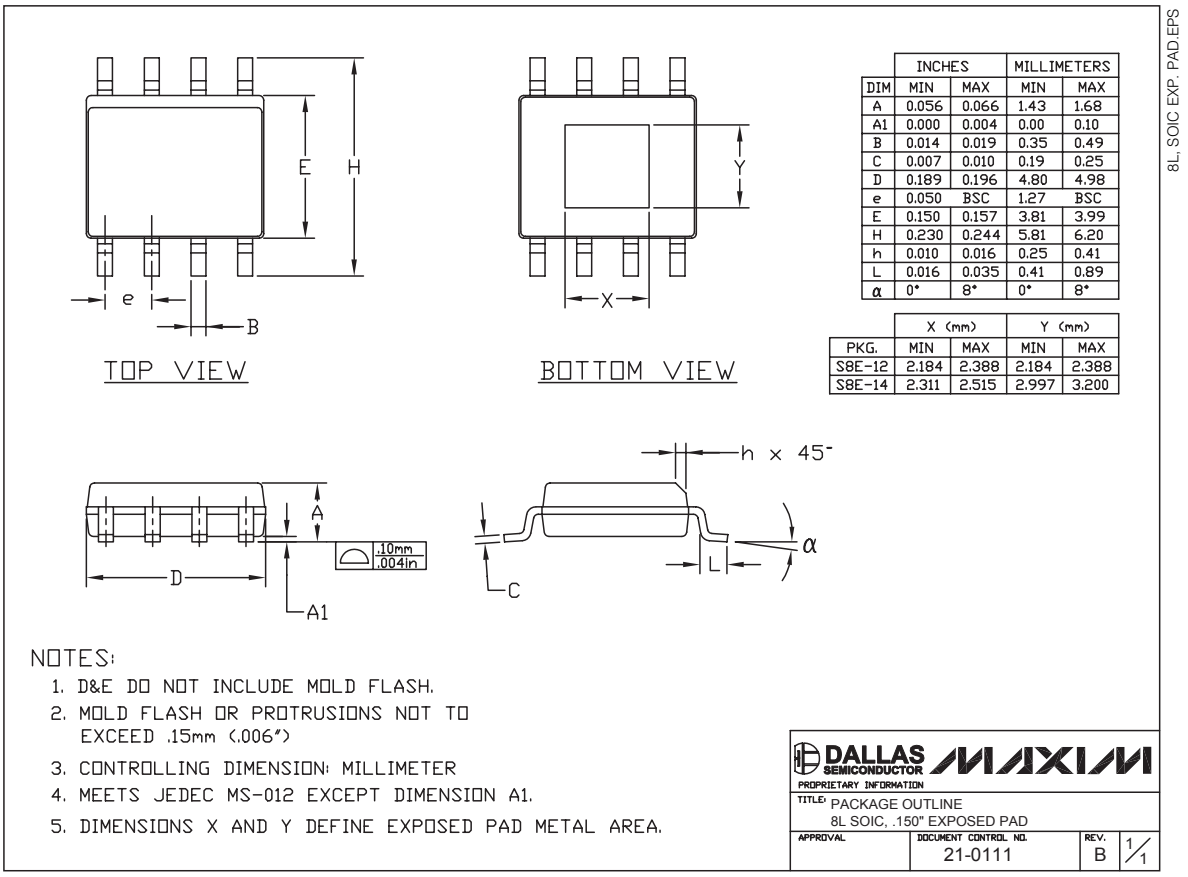
PROCESS: BiCMOS

65V, Low-Quiescent-Current, High-Voltage Linear Regulators with µP Reset and Watchdog Timer

Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to **www.maxim-ic.com/packages**.)

8L, SOIC EXP. PAD.EPS



	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
A	0.056	0.066	1.43	1.68
A1	0.000	0.004	0.00	0.10
B	0.014	0.019	0.35	0.49
C	0.007	0.010	0.19	0.25
D	0.189	0.196	4.80	4.98
e	0.050	BSC	1.27	BSC
E	0.150	0.157	3.81	3.99
H	0.230	0.244	5.81	6.20
h	0.010	0.016	0.25	0.41
L	0.016	0.035	0.41	0.89
α	0°	8°	0°	8°

	X (mm)		Y (mm)	
PKG.	MIN	MAX	MIN	MAX
S8E-12	2.184	2.388	2.184	2.388
S8E-14	2.311	2.515	2.997	3.200

NOTES:
1. D&E DO NOT INCLUDE MOLD FLASH.
2. MOLD FLASH OR PROTRUSIONS NOT TO EXCEED .15mm (.006")
3. CONTROLLING DIMENSION: MILLIMETER
4. MEETS JEDEC MS-012 EXCEPT DIMENSION A1.
5. DIMENSIONS X AND Y DEFINE EXPOSED PAD METAL AREA.

DALLAS SEMICONDUCTOR MAXIM
PROPRIETARY INFORMATION

TITLE: PACKAGE OUTLINE
8L SOIC, .150" EXPOSED PAD

APPROVAL	DOCUMENT CONTROL NO.	REV.	
	21-0111	B	1/1

Note: *For exposed pad dimension, use S8E-14 package dimensions.*