# Old Company Name in Catalogs and Other Documents

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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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# RJK0629DPE

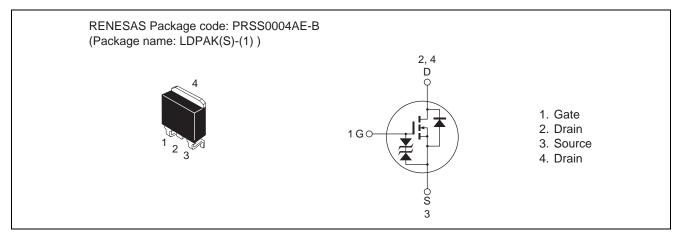
N Channel Power MOS FET High-Speed Switching Use

REJ03G1874-0100 Rev.1.00 Dec 15, 2009

### Features

- V<sub>DSS</sub>: 60 V
- $R_{DS(on)}$ : 4.5 m $\Omega$  (Max)
- I<sub>D</sub>: 85 A

### Outline



## **Absolute Maximum Ratings**

|                                  |   | $(Ta = 25^{\circ}C)$                                   |
|----------------------------------|---|--|
| Symbol                           | Value   | Unit   |
| V <sub>DSS</sub>                 | 60  | V  |
| V <sub>GSS</sub>                 | ±20   | V  |
| I <sub>D</sub>                   | 85  | A  |
| I <sub>D</sub> (pulse) Note1     | 340   | A  |
| I <sub>DR</sub>                  | 85  | A  |
| I <sub>DR</sub> (pulse) Note1    | 340   | A  |
| I <sub>AP</sub> <sup>Note2</sup> | 55  | A  |
| Pch Note3                        | 100   | W  |
| θch-c                            | 1.25  | °C/W   |
| Tch                              | 150   | °C   |
| Tstg                             | -55 to +150   | °C   |
|                                  | $V_{DSS}$ $V_{GSS}$ $I_D$ $I_D (pulse)^{Note1}$ $I_{DR} (pulse)^{Note1}$ $I_{DR} (pulse)^{Note2}$ $Pch^{Note3}$ $\theta ch-c$ $Tch$ | $\begin{array}{c c c c c c c c c c c c c c c c c c c $ |

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. Tc = 25°C, Tch  $\leq$  150°C, L = 100  $\mu H$ 

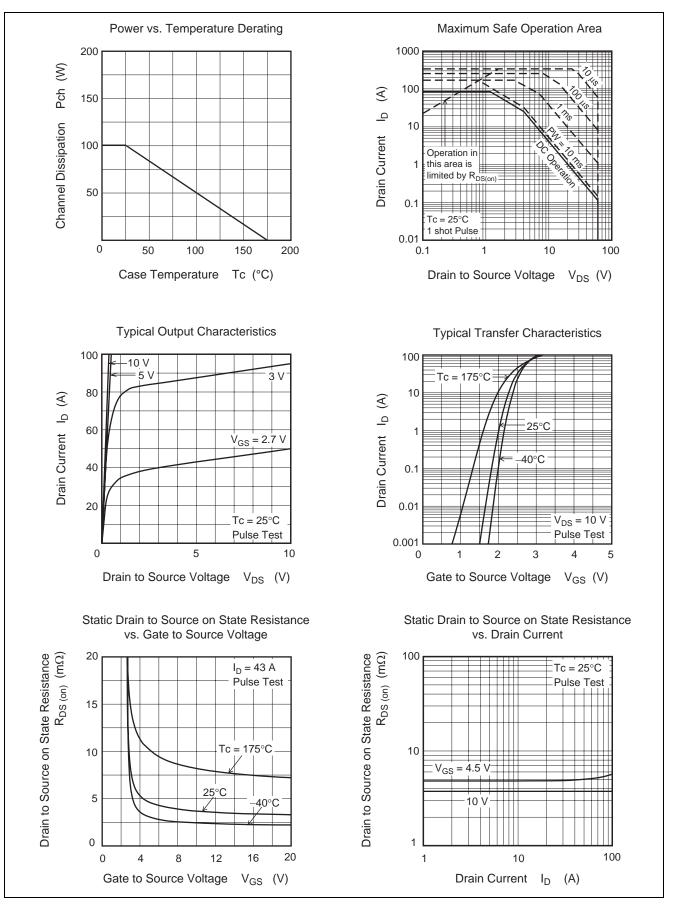
3. Value at Tc = 25°C

# **Electrical Characteristics**

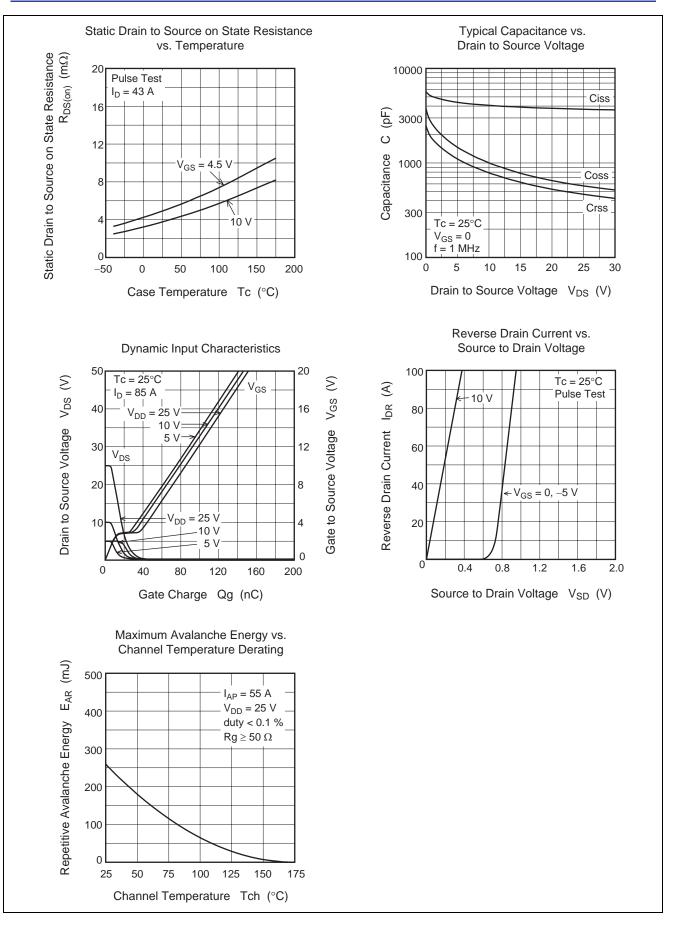
|   |                      |     |      |     |      | $(Ta = 25^{\circ}C)$   |
|---|----------------------|-----|------|-----|------|--|
| Item                                    | Symbol               | Min | Тур  | Max | Unit | Test Conditions  |
| Drain to source breakdown voltage       | V <sub>(BR)DSS</sub> | 60  | —    | —   | V    | $I_D = 100 \ \mu A, \ V_{GS} = 0$  |
| Gate to source breakdown voltage        | V <sub>(BR)GSS</sub> | ±20 | —    | _   | V    | $I_G = \pm 100 \ \mu A, \ V_{DS} = 0$  |
| Zero gate voltage drain current         | I <sub>DSS</sub>     | _   | —    | 1   | μΑ   | $V_{DS} = 60 V, V_{GS} = 0$  |
| Gate to source leak current             | I <sub>GSS</sub>     | _   | _    | ±10 | μΑ   | $V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$  |
| Gate to source cutoff voltage           | V <sub>GS(off)</sub> | 1.0 | _    | 2.0 | V    | $I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$                                      |
| Static drain to source on state voltage | V <sub>DS(on)</sub>  | _   | 161  | 194 | mV   | $I_D = 43A, V_{GS} = 10 V^{Note^4}$  |
| Static drain to source on state         | R <sub>DS(on)</sub>  | _   | 3.75 | 4.5 | mΩ   | $I_D = 43A, V_{GS} = 10 V^{Note^4}$  |
| resistance                              |                      | _   | 4.9  | 6.6 | mΩ   | $I_D = 43 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$                      |
| Input capacitance                       | Ciss                 | _   | 4100 | _   | pF   | V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0<br>f = 1 MHz                         |
| Output capacitance                      | Coss                 | _   | 1000 | _   | pF   |  |
| Reverse transfer capacitance            | Crss                 | _   | 780  | _   | pF   |  |
| Total gate charge                       | Qg                   | _   | 85   | _   | nC   | $V_{DD} = 25 \text{ V}, \text{ V}_{GS} = 10 \text{ V},$<br>I <sub>D</sub> = 85 A |
| Gate to source charge                   | Qgs                  |     | 11   |     | nC   |  |
| Gate to drain charge                    | Qgd                  |     | 25   | _   | nC   |  |
| Turn-on delay time                      | t <sub>d(on)</sub>   | _   | 20   | _   | ns   | $V_{DD} = 30V, I_D = 43A,$   |
| Rise time                               | tr                   | _   | 40   | _   | ns   | $V_{GS} = 10 \text{ V}, \text{ R}_{G} = 4.7 \Omega$                              |
| Turn-off delay time                     | t <sub>d(off)</sub>  | _   | 100  | —   | ns   |  |
| Fall time                               | t <sub>f</sub>       | _   | 40   | _   | ns   |  |
| Body-drain diode forward voltage        | V <sub>DF</sub>      | _   | 0.92 | 1.2 | V    | $I_F = 85 \text{ A}, V_{GS} = 0^{\text{Note4}}$                                  |
| Body-drain diode reverse recovery time  | t <sub>rr</sub>      |     | 50   | —   | ns   | $I_{F} = 85 \text{ A}, V_{GS} = 0, \\ di_{F}/dt = 100 \text{ A}/\mu \text{s}$    |

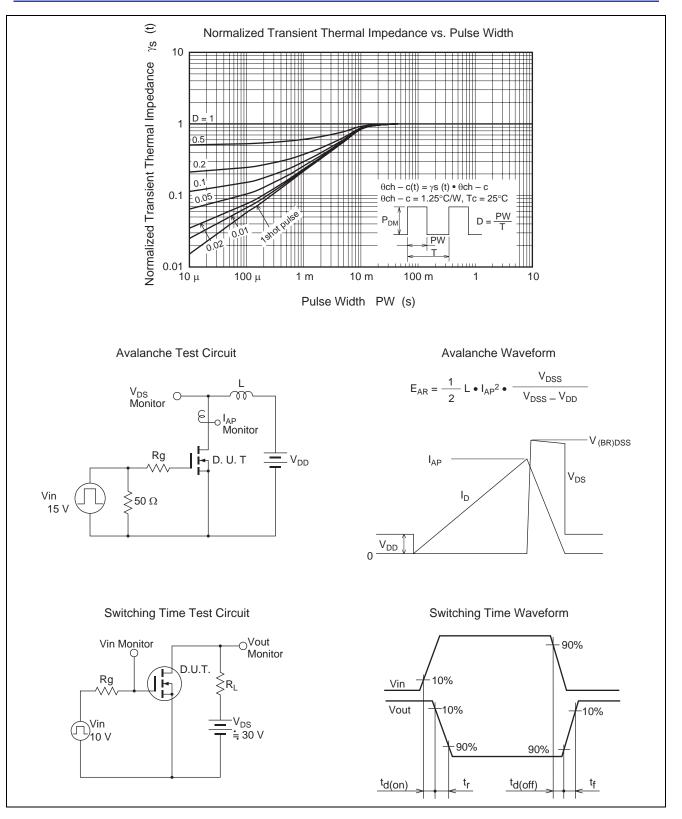
Note: 4. Pulse test

#### **Main Characteristics**

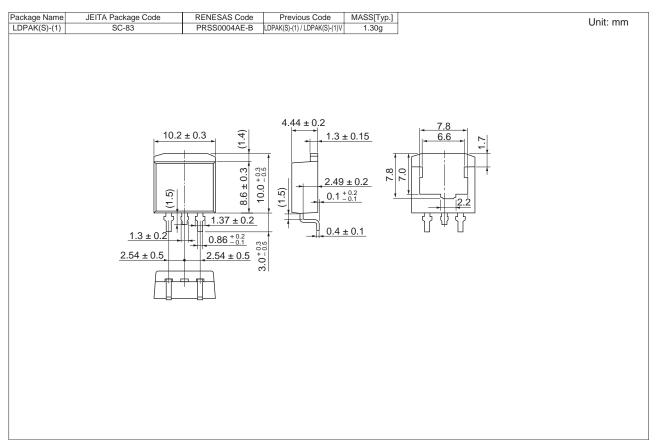


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## **Package Dimensions**



### **Ordering Information**

| Part No.         | Quantity | Shipping Container |
|------------------|----------|--------------------|
| RJK0629DPE-00-J3 | 1000 pcs | Taping             |

### RenesasTechnology Corp. sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

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#### Renesas Technology America, Inc.

450 Holger Way, San Jose, CA 95134-1368, U.S.A Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K. Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology (Shanghai) Co., Ltd. Unit 204, 205, AZIACenter, No.1233 Lujiazui Ring Rd, Pudong District, Shanghai, China 200120 Tel: <86> (21) 5877-1818, Fax: <86> (21) 6887-7858/7898

Renesas Technology Hong Kong Ltd. 7th Floor, North Tower, World Finance Centre, Harbour City, Canton Road, Tsimshatsui, Kowloon, Hong Kong Tel: <852> 2265-6688, Fax: <852> 2377-3473

Renesas Technology Taiwan Co., Ltd. 10th Floor, No.99, Fushing North Road, Taipei, Taiwan Tel: <886> (2) 2715-2888, Fax: <886> (2) 3518-3399

### Renesas Technology Singapore Pte. Ltd.

1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd. Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

Renesas Technology Malaysia Sdn. Bhd Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: <603> 7955-9390, Fax: <603> 7955-9510

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