



A. HE82005 Introduction

HE82005 is a member of 8-bit Micro-controller series developed by King Billion Electronics Ltd. It is a power speech controller. The 7-bit current-type D/A converter and PWM device provide the complete speech output mechanism. The 64K ROM Size can be used in the storage of speech (20 seconds at 3Kbytes per second). In order to simplify IC function, this IC have not exit follow list circuit, **Slow Clock, Internal Power-ON Reset, Timer2 and Watch-dog Timer**. Please pay attention. The **TP automatic increase function still works in this IC**.

The instruction set of HE82005 are quite easy to learn and simple to use. Only about thirty instructions with four-type addressing mode are provided. Most of instructions take only 3 oscillator clocks (machine cycles). The processing power is enough to most of battery operation system.

. B. HE82005 Features

- Operation Voltage : 2.4V – 5.5V
- System Clock : DC ~ 8MHz @ 5.0V
DC ~ 4MHz @ 2.4V
- Internal ROM : 64K Bytes(64K Program ROM)
- Internal RAM : 128 Bytes.
- Single Clock System : Normal (Fast) clock : 32.768K ~ 8MHz
- Operation Mode : FAST 、 SLEEP Mode.
- 8-bit Bi-directional I/O port. Mask Option can select PUSH-PULL or OPEN DRAIN output mode for each I/O pin.
- One 7-bit current-type DAC output.
- PWM device.
- Two external interrupts and one internal timer interrupt.
- One 16-bit timer. (Clock Source reference by Fast Clock)
- Instruction set : 32 instructions, 4 addressing mode. **7-bit DATA POINTER** for RAM and **16-bit TABLE POINTER** for ROM.

C. Internal Block

I.F.C.	E.S.C.	I.P.R.	PROM	DROM	TP	TP+1	RAM	PP	DP	I/O	DTMF	WDT	Timer
—	—	—	64KB	—	16-bit	—	128B	—	7-bit	8	—	—	T1
VO	DAO	OP	PWM	LCD	COM*SEG	Bias	Rgr	ChrgPmp	LV2	LR	LVG	REC	S.R.
⊙	—	—	⊙	—	—	—	—	—	—	—	—	—	—



D. HE82005 Pin Description

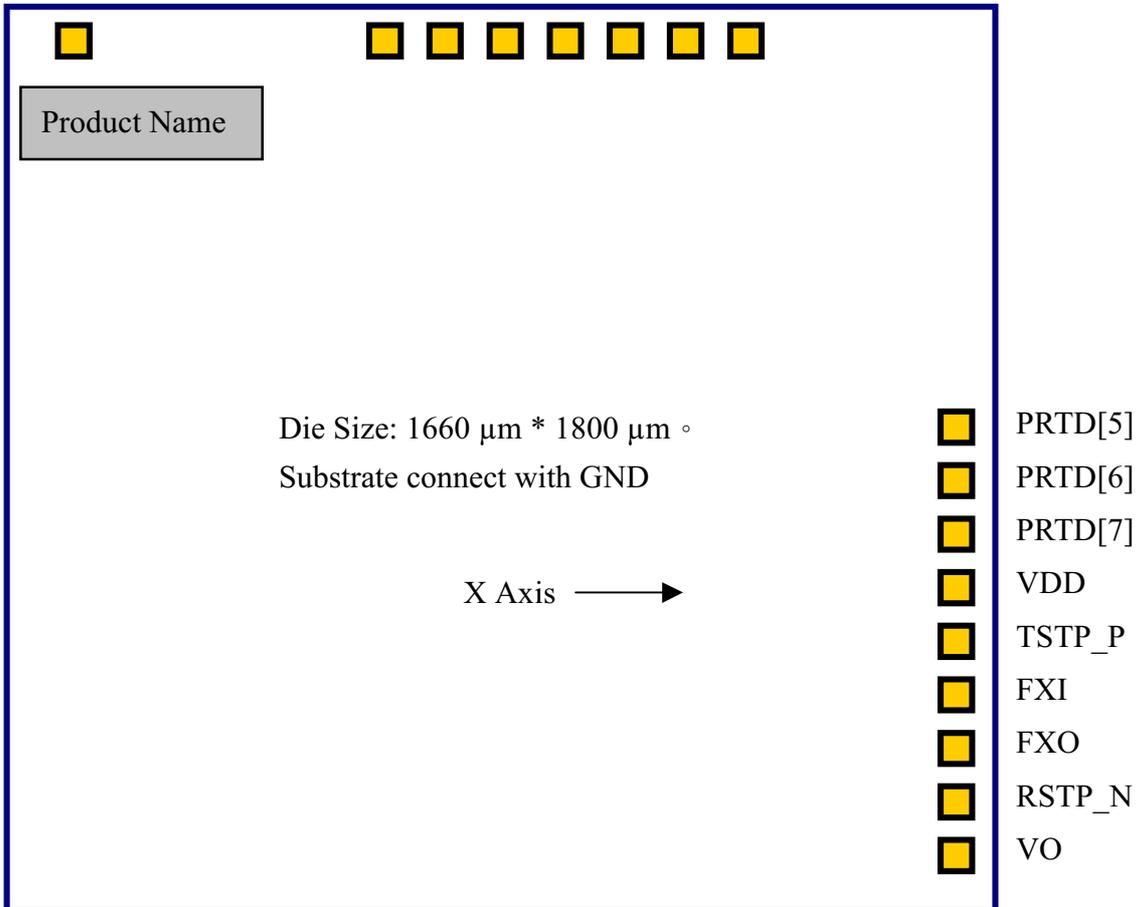
Pin #	Pin name	I/O	Function	Description
4 3	FXI, FXO	B, O	External fast clock pin. Connecting to crystal or RC to generate 32.768 kHz ~ 8MHz frequency.	Mask option setting : MO_FXTAL=0 : R,C oscillator for Fast Clock 1 : Crystal oscillator for Fast Clock User can use OP1 and OP2 to change operation mode between Normal and Sleep.
2	RSTP_N	I	System Reset.	Level trigger, active low. For let system reset.
5	TSTP_P	I	Test Pin	Please bond this pin and add a test point on PCB for debugging. Leave this pin floating is OK
7..14	PRTD[7:0]	B	8-pin bi-directional I/O port. PRTD[7..2] as wake-up pin. PRTD[7..6] as external interrupt pin.	Mask options : MO_DPP[7..0]=1 ~ Push-pull. =0 ~ Open-drain. Output must be "1" before reading whenever use them as input (No tri-state structure).
16	PWM	O	The PWM output can drive speaker or buzzer directly.	
17	GND_PWM	P	Dedicated GND for PWM	Set the bit2 of VOC register as one to turn on PWM.
1	VO	O	DAC Voice Output	Set the bit1(DA=1) of VOC register to turn on DAC with VO output.
6	VDD	P	Positive Power Input	
15	GND	P	Power Ground Input	

E. Pin Diagram

Which version of IC decided by KB when customer gives order to KB. Please make sure which version will be used before you make a PCB!

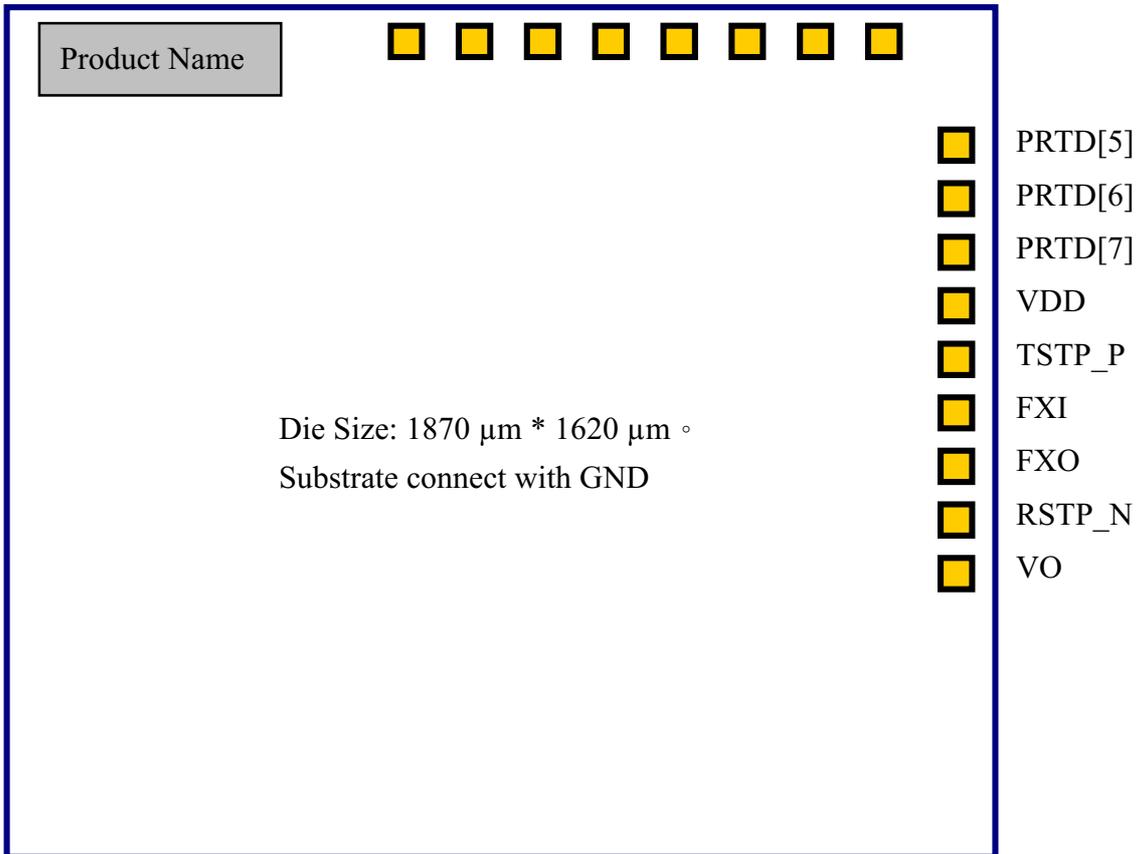
Version A1:

G						
N						
D			P	P	P	P
-			R	R	R	R
P	P	G	T	T	T	T
W	W	N	D	D	D	D
M	M	D	[0]	[1]	[2]	[3]



Version B1(Older):

			P	P	P	P	P
P	P		R	R	R	R	R
W	W	G	T	T	T	T	T
M	M	N	D	D	D	D	D
N	P	D	[0]	[1]	[2]	[3]	[4]





F. Bonding Pad Location

Version A1:

PIN Number	PIN Name	X Coordinate	Y Coordinate
1	VO	X= 719.50	Y= -826.85
2	RSTP_N	X= 719.50	Y= -710.95
3	FXO	X= 719.50	Y= -595.05
4	FXI	X= 719.50	Y= -479.15
5	TSTP_P	X= 719.50	Y= -363.25
6	VDD	X= 719.50	Y= -247.35
7	PRTD[7]	X= 719.50	Y= -131.45
8	PRTD[6]	X= 719.50	Y= -15.55
9	PRTD[5]	X= 719.50	Y= 100.35
10	PRTD[4]	X= 417.50	Y= 805.50
11	PRTD[3]	X= 301.60	Y= 805.50
12	PRTD[2]	X= 185.70	Y= 805.50
13	PRTD[1]	X= 69.80	Y= 805.50
14	PRTD[0]	X= -46.10	Y= 805.50
15	GND	X= -162.00	Y= 805.50
16	PWM	X= -299.35	Y= 805.50
17	GND_PWM	X= -763.25	Y= 805.50

Version B1(Older):

PIN Number	PIN Name	X Coordinate	Y Coordinate
1	VO	X= 856.70	Y= -515.25
2	RSTP_N	X= 856.70	Y= -399.75
3	FXO	X= 856.70	Y= -284.25
4	FXI	X= 856.70	Y= -168.75
5	TSTP_P	X= 856.70	Y= -53.25
6	VDD	X= 856.70	Y= 62.25
7	PRTD[7]	X= 856.70	Y= 177.75
8	PRTD[6]	X= 856.70	Y= 293.25
9	PRTD[5]	X= 856.70	Y= 408.75
10	PRTD[4]	X= 734.75	Y= 727.60
11	PRTD[3]	X= 619.25	Y= 727.60
12	PRTD[2]	X= 503.75	Y= 727.60
13	PRTD[1]	X= 388.25	Y= 727.60
14	PRTD[0]	X= 272.75	Y= 727.60



15	GND	X= 157.25	Y= 727.60
16	PWMP	X= 17.50	Y= 727.60
17	PWMN	X= -145.85	Y= 727.60

G. DC/AC Characteristics

Absolute Maximum Rating

Item	Sym.	Rating	Condition
Supply Voltage	V_{dd}	-0.5V ~ 8V	
Input Voltage	V_{in}	-0.5V ~ $V_{dd}+0.5V$	
Output Voltage	V_o	-0.5V ~ $V_{dd}+0.5V$	
Operating Temperature	T_{op}	0°C ~ 70°C	
Storage Temperature	T_{st}	-50°C ~ 100°C	

Recommended Operating Conditions

Item	Sym.	Rating	Condition
Supply Voltage	V_{dd}	2.4V ~ 5.5V	
Input Voltage	V_{ih}	0.9 V_{dd} ~ V_{dd}	
	V_{il}	0.0V ~ 0.1 V_{dd}	
Operating Frequency	F_{max}	8MHz	$V_{dd}=5.0V$
		4MHz	$V_{dd}=2.4V$
Operating Temperature	T_{op}	0°C ~ 70°C	
Storage Temperature	T_{st}	-50°C ~ 100°C	



測試條件:TEMP=25°C, VDD=3V+/-10%, GND=0V

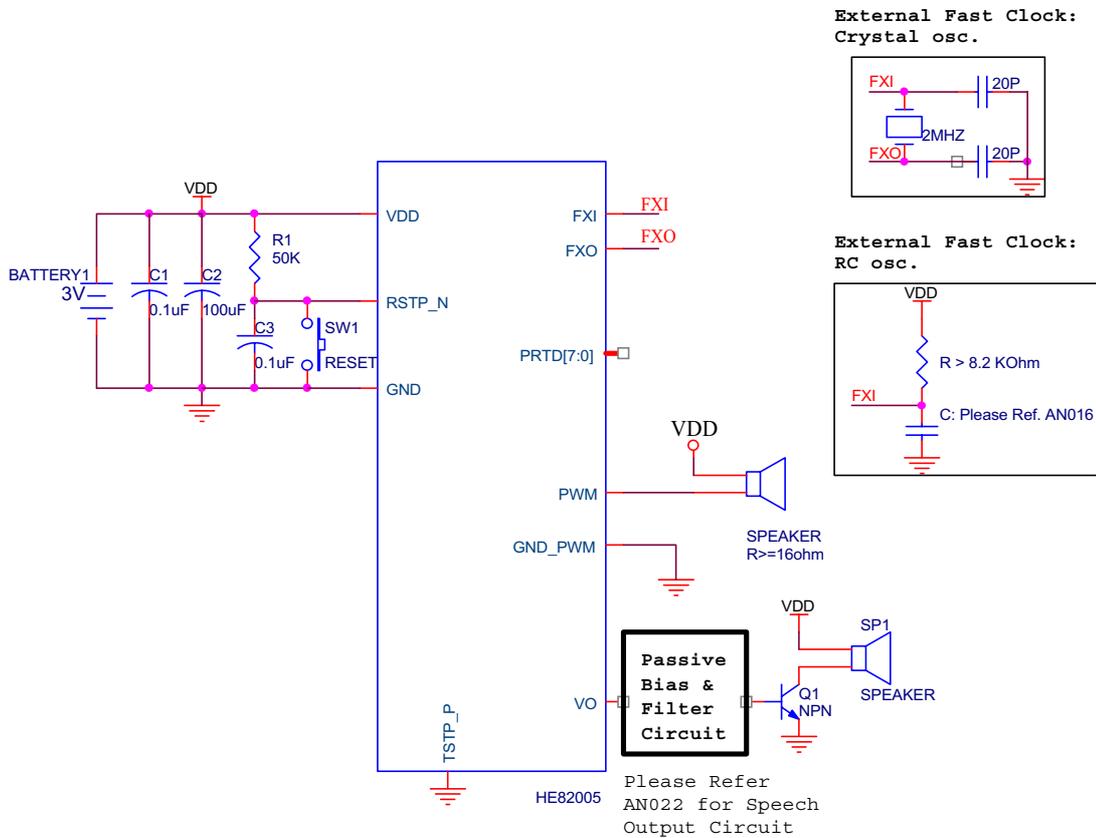
	PARAMETER		CONDITION	MIN	TYP	MAX	UNIT
I_{Fast}	NORMAL Mode Current	System	2M ext. R/C		0.75	1	mA
I_{Sleep}	Sleep Mode current	System				1	μA
I_{PWM}	PWM Output Current	PWM	With 32Ω Loading	10	14		mA
			With 64Ω Loading	6	8		mA
			With 100Ω Loading	4	5		mA
I_{VO}	DAC Output Current	VO	V _{DD} =3V;VO=0~2V,Data=7F	2.5	3		mA
V_{IH}	Input High Voltage	I/O pins		0.8 V _{DD}			V
V_{IL}	Input Low Voltage	I/O pins				0.2 V _{DD}	V
V_{hys}	Input Hysteresis Width	I/O, RSTP_N	Threshold=2/3V _{DD} (input from low to high) Threshold=1/3V _{DD} (input from high to low)		1/3 V _{DD}		V
I_{OH}	Output Drive Current	I/O pull-high*1	V _{oL} =2.0V	50			μA
I_{oL_1}	Output Sink Current	I/O pull-low*1	V _{oL} =0.4V	1.0			mA
I_{iL_1}	Input Low Current	RSTP_N	V _{iL} =GND, pull high Internally		20		μA
I_{iL_2}	Input Low Current	I/O	V _{iL} =GND, if pull high Internally by user		100		μA

Note: *1: Drive Current Spec. for Push-Pull I/O port only

Sink Current Spec. for both Push-Pull and Open-Drain I/O port.

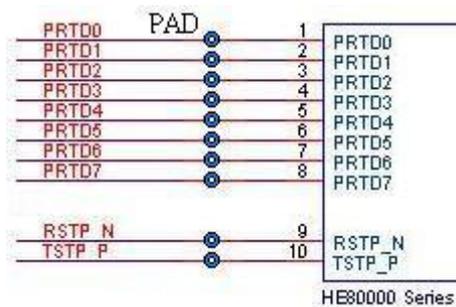
*2: This Spec. base on one driver only. There are five build-in driver, so user just multiply the number of driver he used to one driver current to get the total amount of current.
 (I_{PWM} * N; N=0,1,2,3,4,5)

H. Application Circuit



I. Important Note

1. Please bonds the TSTP_P, RSTP_N and PRTD[7:0] with test point on PCB (can be soldered and probed) as you can, then KB can do some IC testing job on PCB. Neither VDD nor GND connection is necessary for TSTP_P. The following figure is an example (Testing point with through hole).



J. Updated Record

Version	Date	Section	Original Content	New Content
V3.5	Nov 20, 2001	B, G I, J	2.2V (VDD operation voltage)	2.4V New Section