Unit: mm

TOSHIBA Field Effect Transistor Silicon P-Channel MOS Type ( $\pi$ -MOS V)

# **2SJ676**

# Switching Regulator, DC/DC Converter and Motor Drive Applications

• Low drain-source ON-resistance:  $R_{DS (ON)} = 1.6 \Omega (typ.)$ 

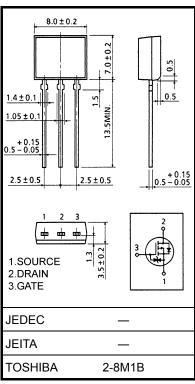
• High forward transfer admittance:  $|Y_{fS}| = 2.0 \text{ S (typ.)}$ 

• Low leakage current:  $I_{DSS} = -100 \mu A \text{ (max) (V}_{DS} = -200 \text{ V)}$ 

• Enhancement mode:  $V_{th} = -1.5$  to -3.5 V ( $V_{DS} = -10$  V,  $I_D = -1$  mA)

## Absolute Maximum Ratings (Ta = 25°C)

Characteri	stic	Symbol	Rating	Unit
Drain-source voltage		$V_{DSS}$	-200	٧
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)		$V_{DGR}$	-200	٧
Gate-source voltage		$V_{GSS}$	±20	٧
Drain current	DC (Note 1)	ΙD	-2.5	Α
	Pulse(Note 1)	I <sub>DP</sub>	-10	Α
Drain power dissipation	١	$P_{D}$	1.3	W
Single-pulse avalanche energy (Note 2)		E <sub>AS</sub>	191	mJ
Avalanche current		I <sub>AR</sub>	-2.5	А
Repetitive avalanche e	nergy (Note 3)	E <sub>AR</sub>	0.13	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature ra	ange	T <sub>stg</sub>	-55~150	°C



Weight: 0.54 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

### **Thermal Characteristics**

Characteristic	Symbol	Max	Unit	
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	96.1	°C/W	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = -50 V,  $T_{ch}$  = 25°C (initial), L = 48.6 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = -2.5 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.

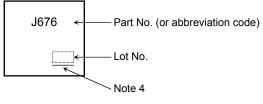
# **Electrical Characteristics (Ta = 25°C)**

Chara	cteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V	_	_	±10	μΑ
Drain cutoff curr	ent	I <sub>DSS</sub>	V <sub>DS</sub> = -200 V, V <sub>GS</sub> = 0 V	_	_	-100	μΑ
Drain-source bi	eakdown voltage	V (BR) DSS	I <sub>D</sub> = -10 mA, V <sub>GS</sub> = 0 V	-200	_	_	V
Gate threshold	voltage	V <sub>th</sub>	V <sub>DS</sub> = -10 V, I <sub>D</sub> = -1 mA	-1.5	_	-3.5	V
Drain-source O	N-resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = -10 V, I <sub>D</sub> = -1.5 A	_	1.6	2.0	Ω
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = -10 V, I <sub>D</sub> = -1.5 A	1.0	2.0	_	S
Input capacitano	ce	C <sub>iss</sub>		_	410	_	
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	40	_	pF
Output capacita	Output capacitance			_	145	_	
Switching time	Rise time	t <sub>r</sub>	$V_{GS}$ O $V_{DD} = -1.5 \text{ A}$ O $O$ Output $R_L = 66.7 \Omega$ $V_{DD} \approx -100 \text{ V}$ Duty $\leq$ 1%, $t_W = 10 \mu \text{s}$	_	20	_	
	Turn-on time	t <sub>on</sub>		_	45	_	ns
	Fall time	t <sub>f</sub>		_	15	_	115
	Turn-off time	t <sub>off</sub>		_	85	1	
Total gate charge (gate-source plus gate-drain)		Qg			10	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx -160 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -2.5 \text{ A}$	_	6	_	nC
Gate-drain ("Miller") charge		$Q_{gd}$		_	4	_	

# Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	-2.5	Α
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_	_	-10	Α
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = -2.5 A, V <sub>GS</sub> = 0 V	_	_	2.0	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = -2.5 A, V <sub>GS</sub> = 0 V	-	135	_	ns
Reverse recovery charge	Qrr	dl <sub>DR</sub> / dt = 100 A / μs		0.81	_	μС

# Marking



Note 4: A line under a Lot No. identifies the indication of product Labels.

Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

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