Complex Midium Power Transistors (±50V/±1A)

QS6Z5

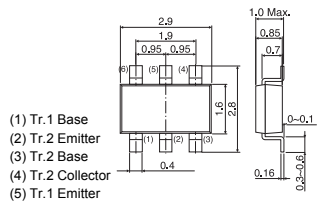
● Structure
NPN/PNP Silicon epitaxial planar transistor

● Features
1) Low saturation voltage
$V_{CE\ (sat)}$ = 0.35V (Max.) (I_C / I_B= 500mA / 25mA)
$V_{CE\ (sat)}$ = −0.40V (Max.) (I_C / I_B= −500mA / −25mA)
2) High speed switching

● Applications
Low Frequency Amplifier
Driver

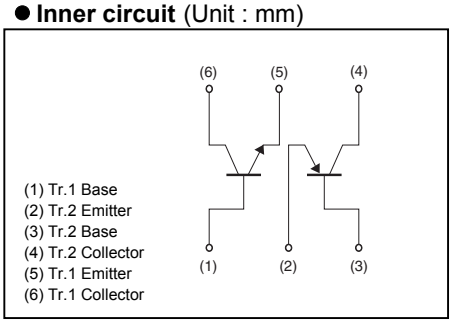
● Dimensions (Unit : mm)

TSMT6



Abbreviated symbol : Z05

● Inner circuit (Unit : mm)



● Packaging specifications

Type	Package	TSMT6
	Code	TR
	Basic ordering unit (pieces)	3000

● Absolute maximum ratings (Ta = 25°C)

<Tr.1>

Parameter		Symbol	Limits	Unit
Collector-base voltage		V_{CBO}	50	V
Collector-emitter voltage		V_{CEO}	50	V
Emitter-base voltage		V_{EBO}	6	V
Collector current	DC	I_C	1	A
	Pulsed	I_{CP} *1	2	A

<Tr.2>

Parameter		Symbol	Limits	Unit
Collector-base voltage		V_{CBO}	−50	V
Collector-emitter voltage		V_{CEO}	−50	V
Emitter-base voltage		V_{EBO}	−6	V
Collector current	DC	I_C	−1	A
	Pulsed	I_{CP} *1	−2	A

<Tr.1 and Tr.2>

Parameter	Symbol	Limits	Unit
Power dissipation	P_D *2	0.5	W/Total
	P_D *3	1.25	W/Total
	P_D *3	0.9	W/Element
Junction temperature	T_j	150	°C
Range of storage temperature	T_{stg}	−55 to +150	°C

*1 Pw=10ms, Single Pulse

*2 Mounted on a recommended land.

*3 Mounted on a 40 x 40 x 0.7[mm] ceramic board.

●**Electrical characteristics** (Ta = 25°C)

<Tr.1>

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Collector-emitter breakdown voltage	BV_{CEO}	50	-	-	V	I_C= 1mA
Collector-base breakdown voltage	BV_{CBO}	50	-	-	V	I_C= 100μA
Emitter-base breakdown voltage	BV_{EBO}	6	-	-	V	I_E= 100μA
Collector cut-off current	I_{CBO}	-	-	1	μA	V_{CB}= 50V
Emitter cut-off current	I_{EBO}	-	-	1	μA	V_{EB}= 4V
Collector-emitter staturation voltage	$V_{CE(sat)}$ *1	-	130	350	mV	I_C=500mA, I_B=25mA
DC current gain	h_{FE}	180	-	450	-	V_{CE}= 2V, I_C= 50mA
Transition frequency	f_T *1	-	360	-	MHz	V_{CE}= 10V I_E=−200mA, f=100MHz
Collector output capacitance	C_{ob}	-	7	-	pF	V_{CB}= 10V, I_E=0A f=1MHz
Turn-on time	t_{on} *2	-	40	-	ns	I_C= 0.5A, I_{B1}= 50mA, I_{B2}=−50mA, V_{CC} ≃10V
Storage time	t_{stg} *2	-	410	-	ns	
Fall time	t_f *2	-	75	-	ns	

*1 Pulsed

*2 See switching time test circuit

<Tr.2>

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Collector-emitter breakdown voltage	BV_{CEO}	−50	-	-	V	I_C= −1mA
Collector-base breakdown voltage	BV_{CBO}	−50	-	-	V	I_C= −100μA
Emitter-base breakdown voltage	BV_{EBO}	−6	-	-	V	I_E= −100μA
Collector cut-off current	I_{CBO}	-	-	−1	μA	V_{CB}= −50V
Emitter cut-off current	I_{EBO}	-	-	−1	μA	V_{EB}= −4V
Collector-emitter staturation voltage	$V_{CE(sat)}$ *1	-	−200	−400	mV	I_C= −500mA, I_B= −25mA
DC current gain	h_{FE}	180	-	450	-	V_{CE}= −2V, I_C= −50mA
Transition frequency	f_T *1	-	400	-	MHz	V_{CE}= −10V I_E=200mA, f=100MHz
Collector output capacitance	C_{ob}	-	12	-	pF	V_{CB}= −10V, I_E=0A f=1MHz
Turn-on time	t_{on} *2	-	40	-	ns	I_C= −0.5A, I_{B1}= −50mA, I_{B2}= 50mA, V_{CC} ≃ −10V
Storage time	t_{stg} *2	-	250	-	ns	
Fall time	t_f *2	-	35	-	ns	

*1 Pulsed

*2 See switching time test circuit

●Electrical characteristic curves (Ta=25°C)

〈Tr.1〉

Fig.1 Ground Emitter Propagation Characteristics

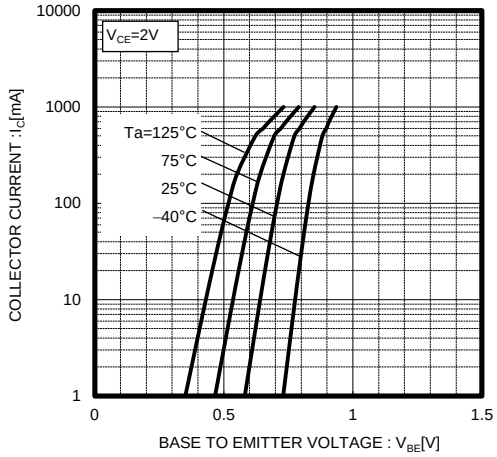


Fig.2 Typical OutputCharacteristics

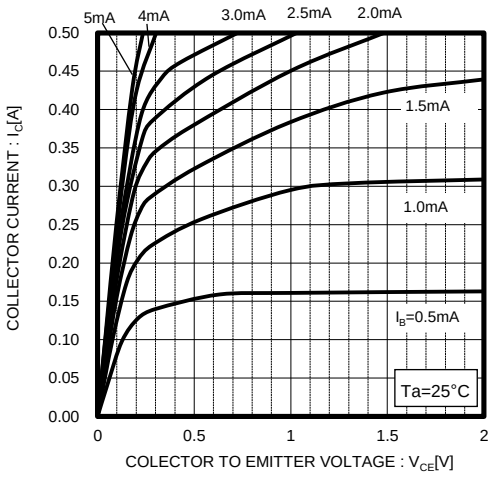


Fig.3 DC Current Gain vs. Collector Current (I)

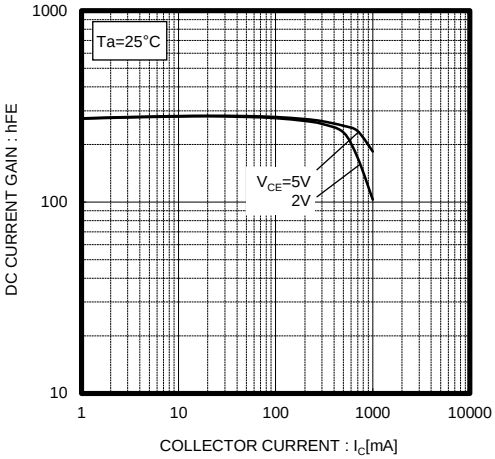


Fig.4 DC Current Gain vs.Collector Current (II)

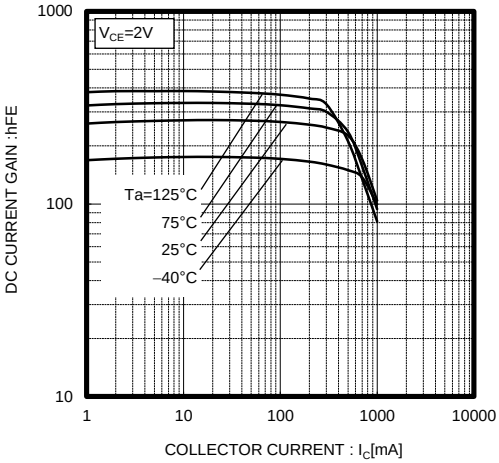


Fig.5 Collector-Emitter Saturation Voltage vs. Collector Current (I)

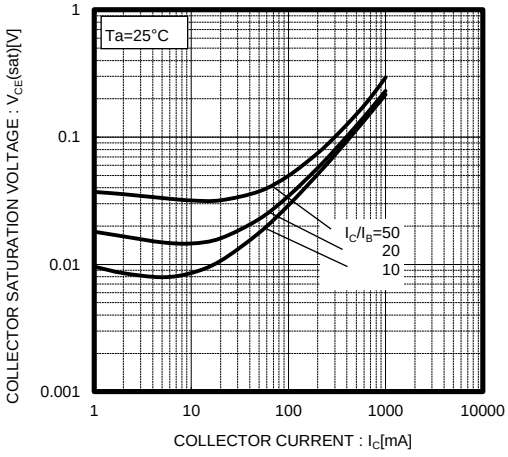


Fig.6 Collector-Emitter Saturation Voltage vs. Collector Current(II)

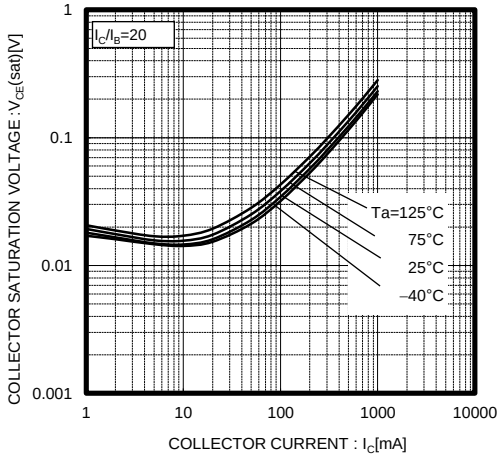


Fig.7 Emitter input capacitance vs. Emitter-Base Voltage
Collector output capacitance vs. Collector-Base

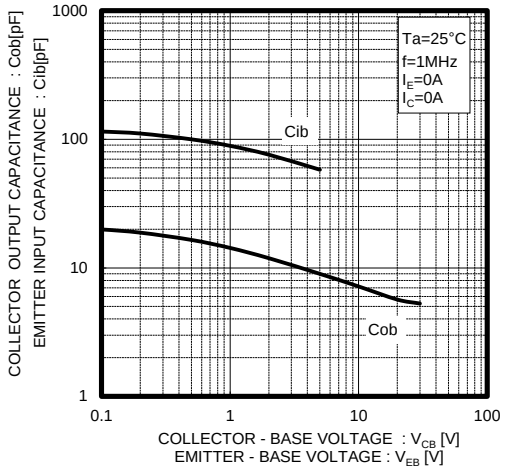


Fig.8 Gain BandwidthProduct vs. Emitter

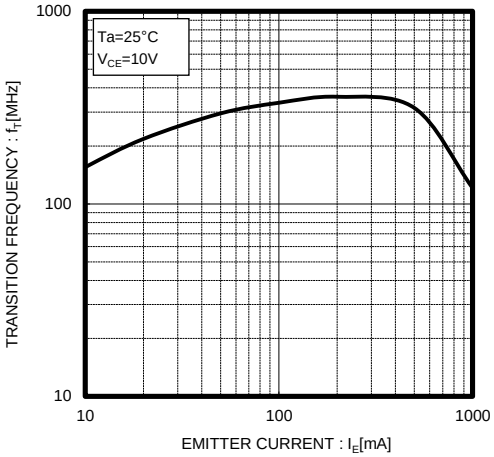
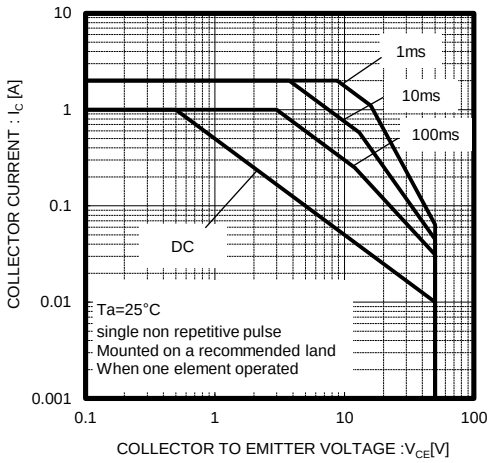


Fig.9 Safe Operating Area



〈Tr.2〉

Fig.1 Ground Emitter Propagation Characteristics

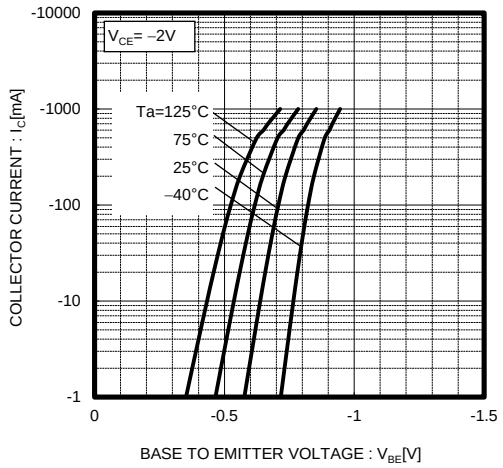


Fig.2 Typical Output Characteristics

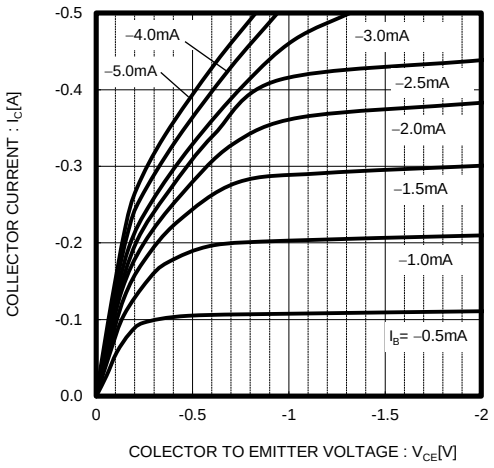


Fig.3 DC Current Gain vs. Collector Current (I)

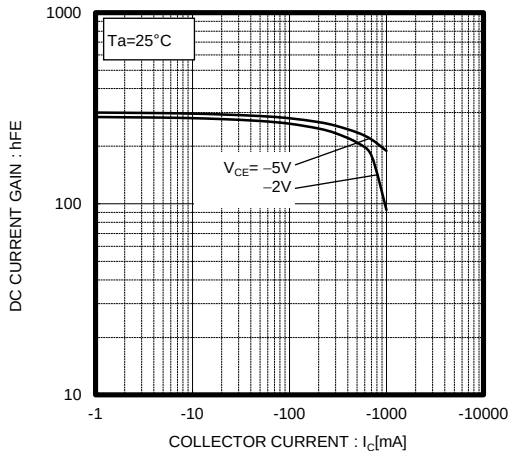


Fig.4 DC Current Gain vs. Collector Current (II)

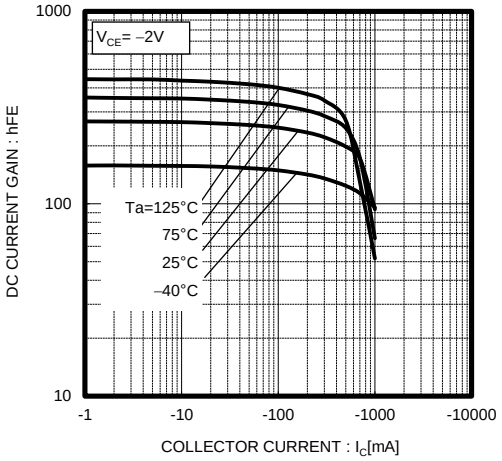


Fig.5 Collector-Emitter Saturation Voltage vs. Collector Current (I)

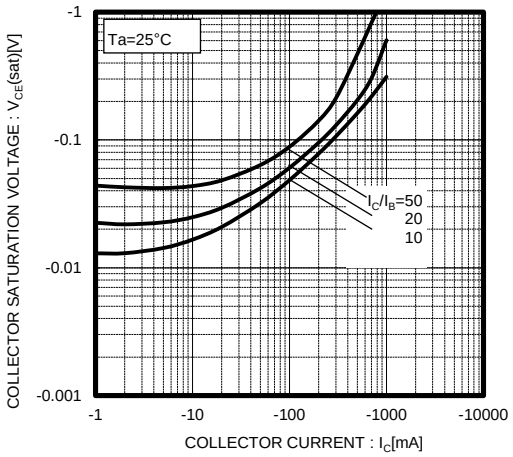


Fig.6 Collector-Emitter Saturation Voltage vs. Collector Current (II)

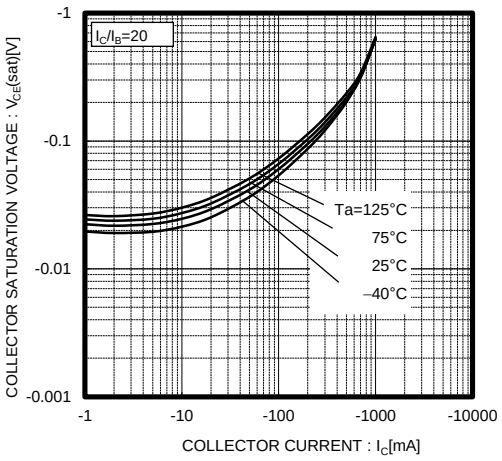


Fig.7 Emitter Input Capacitance vs. Emitter-Base Voltage
Collector Output Capacitance vs.Collector-Base Voltage

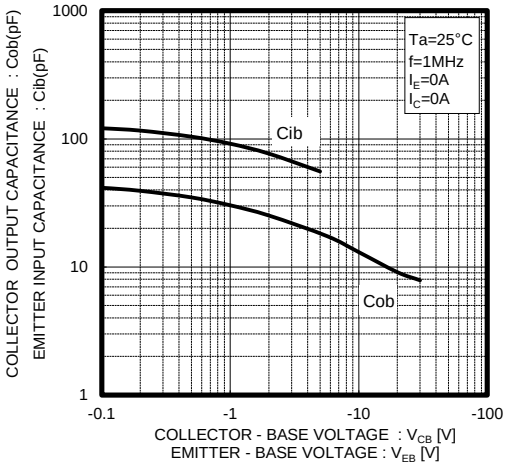


Fig.8 Gain Bandwidth Product vs. Emitter

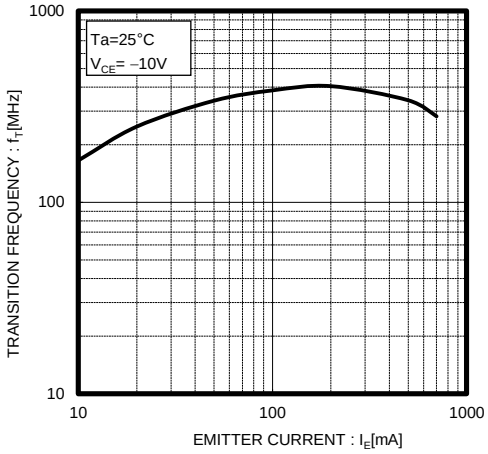
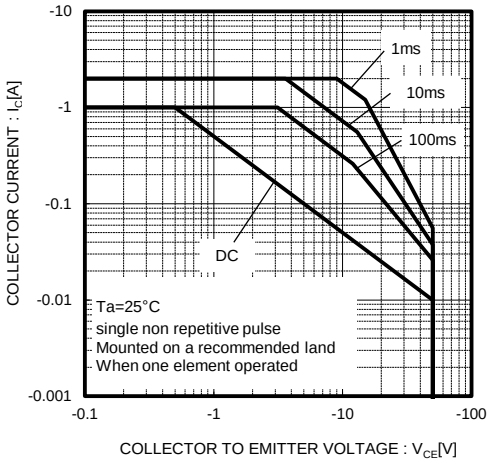
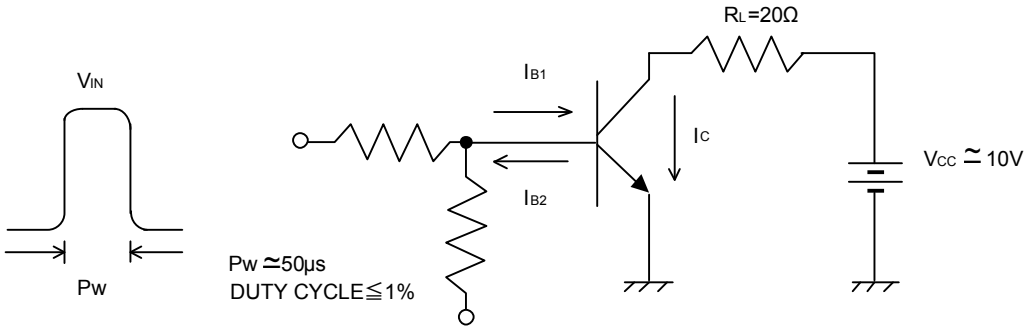


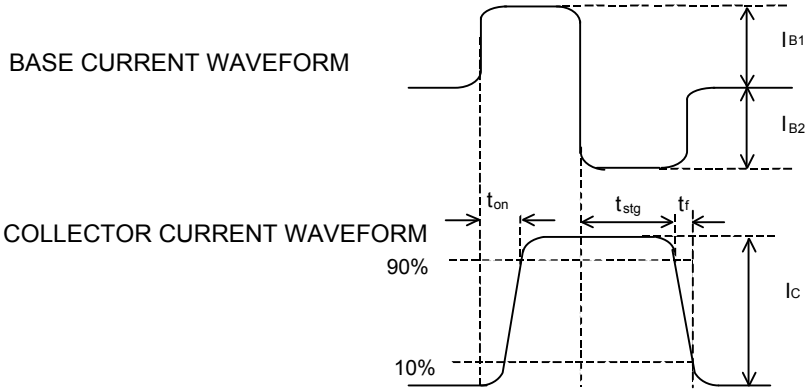
Fig.9 Safe Operating Area



● **Switching time test circuit**

<Tr.1>





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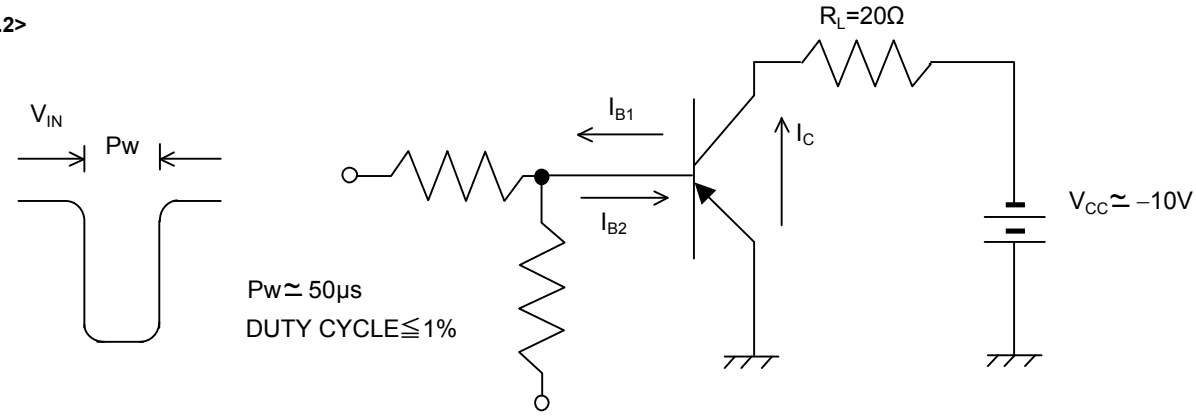

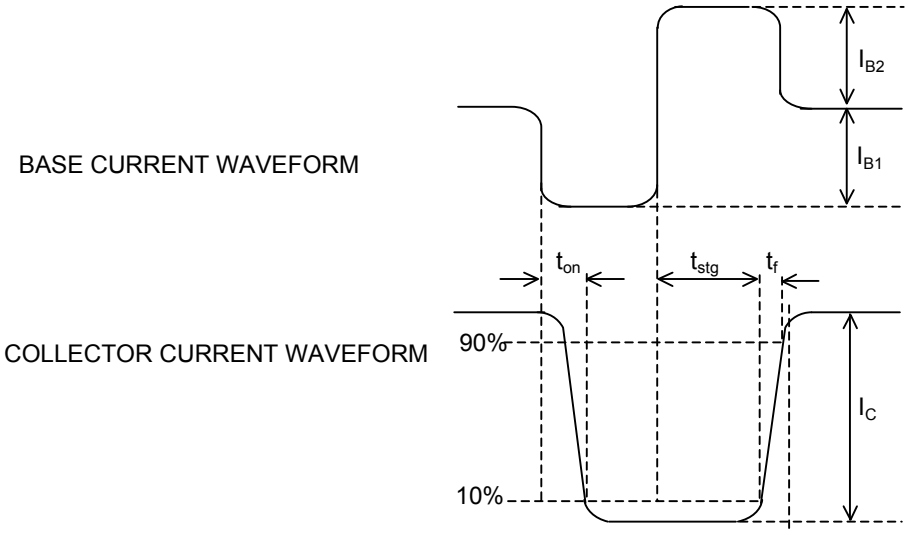

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