TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SH04F,TC7SH04FU

INVERTER

Features

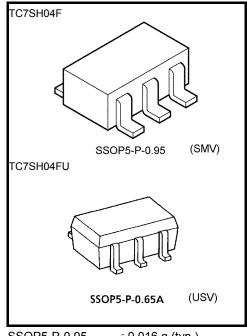
• High Speed : t_{pd} = 3.8ns (typ.)

at $V_{CC} = 5V$, $C_L = 15pF$

Low power dissipation : I_{CC} = 2 μA (max) at Ta = 25°C
 High noise immunity : V_{NIH} = V_{NIH} = 28% V_{CC} (min)

• 5.5-V tolerant input

Wide operating voltage range : V_{CC} = 2 to 5.5V
 Identical pin assignment and function with TC7S04



SSOP5-P-0.95 : 0.016 g (typ.) SSOP5-P-0.65A : 0.006 g (typ.)

Absolute Maximum Ratings (Ta=25°C)

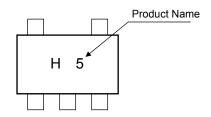
Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	−0.5 to 7	V	
DC input voltage	V _{IN}	–0.5 to 7	V	
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V	
Input diode current	I _{IK}	-20	mA	
Output diode current	lok	±20 (Note1)	mA	
DC output current	lout	±25	mA	
DC V _{CC} /ground current	Icc	±50	mA	
Power dissipation	PD	200	mW	
Storage temperature	T _{stg}	-65 to 150	°C	
Lead Temperature (10s)	TL	260	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

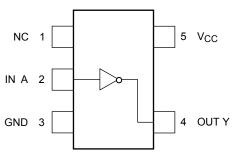
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: V_{OUT} < GND, V_{OUT} > V_{CC}

Marking

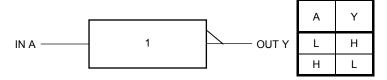


Pin Assignment (top view)



IEC Logic Symbol

Truth Table



Operating Range

Characteristics	Symbol	Rating	Unit
Supply voltage	V_{CC}	2 to 5.5	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 100 (V _{CC} = $3.3 \text{ V} \pm 0.3 \text{ V}$)	ns/V
	avav	0 to 20 (V _{CC} = 5.0 V \pm 0.5 V)	113/ V

Electrical Characteristics

DC Characteristics

Parameter Symbol		Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
				V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
High-level input voltage		_		2.0	1.5	_	_	1.5	_	V
				3.0 to 5.5	V _{CC} × 0.7	_	_	V _{CC} × 0.7	_	
I am land bank			2.0		_	0.5	_	0.5		
Low-level input voltage			-	3.0 to 5.5	l		V _{CC} × 0.3	_	V _{CC} × 0.3	٧
		V _{IN} = V _{IL}	Ι _{ΟΗ} = -50 μΑ	2.0	1.9	2.0	_	1.9	_	V
High-level output voltage				3.0	2.9	3.0	_	2.9	_	
	V _{OH}			4.5	4.4	4.5	_	4.4	_	
			$I_{OH} = -4 \text{ mA}$	3.0	2.58	_	_	2.48	_	
			$I_{OH} = -8 \text{ mA}$	4.5	3.94	_	_	3.80	_	
Low-level output voltage V _{OL}	$V_{IN} = V_{IH}$	I _{OL} = 50 μA	2.0		0	0.1	_	0.1	\ \ \	
			3.0		0	0.1	_	0.1		
			4.5	_	0	0.1	_	0.1		
		$I_{OL} = 4 \text{ mA}$	3.0	_	_	0.36	_	0.44		
			$I_{OL} = 8 \text{ mA}$	4.5	_	_	0.36	_	0.44	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5	_		±0.1	_	±1.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} or GN	5.5	_	_	2.0	_	20.0	μА	

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AC Characteristics (unless otherwise specified, input: $t_r = t_f = 3$ ns)

Parameter	Symbol		Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
		V _{CC} (V)	C _{L (} pF)	Min	Тур.	Max	Min	Max	Offic	
Propagation delay time	t _{PLH}	3.3 ± 0.3	15	_	5.0	7.1	1.0	8.5		
			3.3 ± 0.3	50	_	7.5	10.6	1.0	12.0	- ns
			5.0 ± 0.5	15	_	3.8	5.5	1.0	6.5	
				50	_	5.3	7.5	1.0	8.5	
Input capacitance	C _{IN}				_	4	10	_	10	pF
Power dissipation capacitance	C _{PD}		(Note 2)		_	13	_	_	_	pF

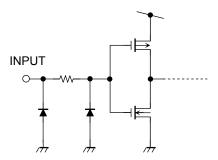
Note 2: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

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Average operating current can be obtained by the equation.

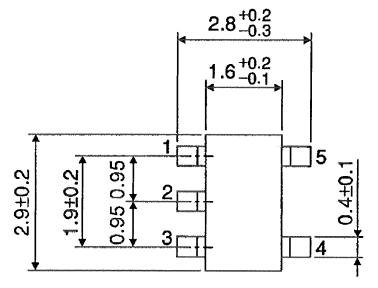
$$I_{CC\;(opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

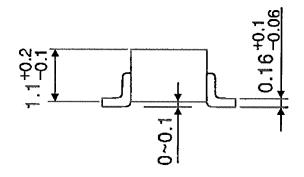
INPUT EQUIVALENT CIRCUIT



Package Dimensions

SSOP5-P-0.95 Unit: mm





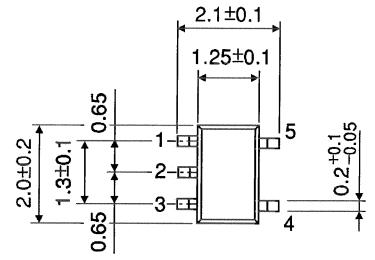
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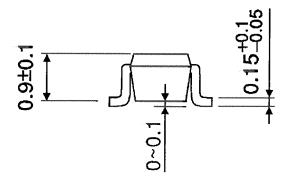
Weight: 0.016 g (typ.)

2009-09-24

Package Dimension

SSOP5-P-0.65A Unit: mm





Weight: 0.006 g (typ.)

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