TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74LCX138F,TC74LCX138FT,TC74LCX138FK

Low-Voltage 3-to-8 Line Decoder with 5-V Tolerant Inputs and Outputs

The TC74LCX138 is a high-performance CMOS 3-to-8 decoder. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low-power dissipation.

The device is designed for low-voltage (3.3 V) VCC applications, but it could be used to interface to 5-V supply environment for inputs.

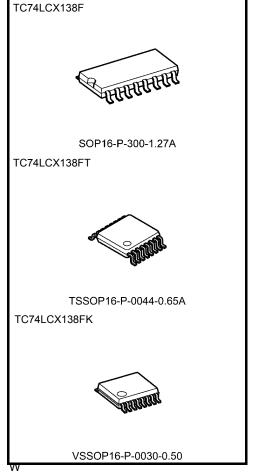
When the device is enabled, 3 binary select inputs (A, B and C) determine which one of the outputs ( $\overline{Y}0 \cdot \overline{Y}7$ ) will go low. When enable input G1 is held low or either  $\overline{G}2A$  or  $\overline{G}2B$  is held high, decoding function is inhibited and all outputs go high.

G1,  $\overline{G}2A$ , and  $\overline{G}2B$  inputs are provided to ease cascade connection and for use as an address decoder for memory systems.

All inputs are equipped with protection circuits against static discharge.

#### **Features**

- Low-voltage operation:  $V_{CC} = 1.65$  to 3.6 V
- High-speed operation:  $t_{pd} = 6.0 \text{ ns (max) (V}_{CC} = 3.0 \text{ to } 3.6 \text{ V)}$
- Ouput current:  $|I_{OH}|/I_{OL} = 24 \text{ mA (min)} (V_{CC} = 3.0 \text{ V})$
- Latch-up performance: > ±500 mA
- Available in JEITA SOP, TSSOP and VSSOP (US)
- Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 138 type

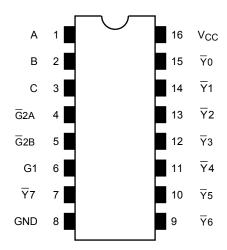


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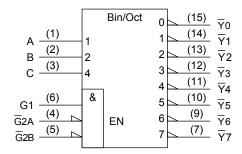
SOP16-P-300-1.27A : 0.18 g (typ.) TSSOP16-P-0044-0.65A : 0.06 g (typ.) VSSOP16-P-0030-0.50 : 0.02 g (typ.)

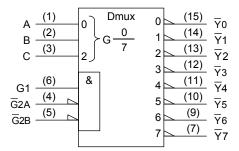
Note: The Electrical Characteristics of  $V_{CC}$ =1.8±0.15V is only applicable for products which manufactured from January 2009 onward.

# Pin Assignment (top view)



# **IEC Logic Symbol**





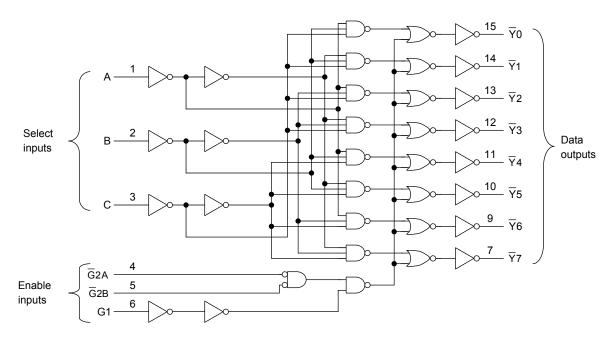
### **Truth Table**

	Inputs					Outputs								
	Enable			Select			<u></u>	_ Y2		_ Y4				Selected Output
G1	G2A	G <sub>2</sub> B	С	В	Α	₹0	Ť I	12	13	14	15	10	Ÿ7	
L	Х	Х	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н	None
Х	Н	Х	X	Х	X	Н	Η	Н	Н	Н	Н	Η	Η	None
Х	Х	Н	Х	Х	Х	Н	Н	Н	Н	Н	Н	Н	Н	None
Н	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	₹0
Н	L	L	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	Ÿ1
Н	L	L	L	Н	L	Н	Н	L	Н	Н	Н	Н	Н	Ÿ2
Н	L	L	L	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	<del>Y</del> 3
Н	L	L	Н	L	L	Н	Н	Н	Н	L	Н	Н	Н	Ÿ4
Н	L	L	Н	L	Н	Н	Н	Н	Н	Н	L	Н	Н	Ÿ5
Н	L	L	Н	Н	L	Н	Н	Н	Н	Н	Н	L	Н	<del>Y</del> 6
Н	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	Ÿ7

X: Don't care



### **System Diagram**



### **Absolute Maximum Ratings (Note 1)**

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V <sub>CC</sub>	−0.5 to 7.0	V	
DC input voltage	V <sub>IN</sub>	-0.5 to 7.0	٧	
		-0.5 to 7.0 (Note 2)	V	
DC output voltage	V <sub>OUT</sub>	-0.5 to V <sub>CC</sub> + 0.5 (Note 3)		
Input diode current	I <sub>IK</sub>	-50	mA	
Output diode current	lok	±50 (Note 4)	mA	
DC output current	lout	±50	mA	
Power dissipation	PD	180	mW	
DC V <sub>CC</sub> /ground current	I <sub>CC</sub> /I <sub>GND</sub>	±100	mA	
Storage temperature	T <sub>stg</sub>	-65 to 150	°C	

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating range (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2:  $V_{CC} = 0 V$ 

Note 3: High or low state. IOUT absolute maximum rating must be observed.

Note 4:  $V_{OUT} < GND, V_{OUT} > V_{CC}$ 



# **Operating Ranges (Note 1)**

Characteristics	Symbol	Rating	Unit	
Power supply voltage	\/	1.65 to 3.6	V	
Power supply voltage	Vcc	1.5 to 3.6 (Note 2)		
Input voltage	V <sub>IN</sub>	0 to 5.5	V	
Output voltage	\/a=	0 to 5.5 (Note 3)	V	
Output voltage	Vout	0 to V <sub>CC</sub> (Note 4)	V	
Output ourrent	la/la.	±24 (Note 5)	mA	
Output current	I <sub>OH</sub> /I <sub>OL</sub>	±12 (Note 6)	ША	
Operating temperature	T <sub>opr</sub>	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 10 (Note 7)	ns/V	

Note 1: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{CC}$  or GND.

Note 2: Data retention only

Note 3:  $V_{CC} = 0 V$ 

Note 4: High or low state

Note 5:  $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$ 

Note 6:  $V_{CC} = 2.7 \text{ to } 3.0 \text{ V}$ 

Note 7:  $V_{IN} = 0.8$  to 2.0 V,  $V_{CC} = 3.0$  V



### **Electrical Characteristics**

### DC Characteristics (Ta = -40 to 85°C)

Characteristics		Symbol	Test Co	ndition		Min	Max	Unit
		.,		V <sub>CC</sub> (V)				
				1.65 to 2.3	$V_{CC} \times 0.9$	_	V	
land to the sec	H-level	VIH	_	2.3 to 2.7	1.7	_		
				2.7 to 3.6	2.0	_		
Input voltage					1.65 to 2.3		V <sub>CC</sub> × 0.1	v
	L-level	VIL	_	-	2.3 to 2.7	_	0.7	
				2.7 to 3.6	_	0.8		
				I <sub>OH</sub> = -100 μA	1.65 to 3.6	V <sub>CC</sub> -0.2	_	
			V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	$I_{OH} = -4 \text{ mA}$	1.65	1.05	_	V
	H-level	V <sub>OH</sub>		$I_{OH} = -8 \text{ mA}$	2.3	1.7	_	
	H-level			I <sub>OH</sub> = -12 mA	2.7	2.2	_	
				I <sub>OH</sub> = -18 mA	3.0	2.4	_	
Outrot valta aa				$I_{OH} = -24 \text{ mA}$	3.0	2.2	_	
Output voltage				$I_{OL} = 100 \mu A$	1.65 to 3.6	_	0.2	
				$I_{OL} = 4 \text{ mA}$	1.65	_	0.45	
				$I_{OL} = 8 \text{ mA}$	2.3	_	0.7	
	L-level	V <sub>OL</sub>	$V_{IN} = V_{IH}$ or $V_{IL}$	I <sub>OL</sub> = 12 mA	2.7	_	0.4	
				I <sub>OL</sub> = 16 mA	3.0	_	0.4	
				I <sub>OL</sub> = 24 mA	3.0	_	0.55	
Input leakage current		I <sub>IN</sub>	V <sub>IN</sub> = 0 to 5.5 V		1.65 to 3.6	_	±5.0	μА
Power-off leakage current		loff	V <sub>IN</sub> /V <sub>OUT</sub> = 5.5 V		0		10.0	μА
Quincoant aunaly aurrent		loo	V <sub>IN</sub> = V <sub>CC</sub> or GND	1.65 to 3.6		10.0		
Quiescent supply current		Icc	V <sub>IN</sub> = 3.6 to 5.5 V	1.65 to 3.6	_	±10.0	μΑ	
Increase in Icc per inp	ut	Δl <sub>CC</sub>	$V_{IH} = V_{CC} - 0.6 V$		2.7 to 3.6	_	500	



#### AC Characteristics (Ta = -40 to 85°C)

Characteristics	Symbol Test Condition			Min	Max	Unit
Characteristics	Symbol	rest Condition	V <sub>CC</sub> (V)	IVIIII	WICK	Offic
			1.8±0.15	_	25.0	
Propagation delay time	t <sub>pLH</sub>	Figure 4 Figure 0	2.5±0.2		8.0	
(A, B, C- $\overline{Y}$ )	t <sub>pHL</sub>	Figure 1, Figure 2	2.7	_	7.0	ns
			$3.3\pm0.3$	1.5	6.0	
			1.8±0.15		25.0	ns
Propagation delay time	t <sub>pLH</sub>	Figure 1, Figure 2	2.5±0.2		9.0	
(G1- \overline{Y})			2.7		8.0	
			$3.3 \pm 0.3$	1.5	7.0	
			1.8±0.15	_	25.0	
Propagation delay time	t <sub>pLH</sub>	Figure 1, Figure 2	2.5±0.2		8.0	ns
( <del>G</del> 2 - <del>Y</del> )	t <sub>pHL</sub>		2.7	_	7.0	
			$3.3 \pm 0.3$	1.5	6.0	
Output to output skew	t <sub>osLH</sub>	(Note)	2.7	_	_   .	ne
Output to output shew	t <sub>osHL</sub>	(INOIE)	$3.3\pm0.3$	_	1.0	ns

Note: Parameter guaranteed by design.

 $(t_{OSLH} = |t_{DLHm} - t_{DLHn}|, t_{OSHL} = |t_{DHLm} - t_{DHLn}|)$ 

### Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.5$ ns, $C_L = 50$ pF, $R_L = 500$ $\Omega$ )

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
Quiet output maximum dynamic $V_{OL}$	V <sub>OLP</sub>	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	8.0	V
Quiet output minimum dynamic V <sub>OL</sub>	V <sub>OLV</sub>	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	8.0	V

### **Capacitive Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
Input capacitance	C <sub>IN</sub>	_	3.3	7	pF
Output capacitance	C <sub>OUT</sub>	_	0	8	pF
Power dissipation capacitance	C <sub>PD</sub>	f <sub>IN</sub> = 10 MHz (Not	9) 3.3	25	pF

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC\;(opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$ 



### **AC Test Circuit**

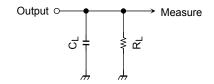


Figure 1

### **AC Waveform**

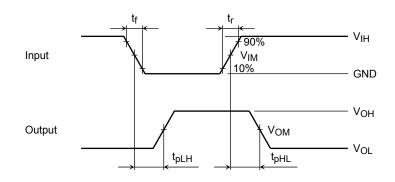


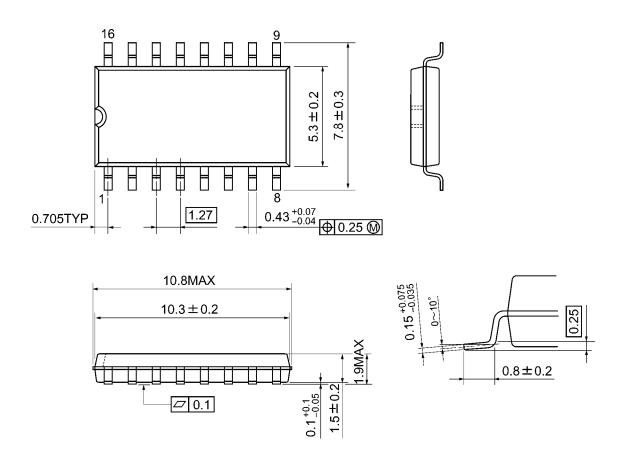
Figure 2 t<sub>pLH</sub>, t<sub>pHL</sub>

		Vcc						
	Symbol	$3.3 \pm 0.3 \text{ V}$ $2.7 \text{V}$	$2.5\pm0.2~\textrm{V}$	1.8 ± 0.15 V				
Input	V <sub>IH</sub>	2.7V	V <sub>CC</sub>	V <sub>CC</sub>				
	V <sub>IM</sub>	1.5V	V <sub>CC</sub> /2	V <sub>CC</sub> /2				
	tr,tf	2.5ns	2.0ns	2.0ns				
Output	V <sub>OM</sub>	1.5V	V <sub>OH</sub> /2	V <sub>OH</sub> /2				
Load	CL	50pF	30pF	30pF				
	$R_L$	500Ω	500Ω	1kΩ				



# **Package Dimensions**

SOP16-P-300-1.27A Unit: mm



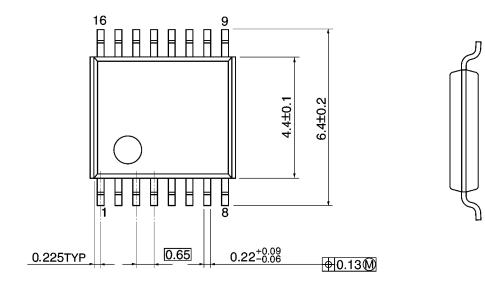
Weight: 0.18 g (typ.)

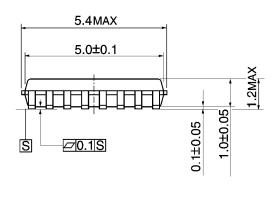


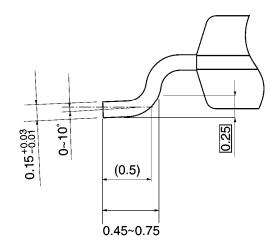
# **Package Dimensions**

TSSOP16-P-0044-0.65A

Unit: mm



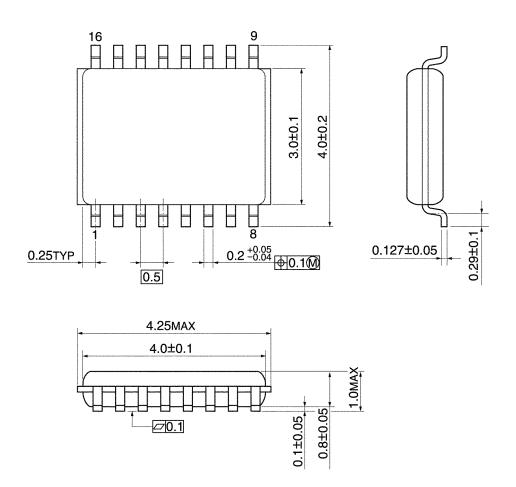




Weight: 0.06 g (typ.)

# **Package Dimensions**

VSSOP16-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)

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