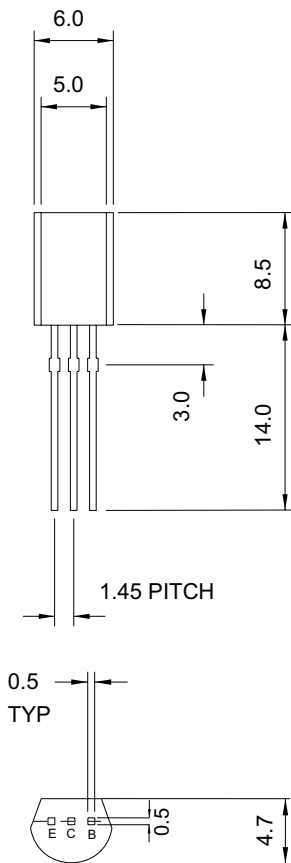


MECHANICAL DATA

Dimensions in mm



TO92(EXTENDED)

NPN EPITAXIAL PLANAR SILICON TRANSISTOR

Ideal for high current driver applications requiring efficient low loss devices

FEATURES

- LOW $V_{CE(SAT)}$
- HIGH CURRENT
- HIGH ENERGY RATING

APPLICATIONS

- Any High Current Driver Applications Requiring Efficient Low Loss Devices.

ABSOLUTE MAXIMUM RATINGS ($T_{case} = 25^{\circ}C$ unless otherwise stated)

V_{CBO}	Collector – Base voltage	60V
V_{CEO}	Collector – Emitter voltage	20V
V_{EBO}	Emitter – Base voltage	6V
I_C	Collector current	5A
$I_{C(PK)}$	Peak Collector current	8A
P_{tot}	Total Dissipation at $T_{case} = 25^{\circ}C$	0.9W
T_{stg}	Storage Temperature	-55 to 150°C
T_j	Maximum Operating Junction Temperature	150°C

ELECTRICAL CHARACTERISTICS ($T_{\text{case}} = 25^{\circ}\text{C}$ unless otherwise stated)

Parameter		Test Conditions		Min.	Typ.	Max.	Unit.
I_{CBO}	Collector Cut-Off Current	$V_{\text{CB}} = 50\text{V}$	$I_{\text{E}} = 0$			1	μA
I_{EBO}	Emitter Cut-Off Current	$V_{\text{EB}} = 5\text{V}$	$I_{\text{C}} = 0$			1	μA
$V_{\text{CE(sat)}}^*$	Collector – Emitter Saturation Voltage	$I_{\text{C}} = 3\text{A}$	$I_{\text{B}} = 60\text{mA}$			0.5	V
$V_{\text{BE(sat)}}^*$	Base – Emitter Saturation Voltage	$I_{\text{C}} = 3\text{A}$	$I_{\text{B}} = 60\text{mA}$	0.6		1.5	V
h_{FE}^*	DC Current Gain	$V_{\text{CE}} = 2\text{V}$ $V_{\text{CE}} = 2\text{V}$	$I_{\text{C}} = 0.5\text{A}$ $I_{\text{C}} = 3\text{A}$	100 75		560	—
f_{T}	Transition frequency	$V_{\text{CE}} = 10\text{V}$	$I_{\text{C}} = 50\text{mA}$		120		MHz
C_{ob}	Output Capacitance	$V_{\text{CB}} = 10\text{V}$	$f = 1\text{MHz}$		45		pF

* Pulse test $t_{\text{p}} = 300\mu\text{s}$, $\delta \leq 2\%$

